	Subject Code: 24EC11RC06	K-24	Reg No:	:						
100	🤼 GAYATRI VIDYA PARISHAD COL	LEGE OF	ENGINEER	ING	FO	R W	ΌΜ	EN		
E 300	STATE AND ASSAULT	ONOMOU		11.10	10.		01/1			
Section .	(Affiliated to Andhra		,	am)						
As	I B.Tech II Semester Regul	ar Examin	ations, June	/ Jul	y-2	025				
	ELECTRONIC	CIRCUI	Γ ANALYS	<u>IS</u>						
		(ECE)								
	1. All questions carry ed	qual marks								
	2. Must answer all parts	of the ques	stion at one pl	ace						
T	Sime: 3Hrs.					M	lax N	Mark	s: 7	' 0
••••									•••••	•••••
		UNIT-I						_	_	
1.	a. For CE transistor, draw the hybrid- π mod			-	aran	neter	's inv	olved	1.	7M
	b. Derive the β -cut-off frequency for CE an	aplifier at h	igh frequencie	es.						7M
		OR								
2	a. State the Miller's theorem and derive the		oitanaa if a aa	maai	tor i	G 001	moot	ad		
۷.	between input and output.	mmer capa	icitalice II a ca	арасі	tor i	5 CO1	mecu	cu	7	'M
	b. Derive the voltage gain of CS FET ample	ifier at high	frequencies.							M
		J NIT-II	irequencies.						,	111
	<u>-</u>	<u> </u>								
3.	a. For a two stage RC coupled CE amplifies	r derive ove	rall current ga	ain a	nd in	put:	resist	tance	. 7	M
	b. What is the effect of bypass and coupling capacitors on low frequency response of CE									
	amplifier?		-	•	•				7	M
		OR								
4.	a. Derive the overall bandwidth and gain fo	_		fier.						M
	b. Derive the current and volage gains of ba	asic Darling	ton circuit.						7	M
	U	NIT-III								
	<u>-</u>	111111								
5.	a. list out the characteristics of negative fee	dback amp	ifiers and der	ive tl	ne se	nsiti	vity.		7	M
	b. Draw the block diagram for current seri	es feedbacl	c amplifier an	d de	rive	the o	expre	ssion	ı fo	r gain
	input resistance and output resistance.								7	M
_		OR							_	
6.										with 7M
	b. Derive the expression for gain, input re	esistance an	d output resis	tance	e for	volt	age	shunt	fee	edback
	amplifier.		_						7	'M
	TI	NIT-IV								
	<u>U</u>	1 111-1 V								
7.	a. Draw the circuit diagram of FET based R	RC phase sh	ift oscillator a	nd d	erive	e the	freat	uency	v of	•
	oscillations for this.									M
	b. What is meant by frequency stability and	l discuss ab	out this for cr	ystal	osci	llato	rs.			M
		0	R	,						
8.	1 2	Colpitts osci	illator by deriv	ving	the g	genei	al eq	luatio		
	oscillators.									M
	b. Compare Hartley, Colpitts and Clapp osc	cillators.							7	M

UNIT-V

9. a. Draw and explain the working of class-B complementary symmetry push-pull power amplifier. 7Mb. Derive the efficiency of class-B power amplifier. 7M

OR

10. a. Explain the frequency response of stagger tuned amplifier using single tuned circuits.
Derive the equation for bandwidth for single tuned amplifier.
7M.
7M