Gayatri Vidya Parishad College of Engineering for Women

(Approved by AICTE, New Delhi, Affiliated to JNTUK Kakinada)
Kommadi, Madhurawada Visakhapatnam 530048
Phone: 91-891-2739144/2719124/2719125/2719127
Email: gvpcew@gmail.com
info@gvpcew.ac.in

STUDENT HANDBOOK
for
B.Tech-I Year (2021-2022)

Vision:

To emerge as an acclaimed centre for learning that provides value based technical education for the holistic development of students.

Mission:

•Undertake activities that provide value based knowledge in science, engineering & Technology.

•Provide opportunities for learning through industry-institute interaction on the state-of-the-art technologies.

•Create collaborative environment for research, innovation and entrepreneurship to flourish.

• Promote activities that bring in a sense of social responsibility.

Personal Details

Name:

Roll Number:

Branch:

Section:

Permanent Address:

Present Address:

Aadhar Number:

Blood Group:

Contact name Cell number of
Parent/Guardian &Address:
Phone:
Email ID:

Important Message

This handbook is a document for providing information to the students. For correctness of information, please verify the institute rules and regulations.

It is to be noted that at the time of admission every student shall be required to sign a declaration that on admission, she would abide by the disciplinary rules and regulations of the college. She is expected to comply with these rules and regulations throughout her stay in the college.

Parents/guardians are requested to inform/direct their ward to observe the rules and regulations and maintain discipline.

Management & Principal

Contents

From the Principal's desk

I, on behalf of the management and faculty of GVPCEW welcome the students to our college for the current academic year 2021-22

We wish you the very best in all that you seek to do and we will do our utmost to help you realise your goals and dreams.

Each student is issued a student's handbook at the beginning of the academic year and this will provide the students the opportunity to manage their pace of learning and enable them to monitor their academic performance and progress as well as their involvement in co-curricular activities.

This handbook aims to keep the students and parents informed of the policies and procedures pertaining to the college.

Wishing you a great academic career.

Principal

About Gayatri Vidya Parishad Society

Gayatri Vidya Parishad Society was established in 1988 with a motive of inculcating quality education in the diverse fields of Arts, Science, Engineering, Medicine and School education imbuing cultural and ethical values of this country amongst the students. The GVP Society under its fold presently runs three Engineering Colleges, One Medical College, One MBA/MCA College with Degree courses, One school exclusively for underprivileged children. Presently Prof. Dr. Ing. P Srinivasa Rao, FNAE, former Professor & Dean, Indian Institute of Technology, Madras and former Vice-President of the Parishad is the President, Prof. P.Somaraju, the founder-Secretary, is presently the Secretary.

The Society endeavours to provide a holistic education blending scientific temper with human values by providing quality education and facilitate value based career in the fields of Science, Engineering and Technology.

The Society was established by committed academicians, industrialists, philanthropists and educationists. The founder president of the society is Dr.Bhavaraju Sarveswara Rao, a renowned economist from Andhra University, who later served as Vice-Chancellor of Acharya Nagarjuna University. The subsequent presidents have been Dr. B. Swami, a medical practitioner and an ex-member of Medical Council of India (MCI), Vice-Chancellor of Acharya Nagarjuna University. Sri D.V.Subba Rao, a legal luminary, former Mayor of Visakhapatnam Municipal Corporation and former President of Andhra Cricket Association was President of Gayatri Vidya Parishad. Sri. A.S.N Prasad, a Civil Engineer and Philanthropist, Technocrat and Senior citizen of Visakhapatnam served as President till 30th September 2019.

About Gayatri Vidya Parishad College of Engineering for Women

GVPCEW was started in 2008 by Gayatri Vidya Parishad Society to provide educational opportunities in engineering exclusively for women to bring out their latent talents for the empowerment of women and society at large. The college offers four Undergraduate programs in ECE, EEE, CSE, CSE (AI&ML) and IT with an intake of 420 students and two Post Graduate program in VLSI Design and Embedded Systems and CSE- Data Science with an intake of 18 students each.

The college is primarily a student-centred institution that strives to impart professional and technical skills necessary to meet societal demands. The college emphasizes on the total development of the student for responsible citizenship in a global society. All the facilities are provided for your training both in the theory and practice in the respective disciplines into which you have been given admission. You will find a good and well equipped Library, a Digital Library, well equipped laboratories and committed Teaching Staff. It is left to your discretion how best you can utilize the existing infrastructure and facilities to get yourself educated and updated in your respective fields.

Your first priority in the Campus is 'Studies, Behavior and Discipline self-inculcated.

We, once again, heartily welcome you to Gayatri Vidya Parishad College of Engineering for Women. We congratulate you all for securing a seat in the branch of your choice through EAMCET braving a severe and tough competition! We thank you for choosing this college as a platform for pursuing your professional career. We wish you a happy and useful stay in our campus pursuing higher frontiers of learning.

General Guidelines:

- **Discipline** cannot be imposed by some body at this level, it needs to be self-imposed. Besides your learning, your behaviour and conduct are also to be given priority as it gives a good projection of yourself and the college in the eyes of society. The name and reputation of the college entirely lie on your exemplary behaviour and academic excellence.

- It is only for moulding your character and personality that rules are laid. Therefore it is mandatory that you should train yourself to follow and obey these instructions.

- Any violation of the rules and regulations laid by the college leads to serious repercussions on the very development of your character and thereby on the status, prestige and dignity of the college. Therefore such a violation will be seriously viewed.

- **Strict silence** must be maintained within the campus during the academic session when the classes are going on and also whenever you assemble for any meeting.

- All the students should attend the college in a presentable form and hence should adhere strictly to the '**Dress Code**' prescribed by the college without fail.

- All the students should wear the '**identification badges**' within the campus. They cannot approach any of their teachers or the administration without the **identification badge.**

- Those who avail themselves of college bus facility should carry bus passes issued to them together with their **Identification badges** and must produce them whenever asked by the authority concerned.

- **Cell Phones** are not allowed within the campus as per the directive of the Government. The use of **Cell Phones** will be viewed seriously and it amounts to violation of **College discipline** as well as the Government directive. Disciplinary action will be taken in such a case with intimation to the parents of the involved students. The extent of punishment may go up to suspending the student from the college or classes as the case may be. **Any cell phone** brought

into the campus unintentionally should be deposited as the entrance near the security **at your own risk.**

- Students residing in the hostel are permitted to use the **cell phones** within their hostel premises with some reasonable discrimination. If addicted to its indiscriminate use, your academics will be hampered. At this point of your career, priority should be given to your academic performance alone.

- Irrespective of any Branch of Engineering to which a student belongs, she is expected to respect all the teachers and other staff of the college equally. Talk politely or obligingly to every one of your classmates which will help you in finding **a leader** in yourself

- The education you are receiving should also imbue in you the quality of humility. "**Vidya dadaati vinayam**" Acquire this **quality** and you will be respected anywhere in the society.

- Your behaviour within and outside the campus must be exemplary and this should stand as a testimonial not only to yourself but to the college in which you are studying and your parents too should feel proud of you. Please know that the '**End of Education is Character besides acquisition of knowledge'.** Both character and knowledge will yield rich dividends both in your professional as well as in your social career.

- Remember your success lies in the practice of **FIVE Ds- (5Ds) Discipline, Discrimination, Dedication, Determination and Devotion,** all being equally important!

- It is with a great hope that we look forward for the best in you both as students and also citizens upholding the aspirations of the college and also the society which ultimately is the main beneficiary of your knowledge, behaviour and performance! Always remember you owe a lot to the society from which you have derived or extracted a lot!

- All the **parents and guardians** of the students of this college are therefore earnestly requested to kindly co-operate with administration in giving quality education to your children. Let us strive together to turn out good citizens with a sense of responsibility and accountabi

Rules of Conduct:

The students are expected to be regular with minimum 75% attendance in their Class work and should conduct themselves in a disciplined manner. They should abide by such rules of discipline and conduct as stipulated by the institution from time to time. Attendance less than 65% will not be condoned and 65-70% may be condoned by JNTUK only on medical grounds subject to the condition that the absence is in a single spell and on submission of a medical certificate immediately after sickness. The conduct of the student should be exemplary, not only within the premises of the College but also outside. This will help in maintaining the prestige and status of the institute. The College has full powers to suspend, fine, dismiss or to take any action whichever is felt necessary in case of any indiscipline on the part of the students.

1) In case of any indiscipline, irregularity, default in payment of fee and poor performance or failure in Examinations or any other matter of concern the parents/guardians will be informed for any subsequent corrective action.

2) Students of G.V.P. College of Engineering for Women are not permitted to resort to any strikes and /or demonstrations during the period of their stay in the college. Participation in any such activity shall automatically result in her dismissal from the college. Grievances can be represented to the concerned Heads of Departments and the Principal through their nominated class representatives.

3) If the candidate discontinues her studies she has to pay the tuition fee & special fee for the balance period to get her original certificates.

4) The parents / guardians should inform any change of their address to Academic and examinations sections of the college. All students should open an SB A/C in SBI

5) The office bearers of the students' association /sports or cultural teams are nominated on the basis of merit in the university examinations. No elections are permitted in the College.

6) Hostel facility is available for girls on first come first served basis.

7) The college premises should be kept clean. Writing, sticking -up of posters and notices on the building walls are strictly prohibited. The Institute's property must be guarded as their own property.

8) Ragging is an offence and is strictly prohibited according to A.P, Govt.Act1997. Any complaint of ragging entitles severe disciplinary action after enquiry if students of GVP College of Engineering for Women are found to be involved in Ragging inside or outside the college campus.

9) Students are Strictly Prohibited from using <u>cell-phones</u> in the college as per the Government letter (LR.No.H2/27150/06 Dated 4/12/2006)

10) All the students of the college are hereby instructed to strictly adhere to the college rules and Regulations.

<u>DRESS CODE</u> : **Chudidar or salwar kamez with kurtas and dupatta (chunni), or full -skirt, Blouse and half saree. No skin - tight leggings or chudidars are allowed. Please note that : Informal jeans, Boot cut jeans, cargo jeans and cargo trousers and T-Shirts with / without collars, mini-skirts are not allowed and strictly prohibited. Any violation of the above college dress code will be considered serious by the college administration and one may be debarred from classes/ exams for the days.**

11) The students should abide by the Rules and regulations of the college brought in force from time to time.

ACADEMIC CALENDAR I-I PLANNED

GAYATRI VIDYA PARISHAD COLLEGE OF ENGINEERING FOR WOMEN									
Madhurawada::Visakhapatnam									
Calendar of Academic Activities - Planned									
I B.Tech. I Semester (2021-2022)									
Date: 22-09-2021									
Month	Working Days							Total Working Days	Activities
	Su	M	Tu	W	Th	F	Sa		
NOV		1	2	3	4	5	6		
	7	8	9	10	11	12	13		
	14	15	16	17	18	19	20		
	21	22	23	24	25	26	27		Student Induction Program
	28	29	30					2	**29-11-2021 Commencement of Class Work**
DEC	Su	M	Tu	W	Th	F	Sa		
				1	2	3	4	4	11-12-2021 Second Saturday
	5	6	7	8	9	10	11	5	13-12-2021 Guest Lecture 1
	12	13	14	15	16	17	18	6	18-12-2021 Class Committee meeting 1
	19	20	21	22	23	24	25	5	22-12-2021 National Mathematics Day
	26	27	28	29	30	31		5	25-12-2022 Christmas Day
JAN	Su	M	Tu	W	Th	F	Sa		
							1	1	
	2	3	4	5	6	7	8	5	08-01-2022 Second Saturday
	9	10	11	12	13	14	15	4	14-01-2022 to 16-01-2022 Pongal Holidays
	16	17	18	19	20	21	22	6	MID I EXAMS 17-01-2022 TO 22-01-2022
	23	24	25	26	27	28	29	5	26-01-2022 Republic Day
	30	31						1	
FEB	Su	M	Tu	W	Th	F	Sa		
			1	2	3	4	5	5	05-02-2022 Class Committee meeting 2
	6	7	8	9	10	11	12	5	07-02-2022 Chem Quiz (Periodic Table Day)
	13	14	15	16	17	18	19	6	12-02-2022 Second Saturday
	20	21	22	23	24	25	26	6	18-02-2022 Guest Lecture 2
	27	28						1	28-02-2022 National Science Day Celebrations
MAR	Su	M	Tu	W	Th	F	Sa		
			1	2	3	4	5	4	01-03-2022 Maha Shiva Ratri
	6	7	8	9	10	11	12	5	08-03-2022 International Women's Day

	13	14	15	16	17	18	19	5	11-03-2022 Class Committee meeting 3
	20	21	22	23	24	25	26	6	12-03-2022 Second Saturday
	27	28	29	30	31			4	18-03-2022 Holi
									14-03-2021 to 19-03-2021 Mid II Exams
									Preparation and Practicals
APR	Su	M	Tu	W	Th	F	Sa		
						1	2	2	JNTUK External Exams
	3	4	5	6	7	8	9	6	
	10	11	12	13	14	15	16		
	17	18	19	20	21	22	23		
	24	25	26	27	28	29	30		
	Total Working Days including JNTUK Internal and External Examinations							104	

	Commencement of classwork		Student Induction Program
	JNTUK MID-I Exams		Class Committee meeting
	JNTUK MID-II Exams		Special Events
	Preparation and Practicals		Guest Lectures
	JNTUK External Exams		Holiday

Website: www.jntuk.edu.in
Email: dapjntuk@gmail.com

Phone: 0884-2300991
Mobile: 7032606555

Directorate of Academic Planning
JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY KAKINADA
KAKINADA-533003, Andhra Pradesh, INDIA
(Established by AP Government Act No. 30 of 2008)

Lr. No. JNTUK/DAP/AC/I Year/B. Tech/2021-22 *Date: 19-11-2021*

Dr. R. Srinivasa Rao,
Director, Academic Planning
JNTUK, Kakinada

To
All the Principals of Affiliated Colleges,
JNTUK, Kakinada.

Academic Calendar of I Year B. Tech for the Academic Year 2021-22

I SEMESTER			
Description	**From**	**To**	**Weeks**
Commencement of Class Work	**22.11.2021**		
Induction Classes	22.11.2021	27.11.2021	1W
I Unit of Instruction	29.11.2021	15.01.2022	7W
I Mid Examinations	17.01.2022	22.01.2022	1W
II Unit of Instructions	24.01.2022	12.03.2022	7W
II Mid Examinations	14.03.2022	19.03.2022	1W
Preparation & Practicals	21.03.2022	26.03.2022	1W
End Examinations	28.03.2022	09.04.2022	2W
Commencement of II Semester Class Work	**11.04.2022**		
II SEMESTER			
I Unit of Instructions	**11.04.2022**	28.05.2022	7W
I Mid Examinations	30.05.2022	04.06.2022	1W
II Unit of Instructions	06.06.2022	23.07.2022	7W
II Mid Examinations	25.07.2022	30.07.2022	1W
Preparation & Practicals	01.08.2022	06.08.2022	1W
End Examinations	08.08.2022	20.08.2022	2W
Commencement of next Year Class Work	**22.08.2022**		
Note: Calendar is prepared with 8 hrs/day hence 7 weeks per instruction period			

R. Srinivasa Rao
Director Academic Planning

Director
Academic Planning
JNTUK Kakinada

Copy to the Secretary to the Hon'ble Vice Chancellor, JNTUK
Copy to Rector, JNTUK
Copy to Registrar, JNTUK
Copy to Director Academic Audit, JNTUK
Copy to Director of Evaluation, JNTUK

Academic Regulations:

According to the regulations given by JNTUK , a student will be declared eligible for the award of B.Tech degree if he fulfils the following academic regulations:

1. A student shall be declared eligible for the award of the B.Tech degree, if he pursues a course of study in not less than four and not more than eight academic years. After eight academic years from the year of their admission, he/she shall forfeit their seat in B.Tech course and admission stands cancelled.
2. The candidate shall register for **160 credits and secure all the 160 credits.**
3. For more information, please refer to the Academic Guidelines for B.Tech Four year Degree Course(applicable for the batches admitted from 2020-21) issued by JNTUK, Kakinada. **The Academic Regulations booklet is issued to every student separately along with this handbook.**

Anti Ragging Committee:

GVPCEW has a zero tolerance policy towards ragging in any form on its campus. As per the AICTE guidelines on prevention and prohibition of raging in technical institutions, a committee has been set up to prevent any ragging in the campus, hostels, or buses. The following members can be contacted in case of any issue.

ANTI RAGGING COMMITTEE

S.No.	Name	Remarks	Phone No.
1	Principal	Chairman	88850-43361
2	Vice-Principal	Co-Chairman	88850-43344
3	Dr.P Devendra, Assoc Professor, EEE	Convener	98499-62648
4	Ms.B.Bharathi, Asst Professor, Mathematics	Co-Convener	86396-66486
5	HOD, ECE	Member	94943-79031
6	HOD, CSE	Member	98484-83016
6	HOD, IT	Member	88850-43345
7	HOD, EEE	Member	70934-13324
8	HOD, B S & H	Member	88850-43349
9	Dr.A.Suseelatha, Asst Prof, Mathematics	I Year Coordinator	98660-00118
10	Mr.V.Lakshmana Rao, Asst Prof, CSE & IT	Member	90100-61779
11	Ms.P.Sridevi, Asst Prof, CSE & IT	Member	98491-19025
12	Ms.B.Divya Sathi, Asst Prof, ECE	Member	94914-25382
14	Mr.S.Ashok, Asst Prof, Chemistry	Member	99892-05015
15	Mr.V S V V D Prakash, Asst Prof, Mathematics	Member	72077-04846

Awareness programs on what constitutes ragging are held periodically to sensitize students to this scourge. The following punishments would be meted out to any candidate found involved in any form of ragging

Nature of Ragging		Punishment
1	Teasing, Embarrassing and humiliating	Imprisonment upto 6 months or fine upto Rs.1,000/- or Both
2	Assaulting or using criminal force or criminal intimidation	Imprisonment upto 1 year or fine upto Rs.2,000/- or Both
3	Wrongfully restraining or confining or causing hurt	Imprisonment upto 2 years or fine upto Rs.5,000/- or Both
4	Causing grievous hurt, Kidnapping or rape or committing unnatural offence	Imprisonment upto 5 years and fine upto Rs.10,000/-
5	Causing death or abetting suicide	Imprisonment upto 10 years and fine upto Rs.50,000/-

- A student convicted of any or the above offences will be expelled from the College.
- A student imprisoned for more than six months for any of the above offences will not be admitted in any other college.
- A student against whom there is a prima facie evidence of ragging in any form will be suspended from the college immediately.

Insurance:

All the students and staff of the college are covered by Group Personal Accident Insurance Policy of HDFC ERGO, general Insurance Company Ltd. to help them realize their career and life goals.

Library:

The library provides an appropriate place for study and research for the students and staff. The library has in its holdings over 20000 books. B.Tech Students are given 3 books and M.Tech Students are given 4 books for a duration of 15 days. Books are issued to users from 10.00 AM to 5.00 PM. The students can access online resources through the digital library. A number of books to prepare for competitive exams are also available.

Reprography and printing services are available in the Library for students and faculty.

Free bus service is provided for the library users every day at 6.15 AM to the college and to the city after 7 PM.

Grievance Redressal Cell:

The Grievance Redressal Committee (GRC) has been constituted in the college headed by a Senior faculty member and supported by a few other Senior faculty members drawn from various departments and Non-teaching staff members to look into any complaints/ grievances of students or faculty.

Functions of GRC are:

- Redressal of Student's Grievances by conducting a thorough enquiry on the complaints received from the aggrieved Students / Staff.
- Co-ordinate between students and department to redress their grievances.
- Ensure effective solution to the stakeholders in an impartial and fair approach.

Internal Complaints Committee

It has been set up in the college to protect women students and faculty from any kind of workplace harassment in pursuance of the Sexual Harassment Of Women at Workplace(Prevention, Prohibition, And Redressal) Act 2013. The Committee headed by a senior faculty member investigates any complaint of that nature if it is brought to their notice. The committee then would set into motion the due process for a confidential and quick resolution of the issue.

Capacity Building for SC/ST committee was constituted for the capacity building of students belonging to SC/ST as per the Scheduled Castes and the Scheduled Tribes (Prevention of Atrocities Act, 1989, No 33) to promote equal opportunities for the marginalized groups. The cell at GVPCEW provides supplementary academic support to the candidates in the form of remedial coaching, training for national level eligibility tests. It also disseminates information to the students regarding any financial support schemes offered by State and Central Governments.

Mentoring System:

To help the students at the individual level, the students are mentored by faculty members to provide a sense of security, congenial environment, and guidance for academic and other needs. In the first year, each class is grouped into batches of 20 students. Each batch is assigned to a faculty member. The mentors guide the students in their academic well-being.

Projects:

The students are encouraged to apply the theoretical knowledge to find feasible and practical engineering solutions. Towards this end, hobby projects, term projects, internships are encouraged. The final year projects of students are available in LAN 172.16.5.78:8080/dspace to enable other students to improve and improvise upon them.

Publications of the College

The college publishes a number of technical magazines with contributions mainly drawn from the student community. These student magazines are aimed at encouraging students to be updated with the latest developments in the fields of science, technology, engineering& mathematics. **Techniyati** is a biannual technical magazine while the College newsletter, **Gayatri Sumam,** is published quarterly. A collection of student articles that are published to commemorate Engineers' Day are brought at as **INGENIUR.**

In addition, each Department brings out its own student magazine. ECE department publishes **Electrospectrum**; **Vidyut** from EEE department; **COSCENGERS INPIRE** from CSE department and **SPIKES INSPIRE** from IT department exhibit students' passion for learning, skills in technical writing and a competitive spirit.

Placement Cell:

The placements in Gayatri Vidya Parishad group of institutions is coordinated by a central committee headed by Dr.P. Venkata Rao, Dean of Placements, GVPCE(A). At GVPCEW, the training and placement activities are looked after by TPO, Sri. C. Srinivas and his team, who are faculty from every department.

The college has a good placement record with placements in reputed companies like Amazon, TCS, Infosys, Wipro, Syntel, Cardyltics, Samsung R&D, Mindtree, Hexaware, GGK Tech, Mu Sigma, Nine Leaps etc to name a few. All students with a good academic record and an aptitude for learning are placed.

NSS Unit:

The college has an NSS Unit (No.90214314) to inculcate positive personality traits in students through social service by her active participation in serving the society. The interested students are selected after a short interview/discussion to carry out service oriented activities.

Facilities in Institute:

- Almost all the classrooms are equipped with LCD projectors and are under CCTV surveillance.
- All the laboratories are well equipped with the latest state-of –the- art-technologies. For carrying out Internet of Things based projects, an **IoT lab** has been established.
- The students can utilize the Learning Management System provided online at www.gvpcew.net/moodle in which the following are available:
  1. Lecture schedules, course outcomes, quizzes
  2. Unit-wise lecture notes
  3. Links to references/Reading materials/ educational videos
- The college has an **Education ERP software solution** that helps students and parents monitor the daily attendance, academic performance and other details online. The facility is available at www.gvpwonline.in/login.aspx. The students/parent can login and monitor the progress using the credentials supplied.
- The college has Memoranda of Understanding with several reputed industries that facilitate industrial training /internships. The college's **Industry-Institute Partnership Cell** set up in 2009 with funding by AICTE liaises with the organizations.
- The **Entrepreneur Development Cell** was established in 2009 with funding from AICTE to develop entrepreneurship skills among the students. It aims to become a center for the development of women entrepreneurs. The Industry Innovation Council by MHRD is a forum for students to ideate, innovate to be innovators and entrepreneurs.

Extra-curricular & Sports:

The students are provided with ample facilities for sports and games. Excellent sports equipment is available in the campus including 7 station multi-gymnasiums, motorized treadmills etc.

EcoTech Club:

A student environment club aims to promote awareness on the need to protect our environment. Various activities such as sapling plantations, energy conservation measures are taken up. Further, the students are challenged to showcase their artitistic and creative ideas by making useful products by recycling the waste.

Nada Bharathi:

The Music club of the college provides opportunities for students to pursue their other interests in music. The music club provides one hour training sessions by a qualified music teacher to the interested students every week.

A qualified **yoga** instructor is available between 6.30 to 7.30 am in the campus to train students in yoga.

Literary and Cultural Club:

Literary and Cultural Club has been initiated in order to inculcate and bring out creative skills among the students. It provides opportunity to involve them in culturally enriching activities, and also give those students with special talents a chance to extend them and to grow in their area of expertise. Its main aim is to expand the students' appreciation of Literary and cultural activities by organizing Competitions and sessions such as GD, Debate, PPT, Elocution, Classical Music and Dance. Students actively participate and showcase their skills.

Facilities in Brief:

Physical Resources

Area of the Campus: 8.25 Acres

Built up Area(s.ft.): 84464

No. of Classrooms &tutorial Rooms:24

Smart Classrooms:4

No. of Laboratories: 36

No. of Drawing Halls:01

A/c Auditorium(400 seating Capacity): 01

Internet:

BSNL Leased line: 40mbps

V-online Leased line: 30mbps

Industry sponsored: 500mbps

NMEICT LINE: 10mbps

Library:

Total Holdings: 20000

Print journals: 54

International: 10

National: 44

Digital Library

Online journals 627(IEEE-227; DELNET-400)

Member DELNET, NDL

Reading Room seating Capacity:150

Total Build up Area: 4306 Sq.ft.

Other Amenities:

Canteen & fast food Center

Ladies Waiting Rooms: 3

Stationery & Xerox Stores

Lift

One 125KVA Generator

Dedicated HT Line

Hybrid Power system

Transport


2021
2022
January
February
March
April
May
June
July
August
September
October
November
December

Planning & implementation of Academic calendars of JNTUK

Grams: "TECHNOLOGY"
Email: dapjntuk@gmail.com

Phone: 0884-2300991
Mobile: +917779000

Directorate of Academic & Planning
JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY KAKINADA
KAKINADA-533003, Andhra Pradesh, INDIA
(Established by AP Government Act No. 30 of 2008)

Lr. No. JNTUK/DAP/Aca.Cal/ II B.Tech/2018-19 Date: 24-05-2018

Dr. Ch. Satyanarayana
M.Tech, Ph.D.,
Director, Academic & Planning

To
The Principals of All Affiliated Colleges,
JNTUK, Kakinada

ACADEMIC CALENDAR
for
B.TECH II YEAR
2017 BATCH

B.TECH II YEAR I Semester			
Description	**From**	**To**	**Weeks**
Commencement of Class Work	**11-06-2018**		
I Unit of Instructions	11-06-2018	04-08-2018	8W
I Mid Examinations	06-08-2018	11-08-2018	1W
II Unit of Instructions	13-08-2018	06-10-2018	8W
II Mid Examinations	08-10-2018	13-10-2018	1W
Preparation & Practicals	15-10-2018	20-10-2018	1W
End Examinations	22-10-2018	03-11-2018	2W
Commencement of Class Work	**19-11-2018**		
B.TECH II YEAR II Semester			
I Unit of Instructions	19-11-2018	12-01-2019	8W
I Mid Examinations	17-01-2019	23-01-2019	1W
II Unit of Instructions	24-01-2019	23-03-2019	8W
II Mid Examinations	25-03-2019	30-03-2019	1W
Preparation & Practicals	01-04-2019	06-04-2019	1W
End Examinations	08-04-2019	20-04-2019	2W
Commence of III Year Class Work	**10-06-2019**		

Director Academic and Planning

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Gayatri Vidya Parishad College of Engineering for Women

Madhurawada, Visakhapatnam

(Affiliated to JNTUK, Approved by AICTE, New Delhi)

Department of Electronics & Communication Engineering

Calendar of Academic Activities - Planned

II D.Tech. I Semester E.C.E (2018-2019)

Date: 08 - 06 - 18

Month	Working Days							Total Working Days	Activities
	Sun	Mon	Tue	Wed	Thu	Fri	Sat		
JUNE						1	2	0	11-06-2018 Commencement of Class Work
	3	4	5	6	7	8	9	0	15-06-2018 Class Committee Meeting - 1
	10	11	12	13	14	15	16	5	16-06-2018 Ramzan
	17	18	19	20	21	22	23	6	
	24	25	25	27	28	29	30	6	
JULY	1	2	3	4	5	6	7	6	14-07-2018 Second Saturday
	8	9	10	11	12	13	14	5	21-07-2018 Guest Lecture by Industrial Expert
	15	16	17	18	19	20	21	6	28-07-2018 Workshop on Advanced Technologies
	22	23	24	25	26	27	28	6	
	29	30	31					2	
AUG				1	2	3	4	4	06-08-2018 to 11-08-2018 Mid-I Examination
	5	6	7	8	9	10	11	6	15-08-2018 Independence Day
	12	13	14	15	16	17	18	3	18-08-2018 Class Committee Meeting - 2
	19	20	21	22	23	24	25	5	22-08-2018 Bakrid
	26	27	28	29	30	31		5	
SEPT							1	1	01-09-2018 Guest Lecture by Industrial Expert
	2	3	4	5	6	7	8	4	03-09-2018 Sri Krishnashtami
	9	10	11	12	13	14	15	5	03-09-2018 Second Saturday
	16	17	18	19	20	21	22	5	13-09-2018 Vinayaka Chavithi
	25	24	25	26	27	28	25	6	15-09-2018 Engineers day Celebration
	30								21-09-2018 Muharram
									29-09-2018 Workshop on Advanced Technologies
OCT		1	2	3	4	5	6	5	02-10-2018 Gandhi Jayanthi
	7	8	9	10	11	12	13	6	02-10-2018 Class Committee Meeting - 3
	14	15	16	17	18	19	20	6	08-10-2018 to 13-10-2018 Mid-II Examination
	21	22	23	24	26	25	27	0	15-10-2018 to 20-10-2018 JNTUK Lab External Exams
	23	29	30	31				0	17-10-2018 to 18-10-2018 Dasara Vacation
									22-10-2018 to 03-11-2018 JNTUK External Exams
NOV					1	2	3	0	07-11-2018 Deepavali
	4	5	6	7	8	9	10	0	10-11-2018 Second Saturday
	11	12	13	14	15	16	17	0	21-11-2018 Eid Miladun Nabi
	18	19	20	21	22	23	24	0	
	25	26	27	28	25	30		0	
	Total Working Days including JNTUK Internal Examinations							99 Days	

Commencement of classwork	Preparation Holidays
JNTUK MID-1 Exams	Holidays
JNTUK MID-II Exams	Special Events
JNTUK Lab External Exam	Guest Lectures
JNTUK External Exams	Meetings

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DEPARTMENT OF
ELECTRONICS AND COMMUNICATION ENGINEERING
G V P COLLEGE OF ENGINEERING FOR WOMEN
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Gayatri Vidya Parishad College of Engineering for Women

Madhurawada, Visakhapatnam

(Affiliated to JNTUK, Approved by AICTE, New Delhi)

Department of Electronics & Communication Engineering

Calendar of Academic Activities - Carried Out

II B.Tech. I Semester E.C.E (2018-2019)

Date: 08 - 11 - 18

Month	Working Days							Total Working Days	Activities
	Sun	Mon	Tue	Wed	Thu	Fri	Sat		
JUNE						1	2	0	11-06-2018 Commencement of Class Work
	3	4	5	6	7	8	9	0	16-06-2018 Ramzan
	10	11	12	13	14	15	16	5	18-06-2018 Class Committee Meeting - 1
	17	18	19	20	21	22	23	6	
	24	25	25	27	28	29	30	6	
JULY	1	2	3	4	5	6	7	6	14-07-2018 Second Saturday
	8	9	10	21	12	19	14	5	24-07-2018 Holiday due to bandh
	15	16	17	18	19	28	21	6	28-07-2018 Inagural of ECLAT Association & Guest Lecture
	22	23	24	25	25	27	28	5	on Under Water Communication by Dr. G.V.Krishna
	29	30	31					2	Kumar, Additional Director & HOD, Warship Technology
									Wing, NSTL, VSP.
									20-07-2018 Inagural of Eco-Tech club
AUG				1	2	3	4	4	15-08-2018 Independence Day
	5	6	7	3	9	10	11	5	20-08-2018 tp 27-08-2018 Mid-I Examihation
	12	13	14	15	16	17	18	5	22-08-2018 Bakrid
	19	20	21	22	29	24	25	5	
	25	27	28	29	30	31		5	
SEPT							1	1	01-09-2018 Fresher's Day Celebration
	2	3	4	5	6	7	8	5	03-09-2018 Sd Krishnashtami
	9	10	11	12	13	14	15	4	03-09-2018 Class Committee Meeting - 2
	15	17	18	19	20	21	22	5	10-09-2018 Holiday due to bandh
	29	24	25	26	27	28	29	6	13-09-2018 Vinayaka Chavithi
	30								15-09-2018 Technical magzine released on the occasion
									of Engineers day
									21-09-2015 Muharram
OCT		1	2	3	4	5	6	5	02-10-2018 Gandhi Jayanthi
	7	3	9	10	11	12	13	5	08-10-2018 Class Committee Meeting - 3
	14	15	16	17	18	19	20	1	08-10-2018 to 15-10-2015 Mid-II Examination
	21	22	23	24	25	20	27	0	13-10-2018 Second Saturday
	28	29	30	31				0	17-10-2018 to 19-10-2018 Dasara Vacation
									20-10-2018 to 23-10-2018 JNTUK Lab External Exams
									26-10-2018 to 08-11-2018 JNTUK External Exams
NOV					1	2	3	0	07-11-2018 Deepavali
	4	3	6	7	3	9	10	0	10-11-2018 Second Saturday
	11	12	13	14	15	16	17	8	21-11-2018 Eid Miladun Nabi
	18	19	20	21	21	23	24	6	
	25	26	27	20	29	56		0	
	Total Working Days including JNTUK Internal Examinations							97 Days	

Commencement of classwork	Preparation Holidays
JNTUK MID-I Exams	Holidays
JNTUK MID-II Exams	Special Events
JNTUK Lab External Exam	Guest Lectures
JNTUK External Exams	Meetings

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Gayatri Vidya Parishad College of Engineering for Women

Madhurawada, Visakhapatnam

(Affiliated to JNTUK, Approved by AICTE, New Delhi)

Department of Electronics & Communication Engineering

Calendar of Academic Activities - Planned

II B.Tech. II Semester (2018-2019)

Date: 16-11-2018

Month	Working Days							Total Working Days	Activities
	Sun	Mon	Tue	Wed	Thu	Fri	Sat		
NOV					1	2	3	0	19-11-2018 Commencement of classes
	4	5	6	7	8	9	10	0	21-11-2018 Eid Miladun Nabi
	11	12	13	14	15	16	17	0	26-11-2018 Class Committee Meeting - I
	18	19	20	21	22	23	24	5	
	25	26	27	28	29	30		5	
DEC							1	1	08-12-2018 Second Saturday
	2	3	4	5	6	7	8	5	22-12-2018 Guest Lecture by Industrial Expert
	9	10	11	12	13	14	15	5	25-12-2018 Christmas
	16	17	18	19	20	21	22	6	29-12-2018 Workshop on Advanced Technologies
	23	24	25	26	27	28	29	5	
	30	31						1	
JAN			1	2	3	4	5	5	11-01-2019 Rangoli Competition
	6	7	8	9	10	11	12	5	12-01-2019 to 15-01-2019 Pongal Vacation
	13	14	15	16	17	18	19	4	17-01-2019 to 23-01-2019 MID - I Examination
	20	21	22	23	24	25	26	5	26-01-2019 Republic Day
	27	28	29	30	31			4	
FEB						1	2	2	02-02-2019 Class Committee Meeting - 2
	3	4	5	6	7	8	9	5	09-02-2019 Second Saturday
	10	11	12	13	14	15	16	6	16-02-2019 Guest Lecture by Industrial Expert
	17	18	19	20	21	22	23	6	23-02-2019 Workshop on Advanced Technologies
	24	25	26	27	28			4	
MAR						1	2	2	04-03-2019 Maha Shivaratri
	3	4	5	6	7	8	9	4	09-03-2019 Second Saturday
	10	11	12	13	14	15	16	5	16-03-2019 College Annual Day
	17	18	19	20	21	22	23	5	18-03-2019 Class Committee Meeting - 3
	24	25	26	27	28	29	30	6	21-03-2019 - Holi
	31								25-03-2019 to 30-03-2019 MID - II Examination
APR		1	2	3	4	5	6	0	01-04-2019 to 05-04-2019 JNTUK Lab External Exams
	7	8	9	10	11	12	13	0	06-04-2019 Ugadhi
	14	15	16	17	18	19	20	0	08-04-2019 to 20-04-2019 JNTUK External Exams
	21	22	23	24	25	26	27	0	13-04-2019 Sri Ramanavami
	28	29	30					0	19-04-2019 Good Friday
	Total Working Days including JNTUK Internal Examinations							103 Days	

Commencement of classwork	Preparation Holidays
JNTUK MID-I Exams	Holidays
JNTUK MID-II Exams	Special Events
JNTUK Lab External Exam	Guest Lectures
JNTUK External Exams	Meetings

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Gayatri Vidya Parishad College of Engineering for Women

Madhurawada, Visakhapatnam

(Affiliated to JNTUK, Approved by AICTE, New Delhi)

Department of Electronics & Communication Engineering

Calendar of Academic Activities - Carried Out

II B.Tech. II Semester (2018-2019)

Date: 13-05-2019

Month	Working Days							Total Working Days	Activities
	Sun	Mon	Tue	Wed	Thu	Fri	Sat		
NOV					1	2	3	0	19-11-2018 Commencement of classes
	4	5	6	7	8	9	10	0	21-11-2018 Eid Miladun Nabi
	11	12	13	14	15	15	17	0	26-11-2018 Class Committee Meeting - 1
	18	19	20	21	22	23	24	5	
	25	26	27	28	29	30		5	
DEC							1	1	08-12-2018 Second Saturday
	2	3	4	5	6	7	8	5	14-12-2018 & 15-12-2018 Power Point Presentations on Latest Trends in Technology on behalf of IETE
	9	10	11	12	13	14	15	6	
	16	17	18	19	20	21	22	5	17-12-2018 Holiday due to Pethay Thufan
	23	24	25	26	27	28	29	5	18-12-2018 Poster Presentation on"Life & works of Sri Srinivasa Ramanujan" on the eve of his 132 annual birthday celebration
	30	31						1	
									21-12-2018 Expert talk on Modern Technology resources and collaborative learning by Dr.S.Lakshmi Narayana EC Member, IEEE Vizag Bay Sub-Section for IEEE Students
									25-12-2018 Christmas
JAN			1	2	3	4	5	5	11-01-2019 Rangoli Competition
	6	7	8	9	10	11	12	6	12-01-2019 to 17-01-2019 Pongal Vacation
	13	14	15	16	17	18	19	2	26-01-2019 Republic Day
	20	21	22	23	24	25	26	5	31-01-2019to 11-02-2019 MID - I Examination
	27	28	29	30	31			4	
FEB						1	2	1	01-02-2019 Holiday due to bandh
	3	4	5	6	7	8	9	5	09-02-2019 Second Saturday
	10	11	22	13	14	15	16	6	15-02-2019 Class Committee Meeting - 2
	17	18	29	20	21	22	23	6	23-02-2019 A Hands-on Workshop on IOT by Mr.P.V.K.Chaitanya Assistant Prof. GVPCEW Mr.V.V.V.Satya narayana Assistant prof GVPCEW and Technical Quiz for IEEE Students
	24	25	26	27	28			4	
									28-02-2019 Poster Presentation on Emerging Technologies on the eve of National Science day.
MAR						1	2	2	04-03-2019 Maha Shivaratri
	3	4	5	6	7	8	9	4	07-03-2019 Guest Lecture be Electro Magnetic Waves & Transmission Lines by Dr.K.Chandra Bhushaha Rao M. O.D ECE , HOD ECE, JNTU Vizayanagaram
	10	11	12	13	14	15	16	5	
	17	18	19	20	21	22	23	6	09-03-2019 Second Saturday
	24	25	26	27	28	29	30	6	11-03-2019 Holiday due to administrative reasons
	31								18-03-2019 Students Hardware Expo on behalf of ECLAT
									19-03-2019 Class Committee Meeting - 3
									15-02-2019 to 30-03-2019 MID - II Examination
APR		1	2	3	4	5	6	0	01-04-2019 & 04-04-2019 JNTUK Lab External Exam
	7	8	9	10	11	12	13	0	05-04-2019 Babu Jag Jiven Ram Jayanthi
	14	15	16	17	18	19	20	0	06-04-2019 Ugadhi
	21	22	23	24	25	26	27	0	21-04-2019 Election voting day
	23	29	30					0	13-04-2019 Second Saturday
									16-04-2019 to 13-05-2019 JNTUK External Exams
									16-04-2019 Good Friday
MAY				1	2	3	4	0	16-04-2019 to 13-05-2019 JNTUK External Exams
	5	6	7	8	9	10	11	0	11-05-2019 Second Saturday
	12	13	14	15	16	17	18	0	
	19	20	21	22	23	24	25	0	
	26	27	28	29	30	31		0	
	Total Working Days including JNTUK Internal Examinations							99 Days	

Commencement of classwork	Preparation Holidays
JNTUK MID-I Exams	Holidays
JNTUK MID-II Exams	Special Events
JNTUK Lab External Exam	Guest Lectures
JNTUK External Exams	Meetings

HEAD OF THE DEPARTMENT

DEPARTMENT OF
ELECTRONICS AND COMMUNICATION ENGINEERING
G V P COLLEGE OF ENGINEERING FOR WOMEN
MADHURAWADA, VISAKHAPATNAM-530 048

Grams: "TECHNOLOGY"
Email: dapjntuk@gmail.com

Phone: 0884-2300991
Mobile: +9177790000

Directorate of Academic & Planning
JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY KAKINADA
KAKINADA-533003, Andhra Pradesh, INDIA
(Established by AP Government Act No. 30 of 2008)

Lr. No. JNTUK/D AP /AcaCal/ B.Tech&B.Pharm/III Year/2018-19 Date: 24-05-2018

Dr. Ch. Satyanarayana
M.Tech, Ph.D.,
Director, Academic & Planning

To
The Principals of All Affiliated Colleges,
JNTUK, Kakinada

ACADEMIC CALENDAR
for
B.TECH& B.PHARM III YEAR
2016 BATCH

B.TECH & B.PHARM III YEAR I Semester			
Description	**From**	**To**	**Weeks**
Commencement of Class Work	**11-06-2018**		
I Unit of Instructions	11-06-2018	04-08-2018	8W
I Mid Examinations	06-08-2018	11-08-2018	1W
II Unit of Instructions	13-08-2018	06-10-2018	8W
II Mid Examinations	08-10-2018	13-10-2018	1W
Preparation & Practicals	15-10-2018	20-10-2018	1W
End Examinations	22-10-2018	03-11-2018	2W
Commencement of Class Work	**19-11-2018**		
B.TECH & B.PHARM III YEAR II Semester			
I Unit of Instructions	**19-11-2018**	12-01-2019	8W
I Mid Examinations	17-01-2019	23-01-2019	1W
II Unit of Instructions	24-01-2019	23-03-2019	8W
II Mid Examinations	25-03-2019	30-03-2019	1W
Preparation & Practicals	01-04-2019	06-04-2019	1W
End Examinations	08-04-2019	20-04-2019	2W
Commence of IV Year Class Work	**10-06-2019**		

Director Academic and Planning

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Gayatri Vidya Parishad College of Engineering for Women

Madhurawada, Visakhapatnam

(Affiliated to JNTUK, Approved by AICTE, New Delhi)

Department of Electronics & Communication Engineering

Calendar of Academic Activities - Planned

III B.Tech. I Semester E.C.E (2018-2019)

Date: 08 - 06 - 18

Month	Working Days							Total Working Days	Activities
	Sun	Mon	Tue	Wed	Thu	Fri	Sat		
JUNE						1	2	0	11-06-2018 Commencement of Class Work
	3	4	5	6	7	8	9	0	15-06-2018 Class Committee Meeting - 1
	10	11	12	13	14	15	16	5	16-06-2018 Ramzan
	17	18	19	20	21	22	23	6	
	24	25	26	27	28	29	30	6	
JULY	1	2	3	4	5	6	7	6	14-07-2018 Second Saturday
	8	9	20	11	12	13	14	5	21-07-2018 Guest Lecture by Industrial Expert
	15	16	17	18	19	20	21	6	28-07-2018 Workshop on Advanced Technologies
	22	23	24	25	20	27	28	6	
	29	30	31					2	
AUG				1	2	3	4	4	06-08-2018 to 11-08-2018 Mid-I Examination
	5	6	7	8	9	10	11	6	15-08-2018 Independence Day
	12	18	14	15	16	17	18	6	18-08-2018 Class Committee Meeting - 2
	19	20	21	22	23	24	25	5	22-08-2018 Bakrid
	26	27	28	29	30	31		5	25-08-2018 Guest Lecture by Industrial Expert
SEPT							1	1	01-09-2018 Guest Lecture by Industrial Expert
	2	3	4	5	6	7	8	4	03-09-2018 Sri Krishnashtami
	9	10	11	12	13	14	15	5	08-09-2018 Second Saturday
	16	17	18	19	20	21	22	5	13-09-2018 Vinayaka Chavithi
	23	24	25	20	27	20	20	6	13-09-2018 Engineers day Celebration
	30								21-09-2018 Muharram
									29-09-2018 Workshop on Advanced Technologies
OCT		1	2	3	4	5	6	5	02-10-2018 Gandhi Jayantbi
	7	8	9	10	11	12	13	6	08-10-2018 Class Committee Meeting - 3
	14	15	16	17	18	19	20	0	08-10-2016 to 13-10-2018 Mid-II Examination
	21	22	23	24	28	26	27	0	15-10-2016 to 20-10-2018 JNTUK Lab External Exams
	23	29	30	31				0	17-10-2018 to 18-10-2018 Dasara Vacation
									22-10-2018 to 03-11-2018 JNTUK External Exams
NOV					1	2	3	0	07-11-2018 Deepavali
	4	6	6	7	8	9	10	0	10-11-2018 Second Saturday
	11	12	13	14	15	15	17	0	21-11-2018 Eid Miliarun Nabi
	18	19	20	21	22	23	24	0	
	25	26	27	28	29	30		0	
	Total Working Days including JNTUK Internal Examinations							99 Days	

Commencement of classwork	Preparation Holidays
JNTUK MID-I Exams	Holidays
JNTUK MID-II Exams	Special Events
JNTUK Lab External Exam	Guest Lectures
JNTUK External Exams	Meetings

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Gayatri Vidya Parishad College of Engineering for Women

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(Affiliated to JNTUK, Approved by AICTE, New Delhi)

Department of Electronics & Communication Engineering

Calendar of Academic Activities - Carried Out

III B.Tech. I Semester E.E.E (2018-2019)

Date: 08 - 11 - 13

Month	Working Days							Total Working Days	Activities
	Sun	Mon	Tue	Wed	Thu	Fri	Sat		
JUNE						1	2	0	11-06-2018 Commencement of Class Work
	3	4	5	6	7	8	9	0	16-06-2018 Ramzan
	10	11	12	13	14	15	16	5	20-06-2018 Class Committee Meeting - 1
	17	18	19	20	21	22	23	6	
	24	25	26	27	28	29	30	6	
JULY	1	2	3	4	5	6	7	6	28-04-2018 to 10-02-2018 A Training Program on Machine
	8	9	10	11	12	13	14	5	Learning for interested students
	15	16	17	18	19	20	21	6	14-07-2018 Second Saturday
	22	23	24	26	26	27	28	5	24-07-2018 Holiday due to bandh
	29	30	31					2	28-07-2018 Inaguval of ECLAT Assuciatibh & Guest Lecturd on
									Under Water Communication by Dr. G.V.Krishna Kumar,
									Additional Director & HOD, Warship Technology Wing, NSTL,
									VSP.
									31-07-2018 Condura Interacted with the students
AUG				1	2	3	4	4	06-08-2018 to 10-08-2018 Mid-I Examination
	5	6	7	8	9	10	11	5	15-06-2018 Independence Day
	12	13	14	15	16	17	18	5	17-08-2018 Professional Ethics & Human values Exam.
	19	20	21	22	23	24	25	5	18-08-2013 Class Committee Meeting - 2
	26	27	28	29	30	81		5	22-08-2018 Bakrid
SEPT							1	1	02-09-2018 Sri Krishnashtami
	2	3	4	5	6	7	8	5	10-09-2018 Holiday due to bandh
	9	10	21	12	13	14	15	4	13-09-2018 Vinayaka Chavithi
	16	17	18	19	20	21	22	5	15-09-2018 Technical magzine released on the occasion of
	23	24	25	26	27	26	29	8	Engineers day
	30								21-09-2018 Muharram
OCT		1	2	3	4	6	6	6	02-10-2018 Gandhi Jayanthi
	7	3	9	10	11	12	13	5	03-10-2018 Class Committee Meeting - 3
	14	25	16	17	18	19	29	0	08-10-2018 to 12-10-2018 Mid-II Examination
	21	22	23	24	25	26	27	0	13-10-2018 Second Saturday
	28	29	30	31				0	15,16-10-2018 & 22,23-10-2018 JNTUK Lab External Exams
									J7-10-2018 tb 19-10-2018 Dasara Vacation
									27-10-2018 to 09-11-2018 JNTUK External Exams
NOV					1	2	3	0	07-11-2018 Deepavali
	4	5	6	7	8	9	10	0	10-11-2018 Second Saturday
	11	12	13	14	15	15	17	0	21-11-2018 Eid Miladuh Nabi
	13	19	20	21	22	23	24	0	
	26	20	27	28	23	30		0	
	Total Working Days including JNTUK Internal Examinations							96 Days	

Commencement of classwork	Preparation Holidays
JNTUK MID-I Exams	Holidays
JNTUK MID-II Exams	Special Events
JNTUK Lab Extemel Exam	Guest Lectures
JNTUK External Exams	Meetings

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DEPARTMENT OF
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Department of Electronics & Communication Engineering

Calendar of Academic Activities - Planned

III B.Tech. II Semester (2018-2019)

Date: 16-11-2018

Month	Working Days							Total Working Days	Activities
	Sun	Mon	Tue	Wed	Thu	Fri	Sat		
NOV					1	2	3	0	19-11-2018 Commencement of classes
	4	5	6	7	8	9	10	0	21-11-2018 Eid Milarlun Nabi
	11	12	13	14	15	16	17	0	26-11-2018 Class Committee Meeting - 1
	18	19	20	21	22	23	24	5	
	25	26	27	28	29	30		5	
DEC							1	1	08-12-2018 Second Saturday
	2	3	4	5	6	7	8	5	22-12-2018 Guest Lecture by Industrial Expert
	9	10	11	12	13	14	15	6	25-12-2018 Christmas
	16	17	18	19	16	21	22	6	29-12-2018 Workshop on Advanced Technologies
	23	24	25	26	27	28	29	5	
	30	31						1	
JAN			1	2	3	4	5	5	11-01-2019 Rangoli Competition
	6	7	3	9	10	11	12	5	12-01-2019 to 15-01-2019 Pongal Vacation
	13	14	15	10	17	15	19	4	17-01-2019 to 23-01-2019 MID - I Examination
	20	21	22	23	24	25	26	5	26-01-2019 Republic Day
	27	25	29	20	31			4	
FEB						1	2	2	02-02-2019 Class Committee Meeting - 2
	8	4	5	5	7	8	9	5	09-02-2019 Second Saturday
	10	21	12	13	14	15	16	6	16-02-2019 Guest Lecture by Industrial Expert
	17	18	19	20	21	22	23	6	23-02-2019 Workshop on Advanced Technologies
	24	25	26	27	28			4	
MAR						1	2	2	04-03-2019 Maha Shivaratri
	3	4	5	6	7	8	9	4	09-03-2019 Second Saturday
	10	11	12	13	14	15	16	6	16-03-2019 College Annual Day
	17	18	19	20	21	22	28	5	18-03-2019 Class Committee Meeting - 3
	24	25	25	27	23	20	30	6	21-03-2019 - Holi
	31								18-03-2019 to 30-08-2019 MID - II Examination
APR		1	2	3	4	5	6	0	01-04-2019 to 05-04-2019 JNTUK Lab External Exams
	7	8	9	10	11	12	13	0	06-04-2019 Ugadhi
	14	15	26	17	18	19	20	6	08-04-2019 to 20-04-2019 JNTUK External Exams
	21	22	29	24	25	26	27	0	13-04-2019 Sri Ramanavami
	23	29	30					0	19-04-2019 Good Friday
	Total Working Days including JNTUK Internal Examinations							103 Days	

Commencement of classwork	Preparation Holidays
JNTUK MID-I Exams	Holidays
JNTUK MID-II Exams	Special Events
JNTUK Lab External Exam	Guest Lectures
JNTUK External Exams	Meetings

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ELECTRONICS AND COMMUNICATION ENGINEERING
G. V. P. COLLEGE OF ENGINEERING FOR WOMEN
MADHURAWADA, VISAKHAPATNAM-530 048

Gayatri Vidya Parishad College of Engineering for Women

Madhurawada, Visakhapatnam

(Affiliated to JNTUK, Approved by AICTE, New Delhi)

Department of Electronics & Communicadon Engineering

Calendar of Academic Activities - Carried Out

III B.Tech. II Semester (2018-2019)

Date: 18-05-2019

Month	Working Days							Total Working Days	Activities
	Sun	Mon	Tue	Wed	Thu	Fri	Sat		
NOV					1	2	3	0	19-11-2018 Commencement of classes
	4	5	6	7	8	9	10	0	21-11-2018 Eid Miladun Nabi
	11	12	13	14	15	16	17	0	26-11-2018 Class Committee Meeting - 1
	18	19	20	21	22	23	24	5	29-11-2018 Placement Meeting by Dr. Venket Rao
	25	26	27	28	29	30		5	
DEC							1	1	08-12-2018 Second Saturday
	2	3	4	5	6	7	8	5	14-12-2018 & 15-12-2018 Power Point Presentations on Latest Trends in Technology on behalf of IETE
	9	10	11	12	13	14	15	6	
	16	17	18	19	20	21	22	5	17-12-2018 Holiday due to Pethay Thufan
	23	24	25	26	27	28	29	5	18-12-2018 Poster Presentation on "Life & works of Sri Srinivasa Ramanujan" on the eve of his 132 annual birthday celebration
	30	31						1	
									21-12-2018 Expert talk on Modern Technology resources and collaborative learning by Dr.S.Lakshmi Narayana EC Member, IEEE Vizag Bay Sub-Section for IEEE Students
									25-12-2018 Christmas
JAN			1	2	3	4	5	5	05-01-2019 Industrial Visit at Doordharshan-Simhachalam
	6	7	8	9	10	11	12	5	11-01-2019 Rangoli Competition
	13	14	15	16	17	18	19	2	12-01-2019 to 17-01-2019 Pongal Vacation
	20	21	22	23	24	25	26	5	26-01-2019 Republic Day
	27	28	29	30	31			4	21-01-2019 to 11-02-2019 MID - I Examination
FEB						1	2	1	01-02-2019 Holiday due to bandh
	3	4	5	6	7	8	9	5	06-02-2019 Placement Meeting by Dr. Venkat Rao
	10	11	12	13	14	15	16	6	09-02-2019 Second Saturday
	17	18	19	20	21	22	23	6	15-02-2019 Class Committee Meeting - 2
	24	25	26	27	28			4	28-02-2019 A Hands-on Workshop on IOT by Mr.P.V.K.Chaitanya Assistant Prof. GVPCEW Mr.V.V.V.Satya narayana Assistant prof. GVPCEW and Technical Quiz for IEEE Students
									28-02-2019 Poster Presentation on Emerging Technologies on the eve of National Science day.
MAR						1	2	2	04-03-2019 Maha Shivaratri
	3	4	5	6	7	8	9	4	09-03-2019 Second Saturday
	10	11	12	13	14	15	16	5	11-03-2019 Holiday due to administrative reasons
	17	18	19	20	21	22	23	6	16-03-2019 Expert talk on VLSI by Mr. A. Venkat Krishna, Technical Head, QSOCS Technologies Pvt. Ltd, Bangalore on behalf of IETE
	24	25	26	27	28	29	30	5	
	31								18-03-2019 Students Hardware Expo on behalf of ECLAT
									19-03-2019 Class Committee Meeting - 3
									19-03-2019 to 29-03-2019 MID - II Examination
APR		1	2	3	4	5	6	0	01-04-2019 & 04-04-2019 JNTUK Lab External Exams
	7	8	9	10	11	12	13	0	05-04-2019 Babu Jag Jivan Ram Jayanthi
	14	15	16	17	18	19	20	0	06-04-2019 Ugadhi
	21	22	23	24	25	26	27	0	11-04-2019 Election voting day
	28	29	30					0	13-04-2019 Second Saturday
									15-04-2019 to 07-05-2019 JNTUK External Exams
									15-04-2019 Good Friday
MAY				1	2	3	4	0	15-04-2019 to 07-05-2019 JNTUK External Exams
	5	6	7	8	9	10	11	0	
	12	13	14	15	16	17	18	0	
	19	20	21	22	23	24	25	0	
	26	27	28	29	30	31		0	
	Total Working Days including JNTUK Internal Examinations							98 Days	

Commencement of classwork	Preparation Holidays
JNTUK MID-I Exams	Holidays
JNTUK MID-II Exams	Special Events
JNTUK Lab External Exams	Guest Lectures
JNTUK External Exams	Meetings

HEAD OF THE DEPARTMENT

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING
G.V.P. COLLEGE OF ENGINEERING FOR WOMEN

Grams: "TECHNOLOGY"
Email: dapjntuk@gmail.com

Phone: 0884-2300991
Mobile: +9177790000

Directorate of Academic & Planning
JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY KAKINADA
KAKINADA-533003, Andhra Pradesh, INDIA
(Established by AP Government Act No. 30 of 2008)

Lr. No. JNTUK/DAP/Aca.Cal/ IV B.Tech & B.Pharm/2018-19 Date: 24-05-2018

Dr. Ch. Satyanarayana
M.Tech, Ph.D.,
Director, Academic & Planning

To
The Principals of All Affiliated Colleges,
JNTUK, Kakinada

ACADEMIC CALENDAR
for
B.TECH& B.PHARM IV YEAR
2015 BATCH

B.TECH & B.PHARM IV YEAR I Semester			
Description	**From**	**To**	**Weeks**
Commencement of Class Work	**11-06-2018**		
I Unit of Instructions	11-06-2018	04-08-2018	8W
I Mid Examinations	06-08-2018	11-08-2018	1W
II Unit of Instructions	13-08-2018	06-10-2018	8W
II Mid Examinations	08-10-2018	13-10-2018	1W
Preparation & Practicals	15-10-2018	20-10-2018	1W
End Examinations	22-10-2018	03-11-2018	2W
Commencement of Class Work	**19-11-2018**		
B.TECH & B.PHARM IV YEAR II Semester			
I Unit of Instructions	19-11-2018	12-01-2019	8W
I Mid Examinations	17-01-2019	23-01-2019	1W
II Unit of Instructions	24-01-2019	23-03-2019	8W
II Mid Examinations	25-03-2019	30-03-2019	1W
Preparation & Practicals	01-04-2019	06-04-2019	1W
End Examinations	08-04-2019	20-04-2019	2W

Director Academic and Planning

Copy to the Secretary to the Hon'ble Vice Chancellor
Copy to the Rector
Copy to the Registrar
Copy to the Director of Evaluation
Copy to the Controller of Examination

Gayatri Vidya Parishad College of Engineering for Women

Madhurawada, Visakhapatnam

(Affiliated to JNTUK, Approved by AICTE, New Delhi)

Department of Electronics & Communication Engineering

Calendar of Academic Activities - Planned

IV B.Tech. I Semester E.C.E (2018-2019)

Date: 08 - 06 - 18

Month	Working Days							Total Working Days	Activities
	Sun	Mon	Tue	Wed	Thu	Fri	Sat		
JUNE						1	2	0	11-06-2018 Commencement of Class Work
	3	4	5	6	7	8	9	0	15-06-2018 Class Committee Meeting - 1
	10	11	12	13	14	15	16	5	16-06-2018 Ramzan
	17	18	19	20	21	22	23	6	18-6-18 to 23-6-18 CRT Training program
	24	25	26	27	28	29	30	6	
JULY	1	2	3	4	5	6	7	6	14-07-2018 Second Saturday
	8	9	10	11	12	13	14	5	21-07-2018 Guest Lecture by Industrial Expert
	15	16	17	18	19	20	21	6	28-07-2018 Workshop on Advanced Technologies
	22	23	24	25	26	27	28	6	
	29	30	31					2	
AUG				1	2	3	4	4	06-08-2018 to 11-08-2018 Mid-I Examination
	5	6	7	8	9	10	11	6	15-08-2018 Independence Day
	12	13	14	15	10	17	18	5	18-08-2018 Class Committee Meeting - 2
	19	20	21	22	23	24	25	5	26-08-2018 Bakrid
	25	27	25	23	30	31		5	
SEPT							1	1	03-09-2018 Sri Krishnashtami
	2	3	4	5	6	7	8	4	08-09-2018 Second Saturday
	9	10	11	12	13	14	15	5	13-09-2018 Vinayaka Chavithi
	16	17	10	19	20	21	22	5	13-09-2018 Engineers day Celebration
	23	24	25	26	27	28	29	6	17-09-2010 Guest Lecture by Industrial Expert
	30								21-09-2018 Muharram
									29-09-2018 Workshop on Advanced Technologies
OCT		1	2	3	4	5	6	5	02-10-2018 Gandhi Jayanthi
	7	8	9	10	11	12	13	6	03-10-2018 Class Committee Meeting - 3
	14	15	15	17	10	19	20	0	08-10-2018 to 13-10-2018 Mid-II Examination
	21	22	25	24	25	26	27	0	15-10-2018 to 20-10-2018 JNTUK Lab External Exams
	28	29	30	31				0	17-10-2018 to 18-10-2018 Dasara Vacation
									22-10-2018 to 03-11-2018 JNTUK External Exams
NOV					1	2	3	0	07-11-2018 Deepavali
	4	5	6	7	8	9	10	0	10-11-2018 Second Saturday
	11	12	13	14	16	15	17	0	21-11-2018 Eid Miladun Nabi
	18	13	20	21	22	23	24	0	
	26	28	27	28	29	30		0	
	Total Working Days including JNTUK Internal Examinations							99 Days	

Commencement of classwork	Preparation Holidays
JNTUK Mid-I Exams	Holidays
JNTUK Mid-II Exams	Special Events
JNTUK Lab External Exam	Guest Lectures
JNTUK External Exams	Meetings

HEAD OF THE DEPARTMENT

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DEPARTMENT OF
ELECTRONICS AND COMMUNICATION ENGINEERING
G V P COLLEGE OF ENGINEERING FOR WOMEN
MADHURAWADA, VISAKHAPATNAM-530 048

Gayatri Vidya Parishad College of Engineering for Women

Madhurawada, Visakhapatnam

(Affiliated to JNTUK, Approved by AICTE, New Delhi)

Department of Electronics & Communication Engineering

Calendar of Academic Activities - Carried Out

IV B.Tech. I Semester E.C.E (2018-2019)

Date: 08 - 11 - 18

Month	Working Days							Total Working Days	Activities
	Sun	Mon	Tue	Wed	Thu	Fri	Sat		
JUNE						1	2	0	11-06-2018 Commencement of Class Work
	3	4	5	6	7	8	9	0	16-06-2018 Ramzan
	10	11	12	13	14	15	16	5	18-6-18 to 23-8-18 FACE Training program
	17	18	19	20	21	22	23	5	25-06-2018 Class Committee Meeting - 1
	24	25	26	27	28	29	30	6	
JULY	1	2	3	4	5	6	7	6	14-07-2018 Second Saturday
	8	9	10	11	12	13	14	5	24-07-2018 Holiday due to bandh
	15	16	17	18	19	20	21	6	28-07-2018 Inagural of ECLAT Association & Guest Lecture on Under Water Communication by Dr. G.V.Krishna Kumar, Additional Director & HOD, Warship Technology Wing, NSTL, VSP.
	22	23	24	25	26	27	28	5	
	29	30	31					2	
AUG				1	2	3	4	4	06-08-2018 to 13-08-2018 Mid-I Examination
	5	6	7	8	9	10	11	6	11-08-2018 to 14-08-2018 TALENTIO training program
	12	13	14	15	16	17	18	5	15-08-2018 Independence Day
	19	20	21	22	23	24	25	3	21-08-2018 Class Committee Meeting - 2
	20	27	25	29	30	31		5	22-08-2018 Bakrid
SEPT							1	1	03-09-2018 Sri Krishnashtami
	2	3	4	5	6	7	8	5	10-09-2018 Holiday due to bandh
	9	10	11	12	13	14	15	4	13-09-2018 Vinayaka Chavithi
	16	17	18	19	20	21	22	5	15-09-2018 Technical magzine released on the occasion of Engineers day
	23	24	25	26	27	28	29	6	
	30								21-09-2018 Muharram
OCT		1	2	3	4	6	6	5	02-10-2018 Gandhi Jayanthi
	7	8	9	10	11	12	13	5	03-10-2018 Class Committee Meeting - 3
	14	15	16	17	18	19	20	1	04-10-2018 Guest Lecture on Optical Communication by Dr. B.S. Murthy ,Professor, Dept. of ECE, GVPCEW.
	21	22	23	24	25	25	27	0	
	28	29	30	31				0	03-10-2018 to 15-10-2018 Mid-II Examination
									13-10-1010 Second Saturday
									17-10-2018 to 19-10-2018 Dasara Vacation
									20-10-2018 to 23-10-2018 JNTUK Lab External Exams
									26-10-2018 to 08-11-2018 JNTUK External Exams
NOV					1	1	3	0	07-11-2018 Deepavali
	4	5	6	7	8	9	10	0	10-11-2018 Second Saturday
	11	12	13	14	15	16	17	0	21-11-2018 Eid Miladun Nebi
	18	19	20	21	22	23	24	0	
	25	26	27	28	29	10		0	
	Total Working Days including JNTUK Internal Examinations							98 Days	

Commencement of classwork	Preparation Holidays
JNTUK MID-I Exams	Holidays
JNTUK MID-II Exams	Special Events
JNTUK Lab External Exam	Guest Lectures
JNTUK External Exams	Meetings

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Department of Electronics & Communication Engineering

Calendar of Academic Activities - Planned

IV B.Tech. II Semester (2018-2019)

Date: 16-11-2018

Month	Working Days							Total Working Days	Activities
	Sun	Mon	Tue	Wed	Thu	Fri	Sat		
NOV					1	2	3	0	19-11-2018 Commencement of classes
	4	5	6	7	8	9	10	0	21-11-2018 Eid Miladun Nabi
	11	12	13	14	15	16	17	0	26-11-2018 Class Committee Meeting - 1
	18	19	20	21	22	23	24	5	
	25	26	27	28	29	30		5	
DEC							1	1	09-12-2018 Second Saturday
	2	3	4	5	6	7	8	5	22-12-2018 Guest Lecture by Industrial Expert
	9	10	11	12	13	14	15	6	25-12-2018 Christmas
	16	17	18	19	20	21	22	6	29-12-2018 Workshop on Advanced Technologies
	23	24	25	26	27	28	29	5	
	30	31						1	
JAN			1	2	3	4	5	5	11-01-2019 Rangoli Competition
	6	7	8	9	10	11	12	5	12-01-2018 to 15-01-2019 Pongal Vacation
	13	14	13	16	17	18	19	4	17-01-2019 to 23-01-2019 MID - I Examination
	20	21	22	23	24	25	26	5	26-01-2019 Republic Day
	27	28	28	30	31			4	
FEB						1	2	2	02-02-2019 Class Committee Meeting - 2
	3	4	5	6	7	8	9	5	09-01-2019 Second Saturday
	10	11	12	13	14	15	16	6	16-02-2019 Guest Lecture by Industrial Expert
	17	18	19	20	21	22	23	6	23-02-2019 Workshop on Advanced Technologies
	24	25	26	27	28			4	
MAR						1	2	2	04-03-2019 Maha Shivaratri
	3	4	5	6	7	8	9	4	09-03-2019 Second Saturday
	10	11	12	13	14	15	16	6	16-03-2019 College Annual Day
	17	18	19	20	21	22	23	5	18-03-2019 Class Committee Meeting - 3
	24	25	25	27	28	29	30	6	21-03-2019 - Holi
	31								25-03-2019 to 30-03-2019 MID - II Examination
APR		1	2	3	4	5	6	0	01-04-2019 to 05-04-2019 JNTUK Project External Review
	7	8	9	10	11	12	13	0	06-04-2019 Ugadhi
	14	15	16	17	18	19	20	0	08-04-2019 to 20-04-2019 JNTUK External Exams
	21	22	18	24	25	26	27	0	13-04-2019 Sri Ramanavami
	28	25	30					0	19-04-2019 Good Friday
	Total Working Days including JNTUK Internal Examinations							103 Days	

Commencement of classwork	Preparation Holidays
JNTUK MID-I Exams	Holidays
JNTUK MID-II Exams	Special Events
JNTUK Lab External Exam	Guest Lectures
JNTUK External Exams	Meetings

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Gayatri Vidya Parishad College of Engineering for Women

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Department of Electronics & Communication Engineering

Calendar of Academic Activities - Carried Out

IV B.Tech. II Semester (2018-2019)

Date: 13-05-2019

Month	Working Days							Total Working Days	Activities
	Sun	Mon	Tue	Wed	Thu	Fri	Sat		
NOV					1	2	3	0	19-11-2018 Commencement of classes
	4	5	6	7	8	9	10	0	21-11-2018 Eid Miladun Nabi
	11	12	13	14	15	16	17	0	26-11-2018 Class Committee Meeting - 1
	18	19	20	21	22	23	24	5	
	25	26	27	28	29	30		5	
DEC							1	1	08-12-2018 Second Saturday
	2	3	4	5	6	7	8	5	17-12-2018 Holiday due to Pethay Thufan
	9	10	11	12	13	14	15	6	18-12-2018 Poster Presentation on "Life & works of Sri Srinivasa Ramanujan" on the eve of his 132 annual birthday celebration
	16	17	18	19	20	21	22	5	
	23	24	25	26	27	28	29	5	25-12-2018 Christmas
	30	31						1	
JAN			1	2	3	4	5	5	11-01-2019 Rangoli Competition
	6	7	8	9	10	11	12	5	12-01-2019 to 17-01-2019 Pongal Vacation
	13	14	15	16	17	18	19	2	26-01-2019 Republic Day
	20	21	22	23	24	25	26	5	31-01-2019 to 11-02-2015 MID - I Examination
	27	28	29	30	31			4	
FEB						1	2	1	01-02-2019 Holiday due to Ganuh
	1	4	5	6	7	8	9	5	09-02-2019 Second Saturday
	10	11	12	13	14	15	16	6	15-02-2019 Class Committee Meeting - 2
	17	18	19	20	22	22	23	6	23-02-2019 Industrial Visit at Doppler Radar Station -Kailashgiri,VSP.
	24	25	26	27	28			4	28-02-2019 Poster Presentation on Emerging Technologies on the eve of National Science day.
MAR						1	2	2	04 08-2019 Mahs Shivratin
	3	4	5	6	7	8	9	4	09-03-2019 Second Saturday
	10	11	12	13	14	15	16	5	11-03-2019 Holiday due to administrative reasons
	27	18	19	20	21	22	23	6	18-03-2018 Students Hardware Expo on behalf of ECLAT
	24	25	26	27	28	29	30	4	19-03-2018 Class Committee Meeting - 3
	31								25-03-2019 to 28-03-2019 MID - II Examination
APR		1	2	3	4	5	6	0	02-04-2019 & 03-04-2019 JNTUK Project External Review
	7	8	9	10	11	12	13	0	05-04-2019 Babu Jag Jivan Ram Jayanthi
	14	15	16	17	18	15	20	0	06-04-2019 Ugadhi
	21	22	23	24	25	26	27	0	11-04-2019 Election voting day
	28	29	30					0	13-04-2019 Second Saturday
									16-04-2019 to 10-05-2019 JNTUK External Exams
									19-04-2019 Good Friday
MAY				1	2	3	4	0	16-04-2019 to 10-05-2019 JNTUK External Exams
	5	6	7	8	9	10	11	0	
	12	13	14	15	16	17	18	0	
	19	20	21	22	23	24	25	0	
	26	27	28	29	30	31		0	
	Total Working Days including JNTUK Internal Examinations							97 Days	

Commencement of classwork	Preparation Holidays
JNTUK MID-I Exams	Holidays
JNTUK MID-II Exams	Special Events
JNTUK Lab External Exam	Guest Lectures
JNTUK External Exams	Meetings

HEAD OF THE DEPARTMENT

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DEPARTMENT OF
ELECTRONICS AND COMMUNICATION ENGINEERING
G V P COLLEGE OF ENGINEERING FOR WOMEN
MADHURAWADA, VISAKHAPATNAM-530 048

Gayatri Vidya Parishad College of Engineering for Women
(Approved by AICTE, New Delhi, Affiliated to JNT University, Kakinada)
Madhurawada, Visakhapatnam – 530048
DEPARTMENT of COMPUTER SCIENCE and ENGINEERING

INTERNAL NOTE

Date: 2nd June, 2019

All the faculty Members are requested to attend the meeting on 4th June 2019, in the Head of the Department Cabin at 3:00P.M to discuss the following agenda.

Agenda:

1. Preparation of Academic Calendar.
2. Subject Allocation.
3. Time Tables.
4. Any other matter for Discussion.

HoD - CSE

Signature of the Staff Members:

Dr. E.V.Prasad
Dr.N.B.Venkateswarlu
Dr.M.Bhanu Sridhar
Dr.N.Sharmili
Mrs.K.Suneetha
Mr.S.Sumahasan
Mr.A.Uday Kumar
Mrs.K.Rohini
Mr.V.Lakshmana Rao
Mr.K.Purushotham Naidu
Mrs.B.VijayaLakshmi

Mr.K.Nagaraju
Mr.G.Sankara Rao
Mrs.M.Swapna
Ms.Y.Sowmya
Ms.D.Indu
Mrs.V.Gowtami Annapurna
Mrs.K.N.Satya Chitra
Mr.B.Ramesh
Mr.A.Nagendra
Dr.Tushar Kanti Mishra
Mr.R.Satish Kumar

Gayatri Vidya Parishad College of Engineering for Women
(Approved by AICTE, New Delhi, Affiliated to JNT University, Kakinada)
Madhurawada, Visakhapatnam – 530048
DEPARTMENT of COMPUTER SCIENCE and ENGINEERING

Minutes of the Meeting

Date: 4th June, 2019

The following are the minutes of the staff meeting conducted on 4th June, 2019 in the HoD cabin at 03:00 PM.

The following faculty are endorsed with the following responsibilities.

1. Academic Calendar - Mrs.V.Gowtami Annapurna.
2. Time Tables - Mrs.K.Suneetha.

   Submission of Lecture Schedules on or before 08th June, 2019.

HoD - CSE

Signature of the Staff Members:

Dr. E.V.Prasad
Dr.N.B.Venkateswarlu
Dr.M.Bhanu Sridhar
Dr.N.Sharmili
Mrs.K.Suneetha
Mr.S.Sumahasan
Mr.A.Uday Kumar
Mrs.K.Rohini
Mr.V.Lakshmana Rao
Mr.K.Purushotham Naidu
Mrs.B.VijayaLakshmi
Mr.K.Nagaraju
Mr.G.Sankara Rao
Mrs.M.Swapna
Ms.Y.Sowmya
Ms.D.Indu
Mrs.V.Gowtami Annapurna
Mrs.K.N.Satya Chitra
Mr.B.Ramesh
Mr.A.Nagendra
Dr.Tushar Kanti Mishra
Mr.R.Satish Kumar

A.Y. 2019-20

Grams: "TECHNOLOGY"
Email: dapjntuk@gmail.com

Phone: 0884-2300991
Mobile: +9963993504

Directorate of Academic & Planning

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY KAKINADA
KAKINADA-533003, Andhra Pradesh, INDIA
(Established by AP Government Act No. 30 of 2008)

Lr. No. JNTUK/DAP/AC/R. Tech/II Year/2019-20 *Date: 10.05.2019*

Dr. A. Mallikarjuna Prasad
M.E, Ph.D.,
Director, Academic Planning

To
All the Principals of Affiliated Colleges,
JNTUK, Kakinada

ACADEMIC CALENDAR FOR B.TECH II YEAR (2018 BATCH)

Description	From	To	Weeks
I SEMESTER			
Commencement of Class Work	**10.06.2019**		
I Unit of Instructions	10.06.2019	03.08.2019	8W
I Mid Examinations	05.08.2019	10.08.2019	1W
II Unit of Instructions	12.08.2019	05.10.2019	8W
II Mid Examinations	07.10.2019	12.10.2019	1W
Preparation & Practicals	14.10.2019	19.10.2019	1W
End Examinations	21.10.2019	02.11.2019	2W
Commencement of II Semester Class Work	**18.11.2019**		
II SEMESTER			
I Unit of Instructions	**18.11.2019**	11.01.2020	8W
I Mid Examinations	13.01.2020	23.01.2020	1W
II Unit of Instructions	24.01.2020	21.03.2020	8W
II Mid Examinations	23.03.2020	28-03-2020	1W
Preparation	30.03.2020	04.04.2020	1W
End Examinations	06.04.2020	18.04.2020	2W
Commence of III Year Class Work	**08.06.2020**		

A. M. Prasad
Director Academic Planning

Copy to the Secretary to the Hon'ble Vice Chancellor, JNTUK.
Copy to PA to the Rector, JNTUK.
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Copy to PA to the Director of Evaluation, JNTUK.

II -YEAR

GAYATRI VIDYA PARISHAD COLLEGE OF ENGINEERING FOR WOMEN

Madhurawada, Visakhapatnam

Calendar of Academic Activities-Planning

Academic Year: 2019-2020 **II B.Tech. I Semester** **Branch: C SE**

Month	Working Days							Total Working Days	Activities
	Su	M	Tu	W	Th	F	Sa		
JUN							1		
	2	3	4	5	6	7	8		
	9	10	11	12	13	14	15	6	
	16	17	18	19	20	21	22	6	
	23	24	25	26	27	28	29	6	
	30								
JUL		1	2	3	4	5	6	6	10-7-19 Guest Lecture by Industry Expert
	7	8	9	10	11	12	13	5	13-7-19 Second Saturday
	14	15	16	17	18	19	20	6	14-7-19 Workshop on Advanced Technologies
	21	22	23	24	25	26	27	6	
	28	29	30	31				3	
AUG					1	2	3	3	
	4	5	6	7	8	9	10	6	12-8-19 Bakrid
	11	12	13	14	15	16	17	4	15-8-19 Independence Day
	18	19	20	21	22	23	24	5	23-8-19 Krishnashtami
	25	26	27	28	29	30	31	6	28-8-19 Class Committee Meeting2
SEP	1	2	3	4	5	6	7	5	2-9-19 Vinayakachavithi
	8	9	10	11	12	13	14	4	10-9-19 Muharram
	15	16	17	18	19	20	21	6	12-9-19 Guest Lecture by Industry Expert
	22	23	24	25	26	27	28	6	14-9-19 Second Saturday
	29	30						1	25-9-19 Workshop on Advanced Technologies
									26-9-19 Class Committee Meeting3
OCT			1	2	3	4	5	4	2-10-19 Gandhi Jayanthi
	6	7	8	9	10	11	12	6	7-10-19 to 12-10-19 Mid2 exams
	13	14	15	16	17	18	19	0	14-10-19 to 19-10-19 JNTUK Lab External Exams
	20	21	22	23	24	25	26	0	21-10-19 to 2-11-19 JNTUK External End Examinations
	27	28	29	30	31			0	
NOV						1	2	0	09-10-2019 Second Saturday
	3	4	5	6	7	8	9	0	
	10	11	12	13	14	15	16	0	
	17	18	19	20	21	22	23	0	
	24	25	26	27	28	29	30	0	
	Total Working days including JNTUK Internal Examinations							100	

JNTUK MID Exams
JNTUK Lab External Examinations
JNTUK External End Examinations

Holidays
Guest Lectures
Meetings

GAYATRI VIDYA PARISHAD COLLEGE OF ENGINEERING FOR WOMEN

Madhurawada, Visakhapatnam

Calendar of Academic Activities Carried Out

Academic Year: 2019-2020 II B.Tech. I Semester Branch: CSE

Month	Su	M	Tu	W	Th	F	Sa	Total Working Days	Activities
JUN							1		17-6-19 Commencement of class work; 13-6-19 Class Committee Meeting1; 20-6-19 Seminar on IEEE Student Branch level Activities by Dr. S. Deepika Rani, Sri A Rao, IEEE Vizag Bay Sub Section for all IEEE student members of GVPCEW; 22-6-19, 29-6-19 A 4-Day workshop on Cyber Chalkers: A Web Technologies Latest Trend by Dr. PVSL Jagadamba, Mr. V L Rao, Mr. K P Naidu, Mr. Ms B V Lakshmi
	2	3	4	5	6	7	8		
	9	10	11	12	13	14	15	6	
	16	17	18	19	20	21	22	6	
	23	24	25	26	27	28	29	6	
	30								
JUL		1	2	3	4	5	6	6	6-7-19 A Coding Quiz Competition by CSI STUDENTS [illegible]; 13-7-19 Second Saturday; 20-7-19 A 4-Day workshop on Cyber Chalkers: A Web Technologies Latest Trend by Dr. PVSL Jagadamba, Mr. V L Rao, Mr. K P Naidu, Ms. Ms B V Lakshmi
	7	8	9	10	11	12	13	6	
	14	15	16	17	18	19	20	6	
	21	22	23	24	25	26	27	6	
	28	29	30	31				3	
AUG					1	2	3	3	5-8-19 to 10-8-19 Mid1 Exams; 12-8-19 Bakrid; 15-8-19 Independence Day; 23-8-19 Krishnashtami; 28-8-19 Class Committee Meeting2; 30-8-19 A Guest Lecture on Statistics Concepts in Data Science by Mr. Kanul Behera, Data Scientist, Ambaher, UK; 31-08-19 A 4-Day workshop on Cyber Chalkers: A Web Technologies Latest Trend by Dr. PVSL Jagadamba, Mr. V L Rao, Mr. K P Naidu, Mr. Ms B V Lakshmi
	4	5	6	7	8	9	10	6	
	11	12	13	14	15	16	17	4	
	18	19	20	21	22	23	24	5	
	25	26	27	28	29	30	31	6	
SEP	1	2	3	4	5	6	7	5	2-9-19 Vinayaka Chavithi; 5-9-19 Seminar on Reporting in IEEE events and Blogging [illegible] by Dr. S Lakshminarayana, Executive member, IEEE Vizag Bay Sub Section for all IEEE student members of GVPCEW; 10-9-19 Muharram; 14-9-19 Second Saturday; 24-9-19 Class Committee Meeting3; 27-9-19 2-day training program on Google Explore on Machine Learning Dr. M Swapna; 28-9-19 A Guest Lecture on Latest Trends in Data Analytics: An Introduction by Mr. P Suresh Kumar, WIPRO, Visakhapatnam
	8	9	10	11	12	13	14	4	
	15	16	17	18	19	20	21	6	
	22	23	24	25	26	27	28	6	
	29	30						1	
OCT			1	2	3	4	5	4	1-10-19 Seminar on Machine Learning Applications using Python Programming by B L V Vinay Kumar, Assistant Professor, GVPCEW as a part of IEEE Day Celebrations; 2-10-19 Gandhi Jayanthi; 7-10-19 to 12-10-19 Mid2 exams; 14-10-19 to 19-10-19 JNTUK Lab External Exams; 21-10-19 to 2-11-19 JNTUK External End Examinations
	6	7	8	9	10	11	12	6	
	13	14	15	16	17	18	19	0	
	20	21	22	23	24	25	26	0	
	27	28	29	30	31			0	
NOV						1	2	0	09-11-2019 Second Saturday; 9-11-19 2-day training program on Google Explore on Machine Learning Dr. M Swapna
	3	4	5	6	7	8	9	0	
	10	11	12	13	14	15	16	0	
	17	18	19	20	21	22	23	0	
	24	25	26	27	28	29	30	0	
Total Working days including JNTUK internal Examinations								100	

Legend:
- JNTUK MID Exams
- JNTUK Lab External Examinations
- JNTUK External End Examinations
- Holidays
- Guest Lectures
- Meetings

Grams: "TECHNOLOGY"
Email: dapjntuk@gmail.com

Phone: 0884-2300991
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Directorate of Academic & Planning

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY KAKINADA
KAKINADA-533003, Andhra Pradesh, INDIA
(Established by AP Government Act No. 30 of 2008)

Lr. No. JNTUK/DAP/AC/B. Tech/III Year/2019-20 Date: 08.05.2019

Dr. A. Mallikarjuna Prasad
M.E, Ph.D.,
Director, Academic Planning

To
All the Principals of Affiliated Colleges,
JNTUK, Kakinada

ACADEMIC CALENDAR FOR B.TECH III YEAR (2017 BATCH)

I SEMESTER			
Description	**From**	**To**	**Weeks**
Commencement of Class Work	**10.06.2019**		
I Unit of Instructions	10.06.2019	03.08.2019	8W
I Mid Examinations	05.08.2019	10.08.2019	1W
II Unit of Instructions	12.08.2019	05.10.2019	8W
II Mid Examinations	07.10.2019	12.10.2019	1W
Preparation & Practicals	14.10.2019	19.10.2019	1W
End Examinations	21.10.2019	02.11.2019	2W
Commencement of II Semester Class Work	**18.11.2019**		
II SEMESTER			
I Unit of Instructions	**18.11.2019**	11.01.2020	8W
I Mid Examinations	13.01.2020	23.01.2020	1W
II Unit of Instructions	24.01.2020	21.03.2020	8W
II Mid Examinations	23.03.2020	28-03-2020	1W
Preparation	30.03.2020	04.04.2020	1W
End Examinations	06.04.2020	18.04.2020	2W
Commence of IV Year Class Work	**08.06.2020**		

A. Mallikarjuna Prasad
Director Academic Planning

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Copy to PA to the Rector, JNTUK.
Copy to PA to the Registrar, JNTUK.
Copy to PA to the Director of Evaluation, JNTUK.

GAYATRI VIDYA PARISHAD COLLEGE OF ENGINEERING FOR WOMEN

Madhurawada, Visakhapatnam

Calendar of Academic Activities-Planned

Academic Year: 2019-2020 **III B.Tech. I Semester** **Branch:CSE**

Month	Working Days							Total Working Days	Activities
	Su	M	Tu	W	Th	F	Sa		
JUN							1		10-6-19 Commencement of class work
	2	3	4	5	6	7	8		19-6-19 I Class Committee Meeting1
	9	10	11	12	13	14	15	6	
	16	17	18	19	20	21	22	6	
	23	24	25	26	27	28	29	6	
	30								
JUL		1	2	3	4	5	6	6	13-7-19 Guest Lecture by Industry Expert
	7	8	9	10	11	12	13	4	13-7-19 Second Saturday
	14	15	16	17	18	19	20	6	29-7-19 Workshop on Advanced Technologies
	21	22	23	24	25	26	27	6	
	28	29	30	31				3	
AUG					1	2	3	3	5-8-19 to10-8-19 Mid1 Exams
	4	5	6	7	8	9	10	6	12-8-19 Bakrid
	11	12	13	14	15	16	17	4	15-8-19 Independence Day
	18	19	20	21	22	23	24	5	23-8-19 Krishnashtami
	25	26	27	28	29	30	31	6	26-8-19 Class Committee Meeting2
SEP	1	2	3	4	5	6	7	5	2-9-19 Vinayakachaviti
	8	9	10	11	12	13	14	4	10-9-19 Muharram
	15	16	17	18	19	20	21	6	20-9-19 Guest Lecture by Industry Expert
	22	23	24	25	26	27	28	6	14-9-19 Second Saturday
	29	30						1	27-9-19 Workshop on Advanced Technologies
									30-10-19 Class Committee Meeting3
OCT			1	2	3	4	5	4	2-10-19 Gandhi Jayanthi
	6	7	8	9	10	11	12	6	7-10-19 to 12-10-19 Mid2 exams
	13	14	15	16	17	18	19	0	14-10-19 to 19-10-19 JNTUK Lab External Exams
	20	21	22	23	24	25	26	0	21-10-19 to 2-11-19 JNTUK External End Examinations
	27	28	29	30	31			0	
NOV						1	2	0	09-10-2019 Second Saturday
	3	4	5	6	7	8	9	0	
	10	11	12	13	14	15	16	0	
	17	18	19	20	21	22	23	0	
	24	25	26	27	28	29	30	0	
	Total Working days including JNTUK Internal Examinations							100	

JNTUK MID Exams
JNTUK Lab External Examinations
JNTUK External End Examinations
Holidays
Guest Lectures
Meetings

GAYATRI VIDYA PARISHAD COLLEGE OF ENGINEERING FOR WOMEN

Madhurawada, Visakhapatnam

Calendar of Academic Activities-Carried out

Academic Year: 2019-2020 **III B.Tech. I Semester** **Branch: CSE**

Month	Working Days							Total Working Days	Activities
	Su	M	Tu	W	Th	F	Sa		
JUN							1		
	2	3	4	5	6	7	8		
	9	10	11	12	13	14	15	6	
	16	17	18	19	20	21	22	6	
	23	24	25	26	27	28	29	6	
	30								
JUL		1	2	3	4	5	6	6	
	7	8	9	10	11	12	13	5	
	14	15	16	17	18	19	20	6	
	21	22	23	24	25	26	27	6	
	28	29	30	31				4	
AUG					1	2	3	3	
	4	5	6	7	8	9	10	6	
	11	12	13	14	15	16	17	4	
	18	19	20	21	22	23	24	5	
	25	26	27	28	29	30	31	6	
SEP	1	2	3	4	5	6	7	5	
	8	9	10	11	12	13	14	4	
	15	16	17	18	19	20	21	5	
	22	23	24	25	26	27	28	6	
	29	30						2	
OCT			1	2	3	4	5	4	
	6	7	8	9	10	11	12	6	
	13	14	15	16	17	18	19	0	
	20	21	22	23	24	25	26	0	
	27	28	29	30	31			0	
NOV						1	2	0	
	3	4	5	6	7	8	9	0	
	10	11	12	13	14	15	16	0	
	17	18	19	20	21	22	23	0	
	24	25	26	27	28	29	30	0	
	Total Working days including JNTUK Internal Examinations							100	

JNTUK MID Exams
JNTUK Lab External Examinations
JNTUK External End Examinations
Holidays
Guest Lectures
Meetings

CRITERIA-2
JNTUK-ACADEMIC CALENDARS
A.Y-2019-20

Grams: "TECHNOLOGY"
Email: dapjntuk@gmail.com

Phone: 0884-2300991
Mobile: +919963993504

Directorate of Academic & Planning
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(Established by AP Government Act No. 30 of 2008)

Lr. No. JNTUK/DAP/AC/B. Tech/IV Year/2019-20 Date: 10.05.2019

Dr. A. Mallikarjuna Prasad
M.E, Ph.D.,
Director, Academic Planning

To
All the Principals of Affiliated Colleges,
JNTUK, Kakinada

ACADEMIC CALENDAR FOR B.TECH IV YEAR (2016 BATCH)

I SEMESTER			
Description	**From**	**To**	**Weeks**
Commencement of Class Work	**10.06.2019**		
I Unit of Instructions	10.06.2019	03.08.2019	8W
I Mid Examinations	05.08.2019	10.08.2019	1W
II Unit of Instructions	12.08.2019	05.10.2019	8W
II Mid Examinations	07.10.2019	12.10.2019	1W
Preparation & Practicals	14.10.2019	19.10.2019	1W
End Examinations	21.10.2019	02.11.2019	2W
Commencement of II Semester Class Work	**18.11.2019**		
II SEMESTER			
I Unit of Instructions	**18.11.2019**	11.01.2020	8W
I Mid Examinations	13.01.2020	23.01.2020	1W
II Unit of Instructions	24.01.2020	21.03.2020	8W
II Mid Examinations	23.03.2020	28-03-2020	1W
Preparation	30.03.2020	04.04.2020	1W
End Examinations	06.04.2020	18.04.2020	2W

A. M. Prasad
Director Academic Planning

Copy to the Secretary to the Hon'ble Vice Chancellor, JNTUK.
Copy to PA to the Rector, JNTUK.
Copy to PA to the Registrar, JNTUK.
Copy to PA to the Director of Evaluation, JNTUK.

GAYATRI VIDYA PARISHAD COLLEGE OF ENGINEERING FOR WOMEN

Madhurawada, Visakhapatnam

Calendar of Academic Activities-Planning

Academic Year: 2019-2020 IV B.Tech. I Semester **Branch: CSE**

Month	Working Days							Total Working Days	Activities
	Su	M	Tu	W	Th	F	Sa		
JUN							1		10-6-19 Commencement of Classwork
	2	3	4	5	6	7	8		20-6-19 Class Committee Meeting1
	9	10	11	12	13	14	15	6	24-6-19 to 29-6-19 Python Training Program
	16	17	18	19	20	21	22	6	
	23	24	25	26	27	28	29	6	
	30								
JUL		1	2	3	4	5	6	6	1-7-19 to 3-7-19 FACT Training Program
	7	8	9	10	11	12	13	5	4-7-19 to 10-7-19 Python Training Program
	14	15	16	17	18	19	20	6	13-7-19 Second Saturday
	21	22	23	24	25	26	27	6	19-7-19 Guest Lecture by Industry Expert
	28	29	30	31				3	26-7-19 Workshop on Advanced Technologies
									30-7-19 to 4-8-19 Talentio Training Program
AUG					1	2	3	3	5-8-19 to 10-8-19 Mid1 Exams
	4	5	6	7	8	9	10	6	12-8-19 Bakrid
	11	12	13	14	15	16	17	4	15-8-19 Independence Day
	18	19	20	21	22	23	24	5	23-8-19 Krishnashtami
	25	26	27	28	29	30	31	6	27-8-19 Class Committee Meeting2
SEP	1	2	3	4	5	6	7	5	2-9-19 Vinayakachavithi
	8	9	10	11	12	13	14	4	5-9-19 Guest Lecture by Industry Expert
	15	16	17	18	19	20	21	6	10-9-19 Muharram
	22	23	24	25	26	27	28	6	14-9-19 Second Saturday
	29	30						1	18-9-19 Workshop on Advanced Technologies
OCT			1	2	3	4	5	4	1-10-19 Class Committee Meeting3
	6	7	8	9	10	11	12	6	2-10-19 Gandhi Jayanthi
	13	14	15	16	17	18	19	0	7-10-19 to 12-10-19 Mid2 exams
	20	21	22	23	24	25	26	0	14-10-19 to 19-10-19 JNTUK Lab External Exams
	27	28	29	30	31			0	21-10-19 to 2-11-19 JNTUK External End Examinations
NOV						1	2	0	09-10-2019 Second Saturday
	3	4	5	6	7	8	9	0	
	10	11	12	13	14	15	16	0	
	17	18	19	20	21	22	23	0	
	24	25	26	27	28	29	30	0	
	Total Working days including JNTUK Internal Examinations							100	

JNTUK MID Exams
JNTUK Lab External Examinations
JNTUK External End Examinations
Holidays
Guest Lectures
Meetings
Training& Placement Activities

GAYATRI VIDYA PARISHAD COLLEGE OF ENGINEERING FOR WOMEN

Madhurawada, Visakhapatnam

Calendar of Academic Activities-Carried out

Academic Year: 2019-2020 IV B.Tech. I Semester **Branch: C SE**

Month	Working Days							Total Working Days	Activities
	Su	M	Tu	W	Th	F	Sa		
JUN							1		
	2	3	4	5	6	7	8		
	9	10	11	12	13	14	15	6	
	16	17	18	19	20	21	22	6	
	23	24	25	26	27	28	29	6	
	30								
JUL		1	2	3	4	5	6	5	
	7	8	9	10	11	12	13	5	
	14	15	16	17	18	19	20	6	
	21	22	23	24	25	26	27	6	
AUG					1	2	3	3	
	4	5	6	7	8	9	10	6	
	11	12	13	14	15	16	17	4	
	18	19	20	21	22	23	24	5	
	25	26	27	28	29	30	31	6	
SEP	1	2	3	4	5	6	7	5	
	8	9	10	11	12	13	14	4	
	15	16	17	18	19	20	21	6	
	22	23	24	25	26	27	28	6	
	29	30						1	
OCT			1	2	3	4	5	4	
	6	7	8	9	10	11	12	6	
	13	14	15	16	17	18	19	0	
	20	21	22	23	24	25	26	0	
	27	28	29	30	31			0	
NOV						1	2	0	
	3	4	5	6	7	8	9	0	
	10	11	12	13	14	15	16	0	
	17	18	19	20	21	22	23	0	
	24	25	26	27	28	29	30	0	
	Total Working days including JNTUK Internal Examinations							100	

JNTUK MID Exams

JNTUK Lab External Examinations

JNTUK External End Examinations

Holidays

Guest Lectures

Meetings

Gayatri Vidya Parishad College of Engineering for Women
(Approved by AICTE, New Delhi, Affiliated to JNT University, Kakinada)
Madhurawada, Visakhapatnam – 530048
DEPARTMENT of COMPUTER SCIENCE and ENGINEERING

INTERNAL NOTE

Date: 10th December, 2019

All the faculty Members are requested to attend the meeting on 12th December 2019, in the Head of the Department Cabin at 3:00P.M to discuss the following agenda.

Agenda:

1. Preparation of Academic Calendar.
2. Subject Allocation.
3. Time Tables.
4. Any other matter for Discussion.

HoD - CSE

Signature of the Staff Members:

Dr. E.V.Prasad
Dr.N.B.Venkateswarlu
Dr.M.Bhanu Sridhar
Dr.N.Sharmili
Mrs.K.Suneetha
Mr.S.Sumahasan
Mr.A.Uday Kumar
Mrs.K.Rohini
Mr.V.Lakshmana Rao
Mr.K.Purushotham Naidu
Mrs.B.VijayaLakshmi

Mr.K.Nagaraju
Mr.G.Sankara Rao
Mrs.M.Swapna
Ms.Y.Sowmya
Ms.D.Indu
Mrs.V.Gowtami Annapurna
Mrs.K.N.Satya Chitra
Mr.B.Ramesh
Mr.A.Nagendra
Dr.Tushar Kanti Mishra
Mr.R.Satish Kumar

Gayatri Vidya Parishad College of Engineering for Women
(Approved by AICTE, New Delhi, Affiliated to JNT University, Kakinada)
Madhurawada, Visakhapatnam – 530048
DEPARTMENT of COMPUTER SCIENCE and ENGINEERING

Minutes of the Meeting

Date: 12th December, 2019

The following are the minutes of the staff meeting conducted on 12th December, 2019 in the HoD cabin at 03:00 PM.

The following faculty are endorsed with the following responsibilities:

1. Academic Calendar - Mrs.V.Gowtami Annapurna.
2. Time Tables - Mrs.K.Suneetha.

   Submission of Lecture Schedules on or before 16th December, 2019.

HoD - CSE

Signature of the Staff Members:

Dr. E.V.Prasad
Dr.N.B.Venkateswarlu
Dr.M.Bhanu Sridhar
Dr.N.Sharmili
Mrs.K.Suneetha
Mr.S.Sumahasan
Mr.A.Uday Kumar
Mrs.K.Rohini
Mr.V.Lakshmana Rao
Mr.K.Purushotham Naidu
Mrs.B.VijayaLakshmi
Mr.K.Nagaraju
Mr.G.Sankara Rao
Mrs.M.Swapna
Ms.Y.Sowmya
Ms.D.Indu
Mrs.V.Gowtami Annapurna
Mrs.K.N.Satya Chitra
Mr.B.Ramesh
Mr.A.Nagendra
Dr.Tushar Kanti Mishra
Mr.R.Satish Kumar

A.Y = 2019-20 2nd year

Grams: "TECHNOLOGY"
Email: dapjntuk@gmail.com

Phone: 0884-2300991
Mobile: +9963993504

Directorate of Academic & Planning
JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY KAKINADA
KAKINADA-533003, Andhra Pradesh, INDIA
(Established by AP Government Act No. 30 of 2008)

Lr. No. *JNTUK/DAP/AC/B. Tech/II Year/2019-20* *Date: 30-05-2019*

Dr. A. Mallikarjuna Prasad
M.E, Ph.D.,
Director, Academic Planning

To
All the Principals of Affiliated Colleges,
JNTUK, Kakinada

ACADEMIC CALENDAR FOR B.TECH II YEAR (2018 BATCH)

Description	From	To	Weeks
I SEMESTER			
Commencement of Class Work	**10.06.2019**		
I Unit of Instructions	10.06.2019	03.08.2019	8W
I Mid Examinations	05.08.2019	10.08.2019	1W
II Unit of Instructions	12.08.2019	05.10.2019	8W
II Mid Examinations	07.10.2019	12.10.2019	1W
Preparation & Practicals	14.10.2019	19.10.2019	1W
End Examinations	21.10.2019	02.11.2019	2W
Commencement of II Semester Class Work	**18.11.2019**		
II SEMESTER			
I Unit of Instructions	**18.11.2019**	11.01.2020	8W
I Mid Examinations	13.01.2020	23.01.2020	1W
II Unit of Instructions	24.01.2020	21.03.2020	8W
II Mid Examinations	23.03.2020	28-03-2020	1W
Preparation	30.03.2020	04.04.2020	1W
End Examinations	06.04.2020	18.04.2020	2W
Commence of III Year Class Work	**08.06.2020**		

A.M.Prasad
Director Academic Planning

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Copy to PA to the Rector, JNTUK.
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Copy to PA to the Director of Evaluation, JNTUK.

GAYATRI VIDYA PARISHAD COLLEGE OF ENGINEERING FOR WOMEN

Madhurawada::Visakhapatnam

Calendar of Academic Activities-Planned

Academic Year: 2019-2020 II B.Tech. II Semester **Branch:CSE**

Month	Working Days							Total Working Days	Activities
	Su	M	Tu	W	Th	F	Sa		
NOV						1	2	0	18-11-2019 Classwork Commencement 26-11-19 Class Committee Meeting 1
	3	4	5	6	7	8	9	0	
	10	11	12	13	14	15	16	0	
	17	18	19	20	21	22	23	6	
	24	25	26	27	28	29	30	6	
DEC	1	2	3	4	5	6	7	6	14-12-19 Second Saturday 18-12-19 Guest Lecture by Industrial Expert 25-12-19 Christamas
	8	9	10	11	12	13	14	5	
	15	16	17	18	19	20	21	6	
	22	23	24	25	26	27	28	5	
	29	30	31					2	
JAN				1	2	3	4	4	4-1-20 Workshop on Advanced Technologies 11-1-20 Second Saturday 13-1-20 to 23-1-20 MID-I Examination 14-1-20 to 16-1-20 Pongal Vacation 26-1-20 Republic Day
	5	6	7	8	9	10	11	5	
	12	13	14	15	16	17	18	3	
	19	20	21	22	23	24	25	6	
	26	27	28	29	30	31		5	
FEB							1	1	01-02-20 Guest Lecture by Industrial Expert 03-02-20 Class Committee Meeting 2 08-02-20 Second Saturday 21-02-20 Maha Sivaratri 22-02-20 Workshop on Advanced Technologies
	2	3	4	5	6	7	8	5	
	9	10	11	12	13	14	15	6	
	16	17	18	19	20	21	22	5	
	23	24	25	26	27	28	29	6	
MAR	1	2	3	4	5	6	7	6	09-03-20 Holi 14-03-20 Second Saturday 17-03-20 Class Committee Meeting 3 21-03-20 College Annual day 23-03-2020 to 28-03-2020 MID - II Examination 25-03-20 Ugadi 30-03-20 to 04-04-20 JNTUK Lab External Exams
	8	9	10	11	12	13	14	4	
	15	16	17	18	19	20	21	5	
	22	23	24	25	26	27	28	5	
	29	30	31					0	
APR				1	2	3	4	0	02-04-20 Sri Rama navami 06-04-2020 to 18-04-2020 JNTUK External Exams 10-04-2020 Good Friday 11-04-2020 Second Saturday 14-04-2020 Dr. B.R. Ambedkar Jayanthi
	5	6	7	8	9	10	11	0	
	12	13	14	15	16	17	18	0	
	19	20	21	22	23	24	25	0	
	26	27	28	29	30			0	
	Total Working days including JNTUK Internal Examinations							103	
								103	

JNTUK MID Exams
JNTUK Lab External Examinations
JNTUK External End Examinations

Holidays
Guest Lectures/Workshops
Meetings
Special Events

GVP COLLEGE OF ENGG. FOR WOMEN

Grams: "TECHNOLOGY"
Email: dapjntuk@gmail.com

Phone: 0884-2300991
Mobile: +9963993504

Directorate of Academic & Planning

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY KAKINADA
KAKINADA-533003, Andhra Pradesh, INDIA
(Established by AP Government Act No. 30 of 2008)

Lr. No. JNTUK/DAP/AC/B. Tech/III Year/2019-20 Date: 30-05-2019

Dr. A. Mallikarjuna Prasad
M.E, Ph.D.,
Director, Academic Planning

To
All the Principals of Affiliated Colleges,
JNTUK, Kakinada

ACADEMIC CALENDAR FOR B.TECH III YEAR (2017 BATCH)

I SEMESTER			
Description	**From**	**To**	**Weeks**
Commencement of Class Work	**10.06.2019**		
I Unit of Instructions	10.06.2019	03.08.2019	8W
I Mid Examinations	05.08.2019	10.08.2019	1W
II Unit of Instructions	12.08.2019	05.10.2019	8W
II Mid Examinations	07.10.2019	12.10.2019	1W
Preparation & Practicals	14.10.2019	19.10.2019	1W
End Examinations	21.10.2019	02.11.2019	2W
Commencement of II Semester Class Work	**18.11.2019**		
II SEMESTER			
I Unit of Instructions	**18.11.2019**	11.01.2020	8W
I Mid Examinations	13.01.2020	23.01.2020	1W
II Unit of Instructions	24.01.2020	21.03.2020	8W
II Mid Examinations	23.03.2020	28-03-2020	1W
Preparation	30.03.2020	04.04.2020	1W
End Examinations	06.04.2020	18.04.2020	2W
Commence of IV Year Class Work	**08.06.2020**		

A.M.prasad
Director Academic Planning

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Copy to PA to the Rector, JNTUK.
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Copy to PA to the Director of Evaluation, JNTUK.

GAYATRI VIDYA PARISHAD COLLEGE OF ENGINEERING FOR WOMEN

Madhurawada::Visakhapatnam

Calendar of Academic Activities-Planned

Academic Year: 2019-2020 III B.Tech. II Semester **Branch:CSE**

Month	Working Days							Total Working Days	Activities
	Su	M	Tu	W	Th	F	Sa		
NOV						1	2	0	18-11-2019 Classwork Commencement 27-11-19 Class Committee Meeting 1
	3	4	5	6	7	8	9	0	
	10	11	12	13	14	15	16	0	
	17	[illegible]	19	20	21	22	23	6	
	24	25	26	27	28	29	30	6	
DEC	1	2	3	4	5	6	7	6	14-12-19 Second Saturday 25-12-19 Christamas 28-12-19 Guest Lecture by Industrial Expert
	8	9	10	11	12	13	[illegible]	5	
	15	16	17	18	19	20	21	6	
	22	23	24	[illegible]	26	27	[illegible]	5	
	29	30	31					2	
JAN				1	2	3	4	4	9-1-20 Workshop on Advanced Technologies 11-1-20 Second Saturday 13-1-20 to 23-1-20 MID-1 Examination 14-1-20 to 16-1-20 Pongal Vacation 26-1-20 Republic Day
	5	6	7	8	[illegible]	10	[illegible]	5	
	12	[illegible]	[illegible]	[illegible]	[illegible]	17	18	3	
	19	20	21	22	23	24	25	6	
	[illegible]	27	28	29	30	31		5	
FEB							1	1	04-02-20 Class Committee Meeting 2 05-02-20 Guest Lecture by Industrial Expert 08-02-20 Second Saturday 19-02-20 Workshop on Advanced Technologies 21-02-20 Maha Sivaratri
	2	3	4	5	6	7	[illegible]	5	
	9	10	11	12	13	14	15	6	
	16	17	18	[illegible]	20	[illegible]	22	5	
	23	24	25	26	27	28	29	6	
MAR	1	2	3	4	5	6	7	6	09-03-20 Holi 14-03-20 Second Saturday 16-03-20 Class Committee Meeting 3 21-03-20 College Annual day 23-03-2020 to 28-03-2020 MID - II Examination 25-03-20 Ugadi 30-03-20 to 04-04-20 JNTUK Lab External Exams
	8	[illegible]	10	11	12	13	[illegible]	4	
	15	16	17	18	19	20	[illegible]	6	
	22	23	24	[illegible]	26	27	28	5	
	29	30	31					0	
APR				1	2	3	4	0	02-04-20 Sri Rama navami 06-04-2020 to 18-04-2020 JNTUK External Exams 10-04-2020 Good Friday 11-04-2020 Second Saturday 14-04-2020 Dr. B.R. Ambedkar Jayanthi
	5	6	7	8	9	[illegible]	[illegible]	0	
	12	13	[illegible]	15	16	17	18	0	
	19	20	21	22	23	24	25	0	
	26	27	28	29	30			0	
	Total Working days including JNTUK Internal Examinations							103	
								103	

JNTUK MID Exams
JNTUK Lab External Examinations
JNTUK External End Examinations

Holidays
Guest Lectures/Workshops
Meetings
Special Events

GVP COLLEGE OF ENGG. FOR W... VSP

Grams: "TECHNOLOGY"
Email: dapjntuk@gmail.com

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Directorate of Academic & Planning

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY KAKINADA
KAKINADA-533003, Andhra Pradesh, INDIA
(Established by AP Government Act No. 30 of 2008)

Lr. No. JNTUK/DAP/AC/B.Tech/IV Year/2019-20 *Date: 30-05-2019*

Dr. A. Mallikarjuna Prasad
M.E, Ph.D.,
Director, Academic Planning

To
All the Principals of Affiliated Colleges,
JNTUK, Kakinada

ACADEMIC CALENDAR FOR B.TECH IV YEAR (2016 BATCH)

I SEMESTER			
Description	**From**	**To**	**Weeks**
Commencement of Class Work	**10.06.2019**		
I Unit of Instructions	10.06.2019	03.08.2019	8W
I Mid Examinations	05.08.2019	10.08.2019	1W
II Unit of Instructions	12.08.2019	05.10.2019	8W
II Mid Examinations	07.10.2019	12.10.2019	1W
Preparation & Practicals	14.10.2019	19.10.2019	1W
End Examinations	21.10.2019	02.11.2019	2W
Commencement of II Semester Class Work	**18.11.2019**		
II SEMESTER			
I Unit of Instructions	**18.11.2019**	11.01.2020	8W
I Mid Examinations	13.01.2020	23.01.2020	1W
II Unit of Instructions	24.01.2020	21.03.2020	8W
II Mid Examinations	23.03.2020	28-03-2020	1W
Preparation	30.03.2020	04.04.2020	1W
End Examinations	06.04.2020	18.04.2020	2W

A.M.Prasad
Director Academic Planning

Copy to the Secretary to the Hon'ble Vice Chancellor, JNTUK.
Copy to PA to the Rector, JNTUK.
Copy to PA to the Registrar, JNTUK.
Copy to PA to the Director of Evaluation, JNTUK.

GAYATRI VIDYA PARISHAD COLLEGE OF ENGINEERING FOR WOMEN

Madhurawada::Visakhapatnam

Calendar of Academic Activities-Planned

Academic Year: 2019-2020 IV B.Tech. II Semester **Branch:CSE**

Month	Working Days							Total Working Days	Activities
	Su	M	Tu	W	Th	F	Sa		
NOV						1	2	0	18-11-2019 Classwork Commencement 29-11-19 Class Committee Meeting 1
	3	4	5	6	7	8	9	0	
	10	11	12	13	14	15	16	0	
	17	18	19	20	21	22	23	6	
	24	25	26	27	28	29	30	6	
DEC	1	2	3	4	5	6	7	6	14-12-19 Second Saturday 25-12-19 Christamas 28-12-19 Guest Lecture by Industrial Expert
	8	9	10	11	12	13	14	5	
	15	16	17	18	19	20	21	6	
	22	23	24	25	26	27	28	5	
	29	30	31					2	
JAN				1	2	3	4	4	9-1-20 Workshop on Advanced Technologies 11-1-20 Second Saturday 13-1-20 to 23-1-20 MID-1 Examination 14-1-20 to 16-1-20 Pongal Vacation 26-1-20 Republic Day
	5	6	7	8	9	10	11	5	
	12	13	14	15	16	17	18	3	
	19	20	21	22	23	24	25	6	
	26	27	28	29	30	31		5	
FEB							1	1	01-02-20 Class Committee Meeting 2 05-02-20 Guest Lecture by Industrial Expert 08-02-20 Second Saturday 19-02-20 Workshop on Advanced Technologies 21-02-20 Maha Sivaratri
	2	3	4	5	6	7	8	5	
	9	10	11	12	13	14	15	6	
	16	17	18	19	20	21	22	5	
	23	24	25	26	27	28	29	6	
MAR	1	2	3	4	5	6	7	6	09-03-20 Holi 14-03-20 Second Saturday 16-03-20 Class Committee Meeting 3 20-03-20 College Annual day 23-03-2020 to 28-03-2020 MID - II Examination 25-03-20 Ugadi 30-03-20 to 04-04-20 JNTUK Lab External Exams
	8	9	10	11	12	13	14	4	
	15	16	17	18	19	20	21	6	
	22	23	24	25	26	27	28	5	
	29	30	31					0	
APR				1	2	3	4	0	02-04-20 Sri Rama navami 06-04-2020 to 18-04-2020 JNTUK External Exams 10-04-2020 Good Friday 11-04-2020 Second Saturday 14-04-2020 Dr. B.R. Ambedkar Jayanthi
	5	6	7	8	9	10	11	0	
	12	13	14	15	16	17	18	0	
	19	20	21	22	23	24	25	0	
	26	27	28	29	30			0	
	Total Working days including JNTUK Internal Examinations							103	
								103	

JNTUK MID Exams
JNTUK Lab External Examinations
JNTUK External End Examinations

Holidays
Guest Lectures/Workshops
Meetings
Special Events

GVP COLLEGE OF ENGG FOR WOMEN

Gayatri Vidya Parishad College of Engineering for Women
(Approved by AICTE, New Delhi, Affiliated to JNT University, Kakinada)
Madhurawada, Visakhapatnam – 530048
DEPARTMENT of COMPUTER SCIENCE and ENGINEERING

INTERNAL NOTE

Date: 2nd June, 2019

All the faculty Members are requested to attend the meeting on 4th June 2019, in the [illegible] the Department Cabin at 3:00P.M to discuss the following agenda

Agenda:

1. Preparation of Academic Calendar
2. Subject Allocation.
3. Time Tables.
4. Any other matter for Discussion

H.O.D / CSE

Signature of the Staff Members

Dr. E.V.Prasad

Dr.N.B.Venkateswarlu

Dr.M.Bhanu Sridhar

Dr.N.Sharmili

Mrs.K.Suneetha

Mr.S.Sumahasan

Mr.A.Uday Kumar

Mrs.K.Rohini

Mr.V.Lakshmana Rao

Mr.K.Purushotham Naidu

Mrs.B.VijayaLakshmi

Mr.K.Nagaraju

Mr.G.Sankara Rao

Mrs.M.Swapna

Ms.Y.Sowmya

Ms.D.Indu

Mrs.V.Gowtami Annapurna

Mrs.K.N.Satya Chitra

Mr.B.Ramesh

Mr.A.Nagendra

Dr.Tushar Kanti Mishra

Mr.R.Satish Kumar

Gayatri Vidya Parishad College of Engineering for Women
(Approved by AICTE, New Delhi, Affiliated to JNT University, Kakinada)
Madhurawada, Visakhapatnam – 530048
DEPARTMENT of COMPUTER SCIENCE and ENGINEERING

Minutes of the Meeting

Date: 4th June, 2019

The following are the minutes of the staff meeting conducted on 4th June, 2019 in the Head cabin at 03.00 PM.

The following faculty are endorsed with the following responsibilities:

1. Academic Calendar - Mrs.V.Gowtami Annapurna
2. Time Tables - Mrs.K.Suneetha

Submission of Lecture Schedules on or before 08th June, 2019.

HoD, CSE

Signature of the Staff Members:

Dr. E.V.Prasad

Dr.N.B.Venkateswarlu

Dr.M.Bhanu Sridhar

Dr.N.Sharmili

Mrs.K.Suneetha

Mr.S.Sumahasan

Mr.A.Uday Kumar

Mrs.K.Rohini

Mr.V.Lakshmana Rao

Mr.K.Purushotham Naidu

Mrs.B.VijayaLakshmi

Mr.K.Nagaraju

Mr.G.Sankara Rao

Mrs.M.Swapna

Ms.Y.Sowmya

Ms.D.Indu

Mrs.V.Gowtami Annapurna

Mrs.K.N.Satya Chitra

Mr.B.Ramesh

Mr.A.Nagendra

Dr.Tushar Kanti Mishra

Mr.R.Satish Kumar

Grams: "TECHNOLOGY"
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Directorate of Academic & Planning

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KAKINADA-533003, Andhra Pradesh, INDIA
(Established by AP Government Act No. 30 of 2008)

Lr. No. JNTUK/DAP/AC/B. Tech/II Year/2019-20

Date: 10.07.2019

Dr. A. Mallikarjuna Prasad
M.E, Ph.D.,
Director, Academic Planning

To
All the Principals of Affiliated Colleges,
JNTUK, Kakinada

ACADEMIC CALENDAR FOR B.TECH II YEAR (2018 BATCH)

Description	From	To	Weeks
I SEMESTER			
Commencement of Class Work	**10.06.2019**		
I Unit of Instructions	10.06.2019	03.08.2019	8W
I Mid Examinations	05.08.2019	10.08.2019	1W
II Unit of Instructions	12.08.2019	05.10.2019	8W
II Mid Examinations	07.10.2019	12.10.2019	1W
Preparation & Practicals	14.10.2019	19.10.2019	1W
End Examinations	21.10.2019	02.11.2019	2W
Commencement of II Semester Class Work	**18.11.2019**		
II SEMESTER			
I Unit of Instructions	**18.11.2019**	11.01.2020	8W
I Mid Examinations	13.01.2020	23.01.2020	1W
II Unit of Instructions	24.01.2020	21.03.2020	8W
II Mid Examinations	23.03.2020	28-03-2020	1W
Preparation	30.03.2020	04.04.2020	1W
End Examinations	06.04.2020	18.04.2020	2W
Commence of III Year Class Work	**08.06.2020**		

A m placed

Director Academic Planning

Copy to the Secretary to the Hon'ble Vice Chancellor, JNTUK.
Copy to PA to the Rector, JNTUK.
Copy to PA to the Registrar, JNTUK.
Copy to PA to the Director of Evaluation, JNTUK.

GAYATRI VIDYA PARISHAD COLLEGE OF ENGINEERING FOR WOMEN

Madhurawada, Visakhapatnam

Calendar of Academic Activities-Planning

Academic Year: 2019-2020 II B.Tech. I Semester Branch: CSE

Month	Su	M	Tu	W	Th	F	Sa	Total Working Days	Activities
JUN							1		[illegible]
	2	3	4	5	6	7	8		[illegible]
	9	10	11	12	13	14	15	6	[illegible]
	16	17	18	19	20	21	22	6	Deepika Rani [illegible]
	23	24	25	26	27	28	29	6	Student [illegible]
	30								
JUL		1	2	3	4	5	6	6	10-7-19 Guest Lecture [illegible]
	7	8	9	10	11	12	13	5	13-7-19 Second Saturday
	14	15	16	17	18	19	20	6	24-7-19 [illegible]
	21	22	23	24	25	26	27	6	
	28	29	30	31				4	
AUG					1	2	3	3	5-8-19 to 10-8-19 Mid Exam
	4	5	6	7	8	9	10	6	12-8-19 Bakrid
	11	12	13	14	15	16	17	4	15-8-19 Independence Day
	18	19	20	21	22	23	24	5	23-8-19 Krishnashtami
	25	26	27	28	29	30	31	6	28-8-19 Class Committee Meeting
SEP	1	2	3	4	5	6	7	5	2-9-19 Vinayaka Chavithi
	8	9	10	11	12	13	14	4	10-9-19 Muharram
	15	16	17	18	19	20	21	6	12-9-19 Guest Lecture [illegible]
	22	23	24	25	26	27	28	6	14-9-19 Second Saturday
	29	30						1	25-9-19 Workshop on [illegible] 26-9-19 Class Committee Meeting
OCT			1	2	3	4	5	4	2-10-19 Gandhi Jayanthi
	6	7	8	9	10	11	12	6	7-10-19 to 12-10-19 Mid-2 exams
	13	14	15	16	17	18	19	0	14-10-19 to 19-10-19 JNTUK Lab External Exams
	20	21	22	23	24	25	26	0	21-10-19 to 2-11-19 JNTUK External End Exams
	27	28	29	30	31			0	
NOV						1	2	0	09-11-2019 Second Saturday
	3	4	5	6	7	8	9	0	
	10	11	12	13	14	15	16	0	
	17	18	19	20	21	22	23	0	
	24	25	26	27	28	29	30	0	
	Total Working days including JNTUK Internal Examinations							100	

- JNTUK MID Exams (green)
- JNTUK Lab External Examinations (pink)
- JNTUK External End Examinations (blue)
- Holidays (red)
- Guest Lectures (dark red)
- Meetings (yellow)

GAYATRI VIDYA PARISHAD COLLEGE OF ENGINEERING FOR WOMEN

Madhurawada, Visakhapatnam

Calendar of Academic Activities Carried Out

Academic Year: 2019-2020 — II B.Tech. I Semester — Branch: CSE

Month	Working Days (Su Mo Tu We Th Fr Sa)	Total Working Days	Activities
JUL			
AUG			
SEP			
OCT			
NOV			
	Total Working days including JNTUK Internal Examinations		

JNTUK MID Exams
JNTUK Lab External Examinations
JNTUK External End Examinations
Holidays
Guest Lectures
Meetings

Grams: "TECHNOLOGY"
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Directorate of Academic & Planning

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY KAKINADA
KAKINADA-533003, Andhra Pradesh, INDIA
(Established by AP Government Act No. 30 of 2008)

Lr. No: JNTUK/DAP/ACR Tech III Year/2019-20 Date: 10/06/2019

Dr. A. Mallikarjuna Prasad
M.E., Ph.D.,
Director, Academic Planning

To
All the Principals of Affiliated Colleges,
JNTUK, Kakinada

ACADEMIC CALENDAR FOR B.TECH III YEAR (2017 BATCH)

Description	From	To	Weeks
I SEMESTER			
Commencement of Class Work	**10.06.2019**		
I Unit of Instructions	10.06.2019	03.08.2019	8W
I Mid Examinations	05.08.2019	10.08.2019	1W
II Unit of Instructions	12.08.2019	05.10.2019	8W
II Mid Examinations	07.10.2019	12.10.2019	1W
Preparation & Practicals	14.10.2019	19.10.2019	1W
End Examinations	21.10.2019	02.11.2019	2W
Commencement of II Semester Class Work	**18.11.2019**		
II SEMESTER			
I Unit of Instructions	**18.11.2019**	11.01.2020	8W
I Mid Examinations	13.01.2020	23.01.2020	1W
II Unit of Instructions	24.01.2020	21.03.2020	8W
II Mid Examinations	23.03.2020	28-03-2020	1W
Preparation	30.03.2020	04.04.2020	1W
End Examinations	06.04.2020	18.04.2020	2W
Commence of IV Year Class Work	**08.06.2020**		

A m prasad
Director Academic Planning

Copy to the Secretary to the Hon'ble Vice Chancellor, JNTUK.
Copy to PA to the Rector, JNTUK.
Copy to PA to the Registrar, JNTUK.
Copy to PA to the Director of Evaluation, JNTUK.

GAYATRI VIDYA PARISHAD COLLEGE OF ENGINEERING FOR WOMEN

Madhurawada, Visakhapatnam

Calendar of Academic Activities-Planned

Academic Year: 2019-2020 **III B.Tech. I Semester** **Branch: CSE**

Month	Working Days							Total Working Days	Activities
	Su	M	Tu	W	Th	F	Sa		
JUN							1		
	2	3	4	5	6	7	8		
	9	10	11	12	13	14	15	6	
	16	17	18	19	20	21	22	6	
	23	24	25	26	27	28	29	6	
	30								
JUL		1	2	3	4	5	6	6	
	7	8	9	10	11	12	13	5	
	14	15	16	17	18	19	20	6	
	21	22	23	24	25	26	27	6	
	28	29	30	31				4	
								1	
AUG					1	2	3	1	
	4	5	6	7	8	9	10	6	
	11	12	13	14	15	16	17	4	
	18	19	20	21	22	23	24	5	
	25	26	27	28	29	30	31	6	
SEP	1	2	3	4	5	6	7	5	
	8	9	10	11	12	13	14	4	
	15	16	17	18	19	20	21	6	
	22	23	24	25	26	27	28	6	
	29	30						1	
OCT			1	2	3	4	5	4	
	6	7	8	9	10	11	12	6	
	13	14	15	16	17	18	19	0	
	20	21	22	23	24	25	26	0	
	27	28	29	30	31			0	
NOV						1	2	0	
	3	4	5	6	7	8	9	0	
	10	11	12	13	14	15	16	0	
	17	18	19	20	21	22	23	0	
	24	25	26	27	28	29	30	0	
	Total Working days including JNTUK Internal Examinations							100	

JNTUK MID Exams
JNTUK Lab External Examinations
JNTUK External End Examinations
Holidays
Guest Lectures
Meetings

GAYATRI VIDYA PARISHAD COLLEGE OF ENGINEERING FOR WOMEN

Madhurawada, Visakhapatnam

Calendar of Academic Activities-Carried out

Academic Year: 2019-2020 **III B.Tech. I Semester** **Branch: CSE**

Month	Working Days (Su M Tu W Th F Sa)	Total Working Days	Activities
JULY			
AUG			
SEP			
OCT			
NOV			
	Total Working days including JNTUK-Internal Examinations	100	

JNTUK MID Exams
JNTUK Lab External Examinations
JNTUK External End Examinations
Holidays
Guest Lectures
Meetings

Grams: "TECHNOLOGY"
Email: dapjntuk@gmail.com

Phone: 0884-2300991
Mobile: [illegible]

Directorate of Academic & Planning

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY: KAKINADA
KAKINADA-533003, Andhra Pradesh, INDIA
(Established by AP Government Act No. 30 of 2008)

Lr. No. JNTUK/DAP/AC/B. Tech/IV Year/2019-20

Date: [illegible]

Dr. A. Mallikarjuna Prasad
M.E, Ph.D.,
Director, Academic Planning

To
All the Principals of Affiliated Colleges,
JNTUK, Kakinada

ACADEMIC CALENDAR FOR B.TECH IV YEAR (2016 BATCH)

I SEMESTER			
Description	**From**	**To**	**Weeks**
Commencement of Class Work	**10.06.2019**		
I Unit of Instructions	10.06.2019	03.08.2019	8W
I Mid Examinations	05.08.2019	10.08.2019	1W
II Unit of Instructions	12.08.2019	05.10.2019	8W
II Mid Examinations	07.10.2019	12.10.2019	1W
Preparation & Practicals	14.10.2019	19.10.2019	1W
End Examinations	21.10.2019	02.11.2019	2W
Commencement of II Semester Class Work	**18.11.2019**		
II SEMESTER			
I Unit of Instructions	**18.11.2019**	11.01.2020	8W
I Mid Examinations	13.01.2020	23.01.2020	1W
II Unit of Instructions	24.01.2020	21.03.2020	8W
II Mid Examinations	23.03.2020	28.03.2020	1W
Preparation	30.03.2020	04.04.2020	1W
End Examinations	06.04.2020	18.04.2020	2W

A M Prasad
Director Academic Planning

Copy to the Secretary to the Hon'ble Vice Chancellor, JNTUK.
Copy to PA to the Rector, JNTUK.
Copy to PA to the Registrar, JNTUK.
Copy to PA to the Director of Evaluation, JNTUK.

GAYATRI VIDYA PARISHAD COLLEGE OF ENGINEERING FOR WOMEN

Madhurawada, Visakhapatnam

Calendar of Academic Activities-Planning

Academic Year: 2019-2020 IV B.Tech. I Semester **Branch: C SE**

Month	Working Days							Total Working Days	Activities
	Su	M	Tu	W	Th	F	Sa		
JUN							1		
	2	3	4	5	6	7	8		
	9	10	11	12	13	14	15	6	
	16	17	18	19	20	21	22	6	
	23	24	25	26	27	28	29	6	
	30								
JUL		1	2	3	4	5	6	6	
	7	8	9	10	11	12	13	5	
	14	15	16	17	18	19	20	6	
	21	22	23	24	25	26	27	6	
	28	29	30	31				3	
AUG					1	2	3	3	
	4	5	6	7	8	9	10	6	
	11	12	13	14	15	16	17	4	
	18	19	20	21	22	23	24	5	
	25	26	27	28	29	30	31	6	
SEP	1	2	3	4	5	6	7	5	
	8	9	10	11	12	13	14	4	
	15	16	17	18	19	20	21	6	
	22	23	24	25	26	27	28	6	
	29	30						2	
OCT			1	2	3	4	5	4	
	6	7	8	9	10	11	12	6	
	13	14	15	16	17	18	19	0	
	20	21	22	23	24	25	26	0	
	27	28	29	30	31			0	
NOV						1	2	0	09-10-2019 Second Saturday
	3	4	5	6	7	8	9	0	
	10	11	12	13	14	15	16	0	
	17	18	19	20	21	22	23	0	
	24	25	26	27	28	29	30	0	
	Total Working days including JNTUK Internal Examinations							100	

JNTUK MID Exams

JNTUK Lab External Examinations

JNTUK External End Examinations

Holidays

Guest Lectures

Meetings

Training& Placement Activities

GAYATRI VIDYA PARISHAD COLLEGE OF ENGINEERING FOR WOMEN

Madhurawada, Visakhapatnam

Calendar of Academic Activities Carried out

Academic Year: 2019-2020 IV B.Tech I Semester Branch: CSE

Month	Working Days (Su M Tu W Th F Sa)	Total Working Days	Activities
JULY		6	
		5	
		6	
		6	
AUG		5	
		6	
		4	
		5	
		6	
SEP		5	
		4	
		6	
		6	
		1	
OCT		4	
		6	
		0	
		0	
		0	
NOV		0	04-10-2019 Second Saturday
		0	
		0	
		0	
		0	
	Total Working days including JNTUK Internal Examinations	100	

JNTUK MID Exams

JNTUK Lab External Examinations

JNTUK External End Examinations

Holidays

Guest Lectures

Meetings

GAYATRI VIDYA PARISHAD COLLEGE OF ENGINEERING FOR WOMEN

Madhurawada::Visakhapatnam

Calendar of Academic Activities - Planned

I B.Tech. I Semester (2021-2022)

Date: 22-09-2021

Month	Working Days							Total Working Days	Activities
	Su	M	Tu	W	Th	F	Sa		
NOV		1	2	3	4	5	6		
	7	8	9	10	11	12	13		
	14	15	16	17	18	19	20		
	21	22	23	24	25	26	27		Student Induction Program
	28	29	30					2	29-11-2021 Commencement of Class Work
DEC	Su	M	Tu	W	Th	F	Sa		
				1	2	3	4	4	11-12-2021 Second Saturday
	5	6	7	8	9	10	11	5	13-12-2021 Guest Lecture 1
	12	13	14	15	16	17	18	6	18-12-2021 Class Committee meeting 1
	19	20	21	22	23	24	25	5	22-12-2021 National Mathematics Day
	26	27	28	29	30	31		5	25-12-2022 Christmas Day
JAN	Su	M	Tu	W	Th	F	Sa		
							1	1	
	2	3	4	5	6	7	8	5	08-01-2022 Second Saturday
	9	10	11	12	13	14	15	4	14-01-2022 to 16-01-2022 Pongal Holidays
	16	17	18	19	20	21	22	6	MID I EXAMS 17-01-2022 TO 22-01-2022
	23	24	25	26	27	28	29	5	26-01-2022 Republic Day
	30	32						1	
FEB	Su	M	Tu	W	Th	F	Sa		
			1	2	3	4	5	5	05-02-2022 Class Committee meeting 2
	6	7	8	9	10	11	12	6	07-02-2022 Chem Quiz (Periodic Table Day)
	13	14	15	16	17	18	19	6	12-02-2021 Second Saturday
	20	21	21	23	24	25	26	6	18-02-2022 Guest Lecture 2
	27	28						1	28-02-2022 National Science Day Celebrations
MAR	Su	M	Tu	W	Th	F	Sa		
			1	2	3	4	5	4	01-03-2021 Maha Shiva Ratri
	6	7	8	9	10	11	12	5	08-03-2022 International Women's Day
	13	14	15	16	17	18	19	5	11-03-2022 Class Committee meeting 3
	20	22	22	25	24	25	26	6	12-03-2022 Second Saturday
	27	28	29	30	31			4	18-03-2022 Holi
									14-03-2021 to 19-03-2021 Mid II Exams
									Preparation and Practicals
APR	Su	M	Tu	W	Th	F	Sa		
						1	2	2	JNTUK External Exams
	3	4	5	6	7	8	9	6	
	10	11	12	13	14	15	15		
	17	18	19	20	21	22	23		
	24	25	20	27	28	29	30		
	Total Working Days including JNTUK Internal and External							104	

	Commencement of classwork		Student Induction Program
	JNTUK MID-I Exams		Class Committee meeting
	JNTUK MID-II Exams		Special Events
	Preparation and Practicals		Guest Lectures
	JNTUK External Exams		Holiday

I YEAR CO-ORDINATOR

HEAD OF THE DEPARTMENT

HEAD
DEPARTMENT OF
BASIC SCIENCES & HUMANITIES
GVP COLLEGE OF ENGINEERING FOR WOMEN

GAYATRI VIDYA PARISHAD COLLEGE OF ENGINEERING FOR WOMEN

Madhurawada::Visakhapatnam

Calendar of Academic - Executed

I B.Tech. I Semester (2021-2022)

Date: 18-04-2022

Month	Working Days							Total Working Days	Activities
	Su	M	Tu	W	Th	F	Sa		
NOV		1	2	3	4	5	6		
	7	8	9	10	11	11	13		
	14	15	16	17	18	10	20		
	21	22	23	24	25	26	27		Student Induction Program
	28	29	30					2	29-11-2021 Commencement of Class Work
DEC	Su	M	Tu	W	Th	F	Sa		
				1	2	3	4	4	11-12-2021 Second Saturday
	5	6	7	8	9	10	11	5	17-12-2021 Guest Lecture - Stress Management
	12	13	14	15	16	17	18	6	22-12-2021 National Mathematics Day
	19	20	21	22	23	24	25	5	25-12-2022 Christmas Day
	26	27	28	29	30	31		5	
JAN	Su	M	Tu	W	Th	F	Sa		
							2	1	13-01-2022 to 16-01-2022 Pongal Holidays
	2	3	4	5	6	7	8	5	26-01-2022 Republic Day
	9	10	11	12	13	24	15	4	
	16	17	18	10	20	11	22	6	
	23	24	25	26	27	28	29	5	
	30	31						1	
FEB	Su	M	Tu	W	Tu	F	Sa		
			1	2	3	4	5	5	21-02-2022 Class Committee meeting 1
	6	7	8	9	10	11	17	5	12-02-2022 Second Saturday
	10	14	15	16	12	18	19	5	28-02-2021 National Science Day Celebrations & Guest Lecture
	20	21	22	22	24	25	16	5	
	27	28						1	
MAR	Su	M	Tu	W	Th	F	Sa		
			1	2	3	4	5	4	01-03-2022 Maha Shiva Ratri
	6	7	8	9	10	11	12	5	11-03-2022 Class Committee meeting 2
	13	14	15	16	17	16	19	5	12-03-2022 Second Saturday
	20	21	22	23	24	25	26	6	18-03-2022 Holi
	27	28	29	30	01			4	14-03-2021 to 29-03-2021 Mid I & II Exams
									30-03-2022 to 02-04-2022 Preparation and Practicals
APR	Su	M	Tu	W	Th	F	Sa		
						1	2	2	04-04-2022 to 16-04-2022 JNTUK External Exams
	3	4	5	6	7	8	9	6	
	10	11	12	13	14	15	16	6	
	17	10	19	20	21	22	25		
	24	25	26	27	20	29	30		
	Total Working Days Including JNTUK Internal and External							110	

	Commencement of classwork		Student Induction Program
	JNTUK MID-I Exams		Class Committee meeting
	JNTUK MID-II Exams		Special Events
	Preparation and Practicals		Guest Lectures
	JNTUK External Exams		Holiday

I YEAR CO-ORDINATOR

HEAD OF THE DEPARTMENT

HEAD
DEPARTMENT OF
BASIC SCIENCES & HUMANITIES
GVP COLLEGE OF ENGINEERING FOR WOMEN

GAYATRI VIDYA PARISHAD COLLEGE OF ENGINEERING FOR WOMEN
MADHURAWADA, VISAKHAPATNAM – 530048
(APPRDVED BY AICTE, NEW DELHI, AFFILIATED TO JNT UNIVERSITY, KAKINADA)

DEPARTMENT OF ECE
M.Tech (VLSI DESIGN & ES)

GVPCEW ACADEMIC CALENDAR FOR 2018-2020 BATCH

3RD & 4TH SEMESTERS

THIRD SEMESTER: 05/08/19 to 04/01/20

SL.NO.	EVENT DATE	REVIEW/SEMINAR
1.	07/09/19	PRC REVIEW (Acceptance of the project)
2.	16/09/19	COMPREHENSIVE VIVA-VOCE
3.	18/10/19 & 19/10/19	TECHNICAL SEMINAR- 1
4.	20/12/19 & 21/12/19	REVIEW-1

FOURTH SEMESTER: 06/01/20 to 06/06/20

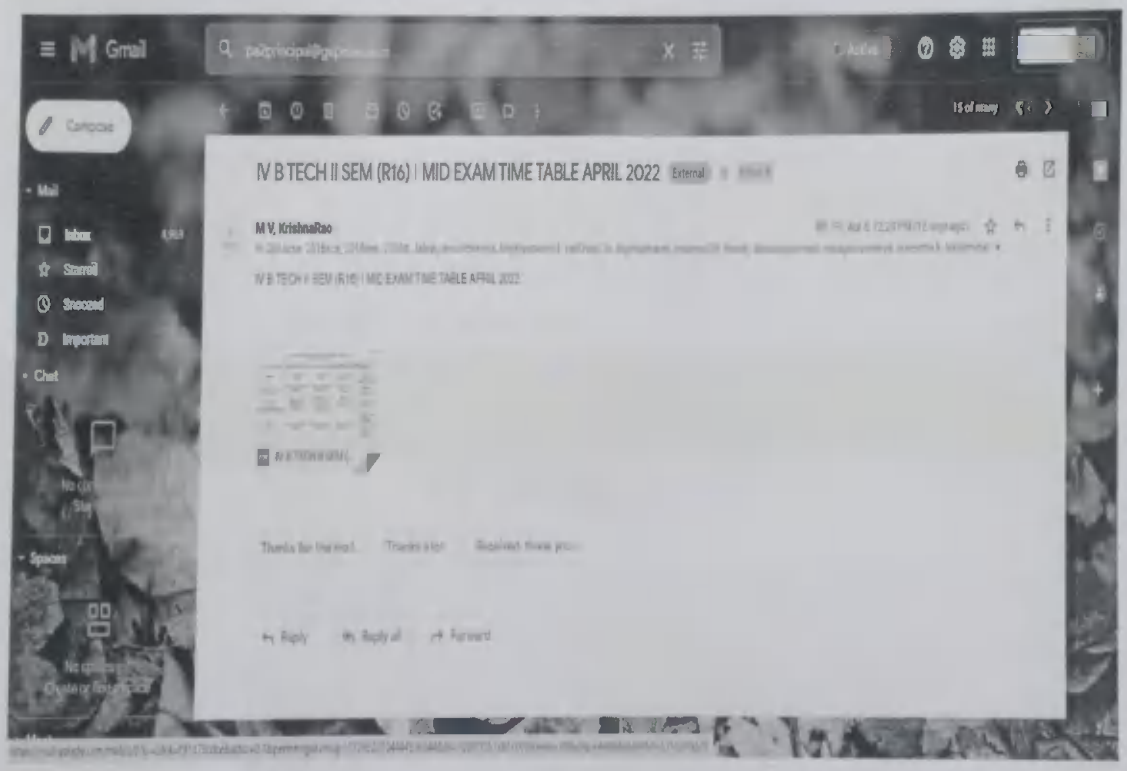
SL.NO.	EVENT DATE	REVIEW/SEMINAR
1.	24/01/20 & 25/01/20	TECHNINCAL SEMINAR-2
2.	20/03/20 & 21/03/20	REVIEW-2
3.	15/05/20 & 16/05/20	REVIEW-3
4.	08/06/20---29/08/20	THESIS SUBMISSION TO JNTUK

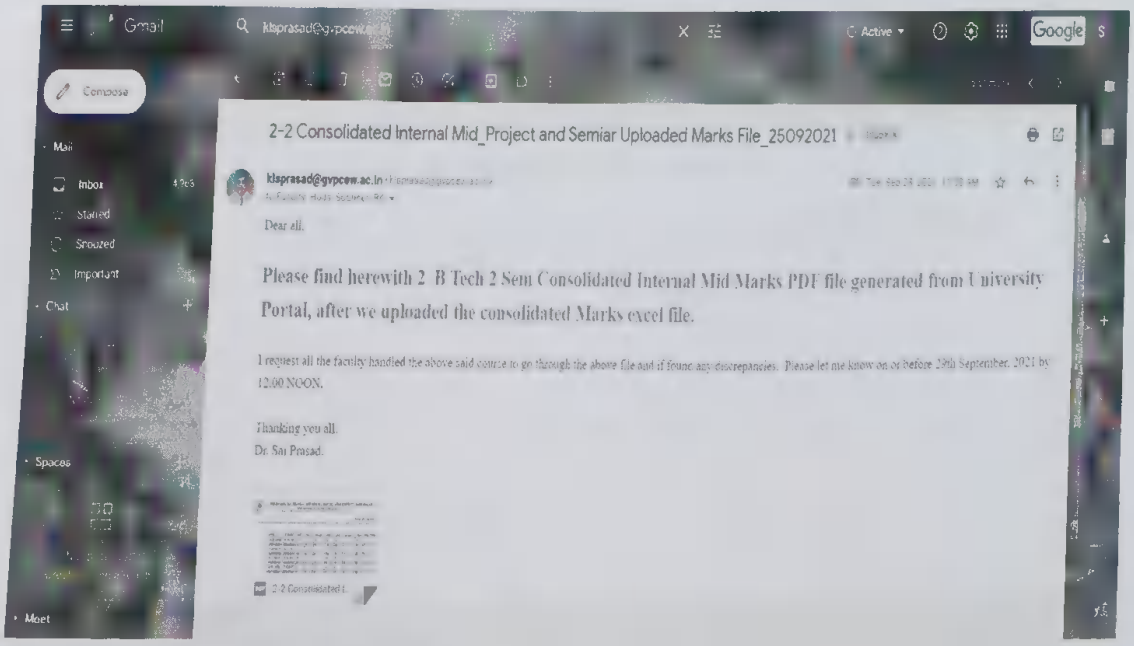
NOTE: All M.Tech students and faculty of E.C.E Department are requested to follow the Aeademic calendar.

M.Tech Co-ordinator

GAYATRI VIDYA PARISHAD COLLEGE OF ENGINEERING FOR WOMEN
(Approved by AICTE New Delhi, Affiliated to JNTUK Kakinada)
[Accredited by National Board of Accreditation(NBA) for B.Tech CSE, ECE & IT - Valid from 2019-20 to 2021-22]
Kommadi, Madhurawada, Visakhapatnam - 530 048
Phone: 91-891-2739144 / 2719124 / 2719125 / 2719127
Email Id: gvpcew@gmail.com, info@gvpcew.ac.in
Eamcet counselling code: GVPW

Sample Emails communicated to Faculty and Students regarding the Internal exam ,End exam time table and internal assessments

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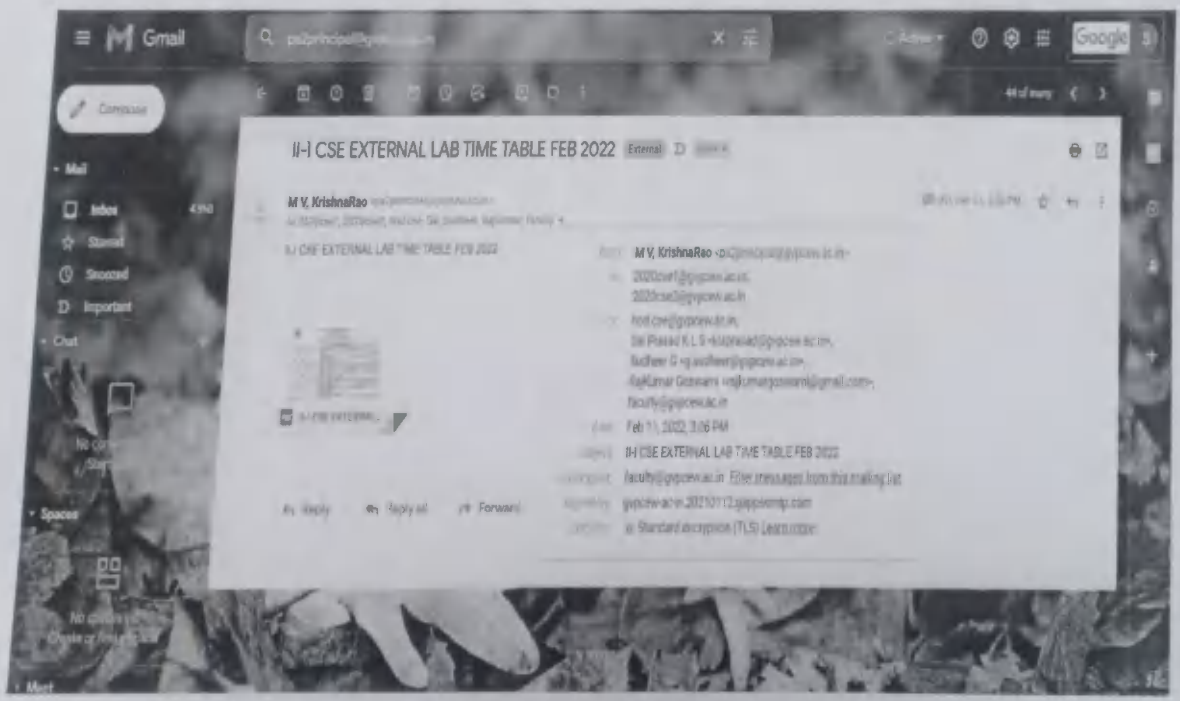
2-2 Consolidated Internal Mid_Project and Semiar Uploaded Marks File_25092021
klsprasad@gvpcew.ac.in
Dear all,
Please find herewith 2 B Tech 2 Sem Consolidated Internal Mid Marks PDF file generated from University Portal, after we uploaded the consolidated Marks excel file.
I request all the faculty handled the above said course to go through the above file and if found any discrepancies. Please let me know on or before 29th September, 2021 by 12.00 NOON.
Thanking you all.
Dr. Sai Prasad.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II-I CSE EXTERNAL LAB TIME TABLE FEB 2022
External
M V, KrishnaRao
II-I CSE EXTERNAL LAB TIME TABLE FEB 2022
Feb 11, 2022, 3:06 PM
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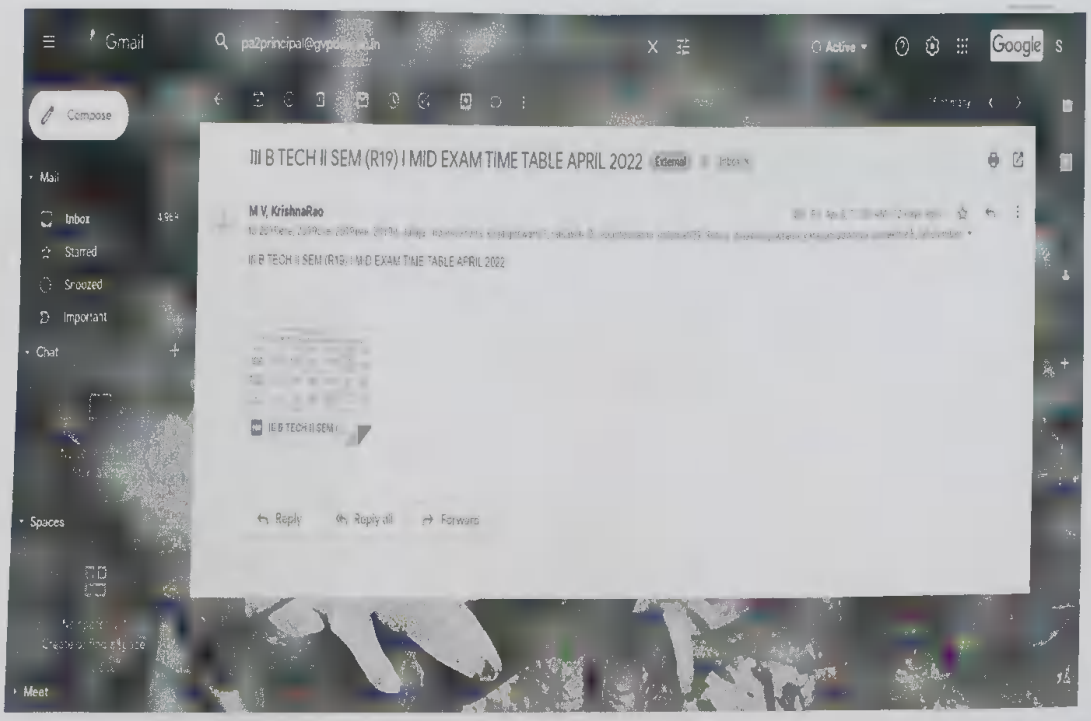
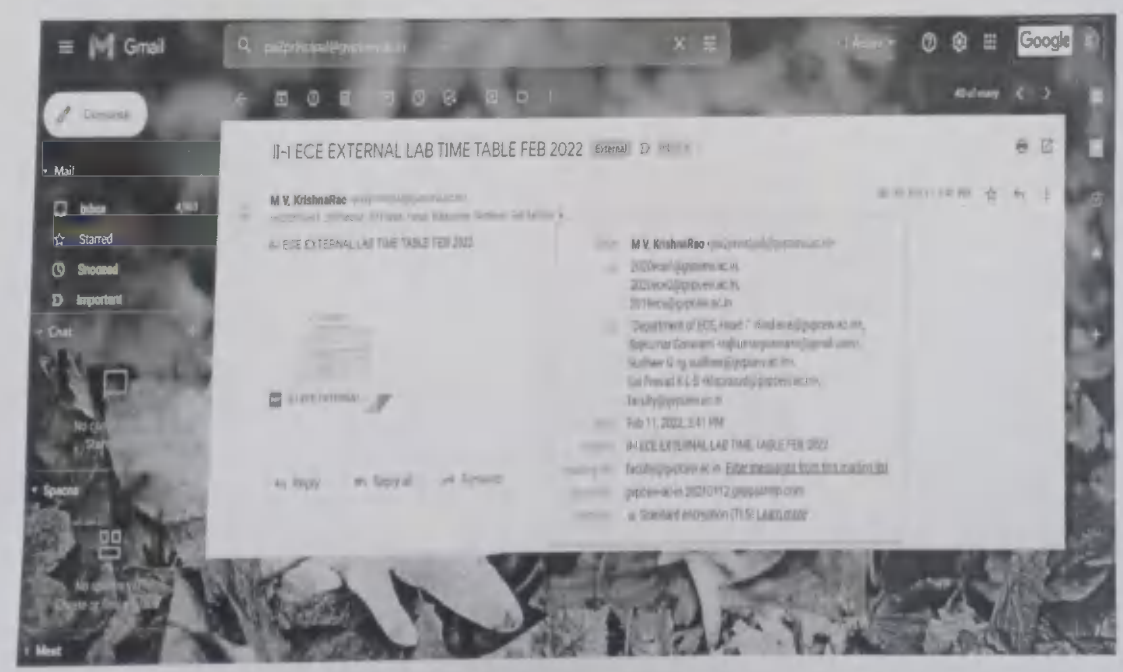
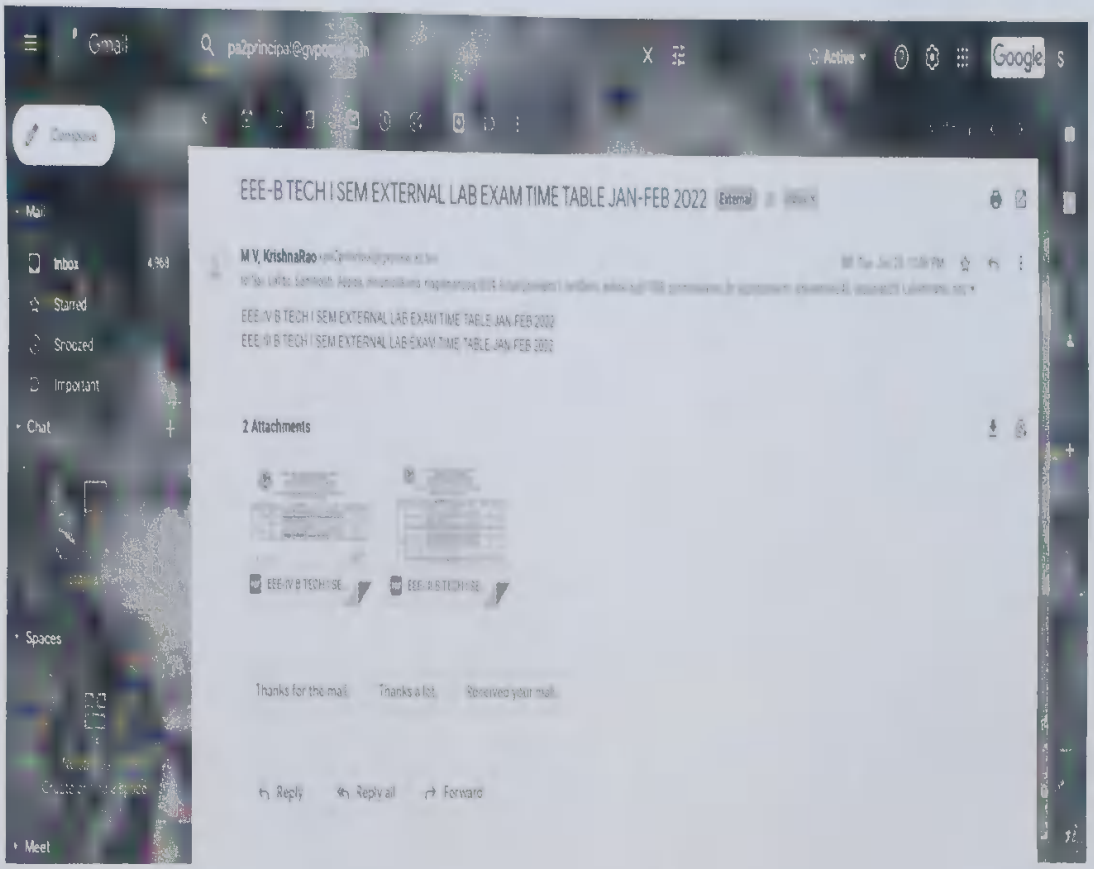
GVP COLLEGE OF ENGG FOR WOMEN
VISAKHAPATNAM



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EEE-B TECH I SEM EXTERNAL LAB EXAM TIME TABLE JAN-FEB 2022
External
M V, KrishnaRao
EEE-IV B TECH I SEM EXTERNAL LAB EXAM TIME TABLE JAN-FEB 2022
EEE-III B TECH I SEM EXTERNAL LAB EXAM TIME TABLE JAN-FEB 2022
2 Attachments
Thanks for the mail.
Thanks a lot.
Received your mail.
Reply
Reply all
Forward


COLLEGE OF ENGG FOR WOMEN

GAYATRI VIDYA PARISHAD COLLEGE OF ENGINEERING FOR WOMEN
Madhurawada :: Visakhapatnam – 530 048

I B.TECH - I SEMESTER – II MID (R20) EXAMINATION

CONSOLIDATED SEATING PLAN – MARCH,2022

DESCRIPTIVE EXAM TIMINGS
10.30 AM TO 12.00 NOON

ROOM NO.	BRANCH	ROLL NOS	TOTAL NOS	GRAND TOTAL
101	CSE-1	21JG1A0501 TO 21JG1A0524	24	72
	ECE-1	21JG1A0401 TO 21JG1A0424-	24	
	IT	21JG1A1201 TO 21JG1A1225 (Except 21JG1A1221)	24	
102	CSE-1	21JG1A0525 TO 21JG1A0548	24	72
	ECE-1	21JG1A0425 TO 21JG1A0448	24	
	IT	21JG1A1226 TO 21JG1A1250	24	
104	CSE-1	21JG1A0549 TO 21JG1A0566	18	72
	CSE-2	21JG1A0567 TO 21JG1A0572	06	
	ECE-1	21JG1A0449 TO 21JG1A0463	15	
	ECE-2	21JG1A0464 TO 21JG1A0472	09	
	CSE (AIML)	21JG1A4201 TO 21JG1A4211	11	
	IT	21JG1A1251 TO 21JG1A1263 (Except 21JG1A1245)	13	
105	CSE-2	21JG1A0573 TO 21JG1A0596	24	72
	ECE-2	21JG1A0473 TO 21JG1A0496	24	
	CSE (AIML)	21JG1A4212 TO 21JG1A4235	24	
116	CSE-2	21JG1A0597 TO 21JG1A0599 & 21JG1A05A0 TO 21JG1A05A9 & 21JG1A05B0 TO 21JG1A05B6	20	51
	ECE-2	21JG1A0497 TO 21JG1A0499 & 21JG1A04A0 TO 21JG1A04A9 & 21JG1A04B0 AND 21JG1A04B1	15	
	CSE (AIML)	21JG1A4236 TO 21JG1A4251	16	
117	CSE-2	21JG1A05B7 TO 21JG1A05B9 & 21JG1A05C0 TO 21JG1A05C9 & 21JG1A05D0	14	48
	EEE	21JG1A0201 TO 21JG1A0222	22	
	CSE (AIML)	21JG1A4252 TO 21JG1A4263	12	

INCHARGE EXAMINATIONS

IN-CHA. GE EXAMINATIONS
G. V. P. College of Engineering for Women
VIS... VISAKHAPATNAM

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY KAKINADA

UNIVERSITY EXAMINATION CENTER, KAKINADA

I B. TECH II SEMESTER (R20 REGULATION) I MID & ONLINE QUIZ EXAMINATIONS, AUGUST/SEPTEMBER - 2021

TIME TABLE

TIME: 10.00 AM TO 12.00 NOON

Branch	31-08-2021 (Tuesday)	01-09-2021 (Wednesday)	02-09-2021 (Thursday)	03-09-2021 (Friday)	04-09-2021 (Saturday)	06-09-2021 (Monday)
Subjects	Mathematics – II **R201201**	Building Materials and Concrete Technology **R201205** (Only for CE) Applied Physics **R201207** (Comm to EEE, ECE, EIE, ECT, CSE-AI&ML, CSE-AI, CSE-DS, CSE-AI&DS, AI&DC) Basic Electrical and Electronics Engineering **R201211** (Comm to ME,AME, Mining,PE,FE,Phar.E) Elements of Mechanical Engineering **R201223** (Only for PE)	Programming for Problem Solving Using C **R201204** (Comm to CE, Agri E) Metallurgy & Materials Science **R201256** (Only for AME) Data Structures Through C **R201208** (Only for EEE) Basic Electrical Engineering **R201214** (Comm to ECE, EIE, ECT) Computer Organization **R201216** (Comm to CSE,IT) Digital Logic Design **R201221** (Comm to CSE-CS&T, CSE-AI&ML, CSE-AI, CSE-DS, CSE-AI&DS, CSE-CS, CSE-IOT&CS INCL BCT, CSE-CS&BS,CSE-IOT, AI&DS, Cyber Security) Engineering Physics **R201222** (Comm. to AME, Mining, PE,FE)	Data Structures **R201218** (Comm to CSE, IT, CSE-AI&ML, CSE-AI, CSE-DS, CSE-AI&DS, AI&DS) Problem Solving Using Python **R201219** (Comm to CSE-CS&T, CSE-CS, CSE-IOT&CS Incl BCT, CSE-CS&BS, CSE-IOT, Cyber Security) Pharmaceutical Chemistry **R201258** (Only for Pharm. E) Thermodynamics **R201254** (Only for ME) Electrical Circuit Analysis – I **R201209** (Only for EEE) Engineering Mechanics **R201210** (Comm to ME, PE, Agri E, FE) Mechanics of Solids **R201255** (Only for Mining)	Engineering Mechanics **R201203** (Only for CE) Basic Civil and Mechanical Engineering **R201227** (Only for EEE) Computer Aided Engineering Drawing **R201226** (Only for FE) Engineering Drawing **R201224**(Comm to Mining,Agri.E,Phar.E) Object Oriented Programming through Java **R201212** (Comm to ECE,EIE, ECT) Basic Electrical & Electronics Engineering **R201220** (Comm to CSE-CS&T, CSE-CS, CSE-IOT&CS Incl BCT, CSE-CS&BS, CSE-IOT, Cyber Security) Engineering Graphics **R201257** (Only for AME) Python Programming **R201225** (Comm to CSE, IT ,CSE-AI&ML,CSE-AI, CSE-DS, CSE-AI&DS, AI&DS)	Mathematics-III **R201206** (Only for EEE) Engineering Chemistry **R201202** (Comm. to CE,AME ,Agri.E) Applied Chemistry **R201215** (Comm to CSE, CSE-CS&T, IT, CSE-CS, CSE-IOT&CS Incl BCT, CSE-CS&BS, CSE-IOT, Cyber Security) Network Analysis **R201213** (Comm to ECE,EIE, ECT) Engineering and Solid Mechanics **R201259** (Only for Pharm. E)

NOTE:

i. ANY OMISSIONS OR CLASHES IN THIS TIME TABLE MAY PLEASE BE INFORMED TO THE CONTROLLER OF EXAMINATIONS IMMEDIATELY.

ii. EVEN IF GOVERNMENT DECLARES HOLIDAY ON ANY OF THE ABOVE DATES, THE EXAMINATIONS SHALL BE CONDUCTED AS USUAL.

iii. THE PRINCIPALS ARE REQUESTED TO INFORM THE UNIVERSITY ANY OTHER SUBSTITUTE SUBJECTS THAT ARE NOT INCLUED IN THE ABOVE TIME TABLE IMMEDIATELY.

DATE: 11-08-2021

Controller of Examiuations

GAYATRI VIDYA PARISHAD COLLEGE OF ENGINEERING FOR WOMEN
MADHURAWADA, VISAKHAPATNAM -48
I-B.TECH-I SEMESTER REGULAR EXTERNAL LAB EXAM TIMETABLE, MARCH/APRIL-2022

CLASS	DATE	TIMINGS	LAB NAME	ROLL NUMBERS		EXCEPT NOS.	TOTAL	ROOM	FACULTY
				FROM	TO				
I - IT	29/3/2022	12 NOON - 3 : 00 PM	CE WORKSHOP	21JG1A1201	21JG1A1263	21JG1A1221& 1245	61	PROJECTS LAB	G. APPAJEE/ A BINDU
	30/3/2022	12 NOON - 3 : 00 PM	PPSC LAB	21JG1A1201	21JG1A1263	21JG1A1221& 1245	61	PROJECTS LAB	C.SRINIVAS/ R SRIDEVI
	31/3/2022	8 : 30 - 11 : 30 AM	ENGLISH.C.S. LAB	21JG1A1201	21JG1A1263	21JG1A1221& 1245	61	ENGLISH LAB	Dr. V. USHA RAMANI
	1/4/2022	8 : 30 - 11 : 30 AM	APP. PHYSICS	21JG1A1201	21JG1A1232	21JG1A1221	31	PHYSICS LAB	V.S.JAHNAVY/ DR. CH.S. LAKSHMI
	1/4/2022	12 NOON - 3 : 00 PM	APP. PHYSICS	21JG1A1233	21JG1A1263	21JG1A1245	30	PHYSICS LAB	V.S.JAHNAVY/ DR. CH.S. LAKSHMI
I CSE-I	29/3/2022	8 : 30 - 11 : 30 AM	ENGLISH.C.S. LAB	21JG1A0501	21JG1A0566		66	ENGLISH LAB	Dr. V. USHA RAMANI
	30/3/2022	8 : 30 - 11 : 30 AM	APP. PHYSICS	21JG1A0501	21JG1A0533		33	PHYSICS LAB	VVVS NARAYANA/ Dr CH.S. LAKSHMI
	30/3/2022	12 NOON - 3 : 00 PM	APP. PHYSICS	21JG1A0534	21JG1A0566		33	PHYSICS LAB	VVVS NARAYANA/ Dr CH.S. LAKSHMI
	31/3/2022	12 NOON - 3 : 00 PM	PPSC LAB	21JG1A0501	21JG1A0566		66	PROJECTS LAB	KINJAL GOSWAMI/ DR. N.SHARMILI
	1/4/2022	12 NOON - 3 : 00 PM	CE WORKSHOP	21JG1A0501	21JG1A0566		66	PROJECTS LAB	G.APPAJEE/ A BINDU
I CSE-II	29/3/2022	8 : 30 - 11 : 30 AM	PPSC LAB	21JG1A0567	21JG1A05D0		64	PROJECTS LAB	K.VS MOUNIKA/ G.DEEPTHI
	30/3/2022	8 : 30 - 11 : 30 AM	CE WORKSHOP	21JG1A0567	21JG1A05D0		64	PROJECTS LAB	G. APPAJEE/ A BINDU
	31/3/2022	8 : 30 - 11 : 30 AM	APP. PHYSICS	21JG1A0567	21JG1A0598		32	PHYSICS LAB	Dr CH.S. LAKSHMI/ VVVS NARAYANA
	31/3/2022	12 NOON - 3 : 00 PM	APP. PHYSICS	21JG1A0599	21JG1A05D0		32	PHYSICS LAB	Dr CH.S. LAKSHMI/ VVVS NARAYANA
	1/4/2022	12 NOON - 3 : 00 PM	ENGLISH.C.S. LAB	21JG1A0567	21JG1A05D0		64	ENGLISH LAB	Dr. V. USHA RAMANI

INCHARGE- EXAMINATIONS 26/3/2022

IN-CHA..GE EXAMINATIONS
G V P. College of Engineering for Wome
VIS' 'IAPATNAM - 48

PRINCIPAL

PRINCIPAL
GVP College of Engineering for Women
Madhurawada, Visakhapatnam-48

GAYATRI VIDYA PARISHAD COLLEGE OF ENGINEERING FOR WOMEN

Madhurawada::Visakhapatnam-530048

IV B TECH - II SEMESTER (R16) I MID EXAMINATIONS - APRIL, 2022

DATE OF EXAMINATIONS : 11-04-2022,12-04-2022,13-04-2022&16-04-2022

BRANCH	STUDENT STRENGTH	ON LINE (OBJECTIVE) EXAM TIMINGS	NAME OF THE LAB	THEORY TIMINGS (DESCRIPTIVE)
CSE - 1	56	11.15 AM to 11.40 AM	LAB-1, LAB-2, LAB-3 & LAB-4 (IT DEPT)	09.15 AM - 10.45 AM
CSE - 2	54		LAB-1, LAB-2 (CSE DEPT)	
ECE - 1	59	11.40 AM to 12.05 PM	LAB-1, LAB-2, LAB-3 & LAB-4 (IT DEPT)	
ECE - 2	61		LAB-1, LAB-2 (CSE DEPT)	
EEE	48	12.05 PM to 12.30 PM	LAB-1, LAB-2, LAB-3 & LAB-4 (IT DEPT)	
IT	46		LAB-1, LAB-2 (CSE DEPT)	

INCHARGE EXAMINATIONS

IN-CHA. GE EXAMINATIONS
G-V P. College of Engineering for Women
VIS HAPATNAM - 48

PRINCIPAL

PRINCIPAL
G.V.P College of Engineering for Women
Madhurawada, Visakhapatnam-48

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY KAKINADA

EXAMINATION BRANCH

19-03-2022

I B.TECH-I SEMESTER(R20)REGULAR & SUPPLEMENTARY EXAMINATIONS

TIME TABLE - APRIL,2022 - TIMINGS: 10.00 AM – 01.00 PM

BRANCH	04-04-2022 MONDAY	06-04-2022 WEDNESDAY	08-04-2022 FRIDAY	11-04-2022 MONDAY	13-04-2022 WEDNESDAY	16-04-2022 SATURDAY
Electrical And Electronics Engineering	COMMUNICATE ENGLISH (R201102)	MATHEMATICS-I (R201101)	PROGRAMMING FOR PROBLEM SOLVING USING C (R201110)	ENGG DRAWING & DESIGN (R201111)	-----	MATHEMATICS-II (R201109)
Electronics And Communications Engineering	COMMUNICATE ENGLISH (R201102)	MATHEMATICS-I (R201101)	PROGRAMMING FOR PROBLEM SOLVING USING C (R201110)	ENGG DRAWING (R201104)	-----	APPLIED CHEMISTRY (R201115)
Computer Science And Engineering	COMMUNICATE ENGLISH (R201102)	MATHEMATICS-I (R201101)	PROGRAMMING FOR PROBLEM SOLVING USING C (R201110)	-----	APPLIED PHYSICS (R201117)	-----
Information Technology	COMMUNICATE ENGLISH (R201102)	MATHEMATICS-I (R201101)	PROGRAMMING FOR PROBLEM SOLVING USING C (R201110)	-----	APPLIED PHYSICS (R201117)	-----
CSM (AI ML)	COMMUNICATE ENGLISH (R201102)	MATHEMATICS-I (R201101)	PROGRAMMING FOR PROBLEM SOLVING USING C (R201110)	-----	-----	APPLIED CHEMISTRY (R201115)

29/3/2022

INCHARGE EXAMINATIONS

IN-CHARGE EXAMINATIONS
G.V.P. College of Engineering for Women
VISAKHAPATNAM - 48

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GVP College of Engineering for Women
Madhurawada, Visakhapatnam-48

GAYATRI VIDYA PARISHAD COLLEGE OF ENGINEERING FOR WOMEN
Madhurawada :: Visakhapatnam – 530 048

CONSOLIDATED SEATING PLAN

DAY	MONDAY
DATE	18/04/2022
TIMINGS	10.00 am–01.00 pm

I B.TECH - I SEMESTER–SUPPLEMENTARY (R16) EXAMINATIONS
APRIL-2022

SUBJECT	BRANCH	ROOM NO	FROM	TO	NOS	TOTAL NOS
ENGG DRAWING (R161112)	CSE	318 (D.H)	16JG1A0532 &	16JG1A0545 &	08	14
			16JG1A0553 &	16JG1A0595 &		
			17JG1A0555 &	18JG1A0502 &		
			18JG1A0520 &	18JG1A0535		
	IT		16JG1A1218 &	17JG1A1219 &	04	
			17JG1A1238 &	18JG1A1215		
ENGG DRAWING (R161113)	ECE		18JG1A0444 &	18JG1A0452	02	

for CHIEF SUPERINTENDENT

GAYATRI VIDYA PARISHAD COLLEGE OF ENGINEERING FOR WOMEN

Madhurawada::Visakhapatnam-530048

IV B TECH - II SEMESTER (R16) I MID EXAMINATIONS - APRIL, 2022

DATE OF EXAMINATIONS : 11-04-2022,12-04-2022,13-04-2022&16-04-2022

BRANCH	STUDENT STRENGTH	ON LINE (OBJECTIVE) EXAM TIMINGS	NAME OF THE LAB	THEORY TIMINGS (DESCRIPTIVE)
CSE - 1	56	11.15 AM to 11.40 AM	LAB-1, LAB-2, LAB-3 & LAB-4 (IT DEPT)	09.15 AM - 10.45 AM
CSE - 2	54		LAB-1, LAB-2 (CSE DEPT)	
ECE - 1	59	11.40 AM to 12.05 PM	LAB-1, LAB-2, LAB-3 & LAB-4 (IT DEPT)	
ECE - 2	61		LAB-1, LAB-2 (CSE DEPT)	
EEE	48	12.05 PM to 12.30 PM	LAB-1, LAB-2, LAB-3 & LAB-4 (IT DEPT)	
IT	46		LAB-1, LAB-2 (CSE DEPT)	

INCHARGE EXAMINATIONS

IN-CHA. GE EXAMINATIONS
G. V P. College of Engineering for Wome
VIS IAPATNAM - 48

PRINCIPAL

PRINCIPAL
GVP College of Engineering for Women
Madhurawada, Visakhapatnam-48

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY KAKINADA

UNIVERSITY EXAMINATION CENTER, KAKINADA

II B TECH - I SEMESTER (R19 REGULATION) SUPPLEMENTARY EXAMINATIONS, SEPTEMBER - 2021

T I M E T A B L E

T I M E: 10.00 AM TO 01.00 PM

BRANCH	DAY AND DATE					
	01-09-2021 (Wednesday)	03-09-2021 (Friday)	06-09-2021 (Mnnday)	08-09-2021 (Wednesday)	11-09-2021 (Saturday)	14-09-2021 (Tnesday)
CIVIL ENGINEERING (01-CE)	Complex Variables and Statistical Methods **R1921011**	Strength of Materials-I **R1921012**	Fluid Mechanics **R1921013**	Surveying and Geometrics **R1921014**	Building Materials, Construction and Pianning **R1921015**	Transportation Engineering-I **R1921016**
ELECTRICAL AND ELECTRONICS ENGINEERING (02-EEE)	Electrical Circuit Analysis - II **R1921021**	Electrical Machines-I **R1921022**	Electronic Devices and Circuits **R1921023**	Electro Magnetic Fields **R1921024**	Thermal and Hydro Prime movers **R1921025**	Managerial Economics & Financial Analysis **R1921026** (Common to EEE,ECE,EIE,MM)
MECHANICAL ENGINEERING (03-ME)	Vector Calculus & Fourier Transforms **R1921031**	Mechanics of Solids **R1921032** (Commou to ME,AME)	Material Science & Metallurgy **R1921033**	Production Technology **R1921034**	Thermodynamics **R1921035** (Common to ME,AME)	Machine Drawing **R1921036**
ELECTRONICS & COMMUNICATION ENGINEERING (04-ECE)	Electronic Devices and Circuits **R1921041** (Common to ECE,EIE)	Switching Theory and Logic Design **R1921042** (Common to ECE,EIE)	Signals and Systems **R1921043** (Common to ECE,EIE)	Random Variables and Stochastic Processes **R1921044**	Object Oriented Programming through Java **R1921045**	Managerial Economics & Financial Analysis **R1921026** (Common to EEE,ECE,EIE,MM)
COMPUTER SCIENE & ENGINEERING (05-CSE)	Mathematical Foundations of Computer Science **R1921051**	Software Engineering **R1921052**	Python Programming **R1921053** (Common to CSE,IT)	Data Structures **R1921054** (Common to CSE,IT)	Computer Organization **R1921056** (Comm to CSE,IT)	Object Oriented Programming through C++ **R1921055**

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY KAKINADA
UNIVERSITY EXAMINATION CENTER, KAKINADA

B.TECH END EXAMINATIONS

NOTIFICATION

Notification No. B.Teoh/IV-II/Adv-Supple/Sep-2021

R16 REGULATIONS

IV B.TECH II SEMESTER ADVANCED SUPPLEMENTARY EXAMINATIONS
(For 2016 Admitted Batches onwards and 2017 Lateral Admitted Batches onwards)

R13 REGULATIONS

IV B.TECH II SEMESTER ADVANCED SUPPLEMENTARY EXAMINATIONS
(For 2013 Admitted Batches onwards and 2014 Lateral Admitted Batches onwards)

R10 REGULATIONS

IV B.TECH II SEMESTER ADVANCED SUPPLEMENTARY EXAMINATIONS
(For 2011 Admitted Batches onwards and 2013 Lateral Admitted Batches onwards)

SEPTEMBER – 2021

CANDIDATES APPEARING FOR THE ABOVE EXAMINATIONS ARE INFORMED THAT THE APPLICATIONS WILL BE RECEIVED AS PER THE TIME SCHEDULE GIVEN BELOW:

EXAM REGISTRATION	LAST DATE
Without Late Fee	23-09-2021
With Late Fee of Rs.100/-	24-09-2021
With Late fee of Rs.1000/-	25-09-2021

*** (Application to be submitted at JNTU Kakinada) ***

EXAMINATION FEE

[A] FOR WHOLE SEMESTER EXAMINATION (ALL SUBJECTS)	Rs. 770/-
[B] FOR ONE SUBJECT (THEORY/PRACTICAL)	Rs. 265/-
FOR TWO SUBJECTS (THEORY/PRACTICAL)	Rs. 390/-
FOR THREE SUBJECTS (THEORY/PRACTICAL)	Rs. 515/-
FOR FOUR AND ABOVE SUBJECTS (THEORY/PRACTICAL)	Rs. 770/-

Detailed Time Table for theory Examinations will notified in the J.N.T. University. Kakinada website.

www.jntuk.edu.in www.jntukexams.net

Note: (i) Principals are requested to verify the eligibility of the candidates for registration for examination in respect of malpractice/ court cases/ credits.

(ii) Hall Tickets are to be issued by the Principal only to the eligible candidates who fulfill the academic requirements of the University. **The Principals are requested to inform the students that mere payment of examination fee does not guarantee eligibility for appearing for examination.**

Cont.....2

1. Applications should enclose the xerox copies of latest marks memos containing details of failed subjects along with the applications.

2. Applications can be obtained at the office of the respective Principal's of the respective colleges and duly filled in applications should be handed over in the respective college office with necessary fee.

3. The Principals are requested to submit the filled in Applications to the Controller of Examinations, J.N.T. University Kakinada as per the schedule given below.

4. **Laboratory/Project Examinations schedule will be communicated later.**

5. All the above Examinations will be conducted in jumbling system.

6. The Payment of Total Examination Fee (**Without Fine/Rs. 100 Fine/Rs. 1000 Fine**) is to be made through Online Link provided in the portal from **27.09.2021 to 28.09.2021.** After Payment a "Payment Form" will be generated which is to be printed.

7. The College has to Submit the printed Paym13ent Form and filled in student application along with attendance Report **on 29.09.2021 to 30.09.2021** without fail as detailed below.

Applications are to be submitted on the following Dates:

1 **East Godavari, Wost Godavari, Visakhapatnam and Krishna Districts on 29.09.2021 (Wednesday).**

2 **Vizianagaram, Srikakulam, Gnntur and Prakasham Districts on 30.09.2021 (Thursday).**

DIRECTOR OF EVALUATION

DATE: 06-09-2021

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY EXAMINATION BRANCH: KAKINADA

IV B.TECH – I SEMESTER (R10) ADVANCED SUPPLEMENTARY EXAMINATIONS
(2012 REGULAR BATCH ONLY)

IV B.TECH – I SEMESTER (R13) ADVANCED SUPPLEMENTARY EXAMINATIONS
(2014 & 2015 REGULAR BATCHES ONLY)

IV B.TECH – I SEMESTER (R16) ADVANCED SUPPLEMENTARY EXAMINATIONS
(2016,2017 & 2018 REGULAR & LATERAL ENTRY BATCHES)

EXAMINATIONS – NOTIFICATION

CANDIDATES APPEARING FOR THE ABOVE EXAMINATIONS
ARE INFORMED THAT THE APPLICATIONS WILL BE RECEIVED AS PER THE TIME SCHEDULE GIVEN BELOW:

EXAM REGISTRATION	LAST DATE	TIMINGS
WITHOUT LATE FEE	**04-05-2022**	**BY 03.00 PM**
WITH LATE FEE OF RS.100/-	**06-05-2022**	**BY 03.00 PM**
WITH LATE FEE OF RS.1000/-	**07-05-2022**	**BY 03.00 PM**

EXAMINATION FEE

(A)	FOR WHOLE SEMESTER EXAMINATION (ALL SUBJECTS)	Rs.770/-
(B)	FOR ONE SUBJECT (THEORY/PRACTICAL)	Rs.265/-
	FOR TWO SUBJECTS (THEORY/PRACTICAL)	Rs.390/-
	FOR THREE SUBJECTS (THEORY/PRACTICAL)	Rs.515/-
	FOR FOUR AND ABOVE SUBJECTS(THEORY/PRACTICAL)	Rs.770/-

18/4/2022

INCHARGE EXAMINATIONS

IN-CHARGE EXAMINATIONS
G V P. College of Engineering for Women
VISAKHAPATNAM

PRINCIPAL

PRINCIPAL
GVP College of Engineering for Women
Madhurawada, Visakhapatnam

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QUIZ/MID MARKS REPORT		
JG:R16:1:1:4:A:IR161110:1:M		
SNO	Hallticket	Marks
1	18JG1A0401	5
2	18JG1A0402	10
3	18JG1A0403	13
4	18JG1A0404	8
5	18JG1A0405	6
6	18JG1A0406	1
7	18JG1A0407	10
8	18JG1A0408	14
9	18JG1A0409	15
10	18JG1A0410	11
11	18JG1A0411	12
12	18JG1A0412	8
13	18JG1A0413	3
14	18JG1A0414	14
15	18JG1A0415	11
16	18JG1A0416	12
17	18JG1A0417	5
18	18JG1A0418	6
19	18JG1A0419	8
20	18JG1A0420	11
21	18JG1A0421	12
22	18JG1A0422	8
23	18JG1A0423	15
24	18JG1A0424	14
25	18JG1A0425	3
26	18JG1A0426	12
27	18JG1A0427	12
28	18JG1A0428	13
29	18JG1A0429	14
30	18JG1A0430	11
31	18JG1A0431	15
32	18JG1A0432	5
33	18JG1A0433	10
34	18JG1A0434	8
35	18JG1A0435	11
36	18JG1A0436	5
37	18JG1A0437	11
38	18JG1A0438	15
39	18JG1A0439	15

I B.Tech-I Sem(R16)

I Mid Marks

Sub: 14-II (MMCV)

Br. ECE-I

Verified
B. Bharathi
1/9/18

32

40	18JG1A0440	15
41	18JG1A0441	8
42	18JG1A0442	15
43	18JG1A0443	13
44	18JG1A0444	3
45	18JG1A0445	7
46	18JG1A0446	2
47	18JG1A0447	12
48	18JG1A0448	11
49	18JG1A0449	11
50	18JG1A0450	1
51	18JG1A0451	14
52	18JG1A0452	2
53	18JG1A0453	11
54	18JG1A0454	8
55	18JG1A0455	7
56	18JG1A0456	8
57	18JG1A0457	6
58	18JG1A0458	13
59	18JG1A0459	9
60	18JG1A0460	8

Class Committee meetings

Proforma

GAYATRI VIDYA PARISHAD COLLEGE OF ENGINEERING FOR WOMEN
Madhurawada :: Visakhapatnam – 530 048

MINUTES OF THE CLASS COMMITTEE MEETING FOR B.TECH PROGRAMME:

Department of ECE (I)

Academic Year : 2019 - 20 Meeting No. : 01/02/03

Place of Meeting : Seminar Hall Date 28/6/19

Members present: 6

S.No.	Name	Position in Class Committee	Signature
1.	Dr. M. Srinivasa Rao	Chairman/Chairperson	
2.	S. Ramanjaneya Reddy	Class Teacher	
3.	B.V.S. Renuka Devi	Faculty Member	
4.	B. Vijayalakshmi	Faculty Member	
5.		Faculty Member	
6.		Faculty Member	
7.		Faculty Member	
8.	K. Rathika	Class Representative 1	K. Rathika
9.	T. Tejaswini	Class Representative 2	T. Tejaswini
10.	G. Nitya Murthy	Class Representative 3	G. N. Murthy
11.	E.S. Sivanathi	Student Member	E. Sivanathi
12.	D. Phani Deepika	Student Member	D. Phani Deepika
13.		Convener	

Minutes of Meeting - I

S.No	Agenda Item	Discussed (Y/N)	Comments/Remarks
I.	General Review		
(a)	University Regulations & Rules	Yes	University Rules & Regulations are discussed with the students
(b)	Assessment procedures	Yes	All the procedures and assessment methods for mid exam and assgns are explained
(c)	Assignment, tutorials & Rubrics	Yes	methodology for assessment are informed to the students
II.			
(a)	Student Identification	Yes	slow learners are identified based on the performance of first year. and given information to cope up with the subjects from 2nd year onwards.

☑	Regularity	Yes	students are advised to maintain a min of 75% attendance for study & 90% for lab sessions
☑	student suggestions	Yes	suggestions from students are noted. Information regarding curriculum were infor

Proforma

GAYATRI VIDYA PARISHAD COLLEGE OF ENGINEERING FOR WOMEN
Madhurawada :: Visakhapatnam – 530 048

MINUTES OF THE CLASS COMMITTEE MEETING FOR B.TECH PROGRAMME:

Department of ECE

Academic Year : 2019-20 | Meeting No. : 01 / 02 / 03

Place of Meeting : 400 Room | Date: 5/08/2019

Members present: 9

S.No.	Name	Position in Class Committee	Signature
1.	Dr. H S Srinivas Rao	Chairman/Chairperson	[signature]
2.	S Ramanjaneya Reddy	Class Teacher	[signature]
3.	B V S Renuka Devi	Faculty Member	[signature]
4.	N.V Maheswara	Faculty Member	[signature]
5.	[illegible]	Faculty Member	[signature]
6.		Faculty Member	
7.		Faculty Member	
8.	Ch. Satya Ramya	Class Representative 1	Ch. Satya Ramya
9.	D. Harika	Class Representative 2	Harika
10.	K. Roshitha	Class Representative 3	K. Roshitha
11.	G. Amrutha Rani	Student Member	G. Amrutha
12.	G. S Naga [illegible]	Student Member	[signature]
13.		Convener	

S.No	Agenda Item	Discussed (Y/N)	Comments/Remarks
I	General Review		
1	Coverage of syllabus	yes	As per the schedule syllabus of all the subject are covered
2	Reviewing Assignments & Tutorials	yes	periodical review of assignments and tutorial questions are provided for each subject
3	Lab Activity	yes	lab activities were conducted as per the schedule timetable & lab experiments conducted as per the requirement
II	performance Review		
	outcomes in each subject is made available. & Slow learners are identified as per mid marks	yes	Discussed the issues with the students regarding the subjects suggestions were given to the student

Irregular students

18JG1A0401 – A Tejaswini
18 JG1A0425 – C. Sai Sahitya
18 JG1A0444 – Thanvi Kollanu

Slow learners

18JG1A0413 – B Tulasi Shree
18JG1A0415 – B. Harshini
18 JG1A0419 – C. Naveena
18 JG1A0422 – C. Silpa
18 JG1A0425 – C. Sai Sahitya
18 JG1A0444 – Thanvi Kollanu
18 JG1A0450 – K pravallika Kiranmai
18 JG1A0455 – K. Venu priyanka

II. Regularity — Yes. Based on the attendance details produced in the meeting Irregular students were identified

IV Student feedback

(a) Feedback	Yes	Feedback on all the subjects was satisfactory
(b) Suggestions	Yes	General and specific suggestions were noted and

Proforma

GAYATRI VIDYA PARISHAD COLLEGE OF ENGINEERING FOR WOMEN
Madhurawada :: Visakhapatnam – 530 048

MINUTES OF THE CLASS COMMITTEE MEETING FOR B.TECH PROGRAMME:

Department of ECE

Academic Year : 2019-20 Meeting No. : 01/02/03

Place of Meeting : HOD Room Date: 01/10/2019

Members present: 8

S.No.	Name	Position in Class Committee	Signature
1.	Dr. M. S. Srinivasa Rao	Chairman/Chairperson	[signature]
2.	S. Ramanjaneya Reddy	Class Teacher	[signature]
3.	Dr. V. S. Renuka Devi	Faculty Member	[signature]
4.	[signature]	Faculty Member	[signature]
5.		Faculty Member	
6.		Faculty Member	
7.		Faculty Member	
8.	Ch. Satya Ramya	Class Representative 1	Ch. Satya Ramya
9	G. T. Naga Sri	Class Representative 2	[signature]
10.	D. Harika	Class Representative 3	Harika D.
11.	K. Rashitha	Student Member	K. Rashitha
12.	G. Amrutha Rani	Student Member	G. Amrutha
13.		Convenor	

Minutes | Meeting

S.No	Agenda Item	Discussed (Y/N)	Minutes
I	General Review		
1.	Coverage of syllabus	Yes	Syllabus completion is as per the schedule & all syllabus completed.
2	Review of the assignments	Yes	Assignments are given as per the concerned teachers & assignment submission is done on time by the students
3	Lab Activity	Yes	All the lab experiments and lab internal were completed.
II	Performance Review		
a	Student performance Analysis	Yes	Unit syllabus as completed for all the subjects previous JNTUK question papers were discussed. Instructions for effective presentation of the End Exam were given to the slow learners

8. Regularity	yes	The attendance report is made available in the meeting all the students are having good percentage of attendance

The students feedback

1. Suggestions from students	yes	Concern from the students regarding counselling and issues regarding subjects and syllabus is made available to the faculty members

Proforma

GAYATRI VIDYA PARISHAD COLLEGE OF ENGINEERING FOR WOMEN
Madhurawada :: Visakhapatnam – 530 048

MINUTES OF THE CLASS COMMITTEE MEETING FOR B.TECH PROGRAMME

Department of ECE

Academic Year : 2019 - 20 Meeting No. : 01/02/03

Place of Meeting : H00 Room Date: 25/11/2019

Members present: 4

S.No.	Name	Position in Class Committee	Signature
1.	Dr. [illegible]	Chairman/Chairperson	
2.	S. Ramanjaneya Reddy	Class Teacher	
3.	N. Roopa Vathi	Faculty Member	
4.	R. Sunil Kumar	Faculty Member	
5.		Faculty Member	
6.		Faculty Member	
7.		Faculty Member	
8.	A. Divija	Class Representative 1	A. Divija
9.	S. Aishwarya Deepika	Class Representative 2	S. Aishwarya Deepika
10.	D. Harsh.	Class Representative 3	
11.	Ch. Satya Ramya	Student Member	Ch. Satya Ramya
12.	K. Rathika	Student Member	K. Rathika
13.		Convener	

Minutes of Meeting - I

S.No	Agenda Item	discussed (Y/N)	comments/remarks
I.	General Review		
a)	University Regulations Rules	Yes	university rules & regulations are discussed briefly (Regarding mid exam, external Exams)
b)	Assignment procedures Tutorial, Rubrics	Yes	statement of Assessment procedure and discussion on Evaluation was taken
c)	Lab activities, preparation & Rubrics	Yes	Methodology, Rubrics & procedure were explained.
d)	Assessment procedures	Yes	Assessment procedures in Assignment, mid exam are discussed

Sl.no	Agenda Item	Discussed (Yes/No)	Comments/Remarks
I	Student Identification	Yes	Based on the performance of the previous semester the students are informed given tips to overcome the difficulties, as per the given feed back from the students
II	Regularity	Yes	students are advised to maintain a min of 85% attendance both for Theory classes & lab sessions.
IV	Student suggestions	Yes	Suggestions from students on Fests and issues regarding the classes & lab sessions were discussed.

Proforma

GAYATRI VIDYA PARISHAD COLLEGE OF ENGINEERING FOR WOMEN
Madhurawada :: Visakhapatnam – 530 048

MINUTES OF THE CLASS COMMITTEE MEETING FOR B.TECH PROGRAMME:

Department of ECE

Academic Year : 2019 - 20 Meeting No.: 01 / 02 / 03

Place of Meeting : HOD Room Date: 17/2/2020

Members present: 8

S.No.	Name	Position in Class Committee	Signature
1.	Dr. I.S. Srinivasa Rao	Chairman/Chairperson	
2.	S. Ramaiganga Reddy	Class Teacher	
3.	K. Sunil Kumar	Faculty Member	
4.	J. Rama Vathi	Faculty Member	
5.		Faculty Member	
6.		Faculty Member	
7.		Faculty Member	
8.	S. Aishwarya Deepika	Class Representative 1	S. Aishwarya Deepika
9.	Harika D	Class Representative 2	
10.	G. J. Naga Jhansi	Class Representative 3	G. J. Naga Jhansi
11.	K. Jashna	Student Member	K. Jashna
12.	A. Madhuri	Student Member	A. Madhuri
13.		Convener	

Minutes of Meeting:

S No.	Agenda Item	Discussed	Comments/Remarks
1.	General Review		
1.	Coverage of syllabus	Yes	As per the scheduled syllabus of all the subjects is in sync with the Academic calendar.
2.	Review of Assignments & Tutorials	Yes	periodical review of assignments and practice of tutorial questions
3.	Lab Activity Review	Yes	lab activities were conducted as per the scheduled time Table.
II.	performance Review:		
	outcomes in each subject made aware & slow learners were identified	Yes	Discussed the respective subject outcomes with slow explanation

Irregular students

18 IGIAD411 — Grace J B Gayathri.
18 IGIAD424 — C Santhi

Slow learners

18 IGIAD406
18 IGIAD 417
18 IGIAD433
18 IGIAD452
18 IGIAD453
18 IGIAD 456

III Regularity

Attendance details of the students are made available in the meeting	Yes	Based on the attendance details produced in the meeting Irregular students are identified

IV Student Feedback

a) Feedback	Yes	Feedback on all subjects were taken
b) Suggestions	Yes	General and specific suggestions were asked

Proform

GAYATRI VIDYA PARISHAD COLLEGE OF ENGINEERING FOR WOMEN
Madhurawada :: Visakhapatnam – 530 048

MINUTES OF THE CLASS COMMITTEE MEETING FOR B.TECH PROGRAMME:

Department of EEE

Academic Year : 2019-20

Meeting No. : 01 / 02 / 03

Place of Meeting :

Date: 16-11-2020

Members present:

S.No.	Name	Position in Class Committee	Signature
1.	Dr. K. Srinivasa Rao	Chairman/Chairperson	
2.	Ramanjaneya Reddy	Class Teacher	
3.	Roopa Vathi	Faculty Member	
4.	R. Ajit Kumar	Faculty Member	
5.		Faculty Member	
6.		Faculty Member	
7.		Faculty Member	
8.	S. Aishwarya Deepika	Class Representative 1	S. Aishwarya Deepika
9	G.Y. Bhavana	Class Representative 2	G.Y. Bhavana
10.	Ch. Satya Ramya	Class Representative 3	Ch. Satya Ramya
11.		Student Member	
12.		Student Member	
13.		Convener	

Agenda of Meeting 3

S.No.	Agenda Item	Discussed (Y/N)	Comments/Remarks
I	General Review		
	Coverage of syllabus	Yes	syllabus of all subjects completed as per the schedule
	Review of Assignments	Yes	Assignments submission is done on time by all the students
	Lab Activity	Yes	All the lab exps and lab internals were completed
	performance Review		
	student performance analysis	Yes	→ 6 units syllabus is completed for all subjects → previous JNTUK Question papers were discussed → Instructions for effective presentation at the end exam were given to slow learners

(b) Regularity Yes - The attendance report is made available in the meeting. All the students are having good percentage of attendance

IV Student feed back

1. suggestions from students Yes concern from students regarding counselling and issues regarding subjects were discussed.

The mechanism of internal assessment is as per the regulations issued by the affiliating university JNTUK. During the period 2016-2021, we have three regulations R16, R19 and R20.

The following are the rnhries followed hy all the departments to evaluate Assignments:

DIMENSIONS	SCALES			
	4	3	2	1
Understanding the Topic (1M)	Complete Understanding of the topic.	Part of the topic is misunderstood.	Most of the topic is misunderstood.	Complete misunderstanding of the topic.
Organization / Logic / Relevance (3M)	The solution is well written statements are mutually supporting and followed from one another to address the question. Achieves the Learning Objective.	Some parts are not clear, statements are usually mutually supporting and follow from one another but does not address the question explicitly to achieve the Learning Objective.	Most of the parts are not clear, statements hang together but other parts are unclear to address the question. Achieves Learning Objectives minimally.	It is hard or impossible to understand since answers of the question are either too vague or filled with trivial details. Fails to achieve the Learning Objectives.
On Time Submission (1M)	Submission of Assignment on time.	Submission of Assignment almost on time.	Submission of Assignment with little delay.	Submission of Assignment is late.

Tbe following rubric is used to evaluate Lab internal Evaluation marks

Internals	Dimensions	Scales			
		4	3	2	1
Day to Day Performance	Attendanee (1)	Attended and completed on tbe same day	Attended and partially completed on the same day	Attended but completed in the extra lab	Not attended but completed in the extra lab

	Understanding of the Experiment (2)	Complete understanding of the experiment with learning objectives	Partial understanding of the experiment with learning objcctives	Most of the experiment misunderstood	Complete misunderstanding of the experiment
	Implementation with result analysis (5)	Complete implementation with result analysis and interpretation	Complete implementation with result analysis only	Complcte implementation with result analysis and interpretation in extra lab	Complete implementation with result analysis only in extra lab
	Observation submission on time (2)	Submission of the observation on time	Submission of the observation almost on time	Submission of the observation immediately after the extra lab	Submission of the observation after the extra lab
Record	**Comprehensiveness & Legible (3)**	Write all the elements of the experiments which can be easily readable	Write all the elemcnts of the experiments with poor handwriting	Some elements are missing but presented clearly	Some elements are missing and poor handwriting
	Timely Suhmission (2)	Submission of thc record on time	Submission of thc record almost on timc	Submission of thc record immediately after the cxtra lab	Submission of the record after the extra lab
Internals	**Aim of the experiment (2)**	Complete understanding of thc learning objectives and outcomes	Complete understanding of the learning objectives only	Partial understanding of the learning objectives	Misunderstanding of the learning objectives
	Write up (3)	Write all the elcments of the experiments which can be easily readable	Write all the elcments of the experiments with poor handwriting	Some elements are missing but presented clearly	Some elements are missing and poor handwriting
	Implementation & result analysis (3)	Complete implementation with result analysis and interpretation	Completc implemcntation with result analysis only	Partial implementation with result analysis only	Partial implemcntation only

	Viva- Voce (2)	Experiment and subject knowledge with good oral presentation	Experiment and subject knowledge with poor oral prescntation	Partial experiment knowledge with poor oral prescntation	Partial subject knowledge with poor oral presentation

The following rubric is used to evaluate Internal Project marks

Project Internals	Dimensions	Seales			
		4	3	2	1
Day to Day Performance by Guide	Day to Day work (10M)	Successfully completed the work in time with result analysis and interpretation with required learning objectives	Successfully completed the work in time with validation of results and required learning objectives	Successfully completed the work in time but vaiidations are used at some places only	Successfully completed the work with changes as suggested with delay
	Involvement (10M)	Able to handle all work related questions with illustrative explanation	Answered most questions correctly and with less illustrative explanation	Answered most questions correctly but sometimes needed clarifications	Answered few questions
	Team work & Time Management (5M)	Contribution towards completion of the assigned work in the team for timely submission	Contribution towards completion of the assigned work in the team with a delay	Independently completed the assigned work in the team but accepted with modifications	Independently completed the assigned work but team usually rejects
	Regularity (5M)	Students having more than 85% in the project attendance	Students having 80%- 85% in the project attendance	Students having 75%- 80% in the projeet attendance	Students having 65%- 75% in the project attendanee
Project Review I & II	Understanding of tbe problem and applicability (5M)	Excellent understanding of the problem and interpretation with required project outcomes	Understanding of the problem and lack of interpretation with required project outcomes	Minimum Understanding of the problem with required project outcomes	Lack of understanding of the problem and project outcomes
	Presentation (5M)	Excellent preparation, Well delivered and organised	Good delivery and preparation, presents idea in an effective manner	Preparation, organisation and delivery satisfactory	Lack of delivery and organisation, minimum preparation

	Analysis, Design and Implementation with valid results **(5M)**	Able to analyze, Design and implement with valid results of the given problem statement	Able to analyze, Design and implement with results of the given problem statement	Able to analyze and Design the given problem statement	Able to analyze the given problem statement
	Viva **(5M)**	Able to answer with precision & completeness; confident and professional	Able to answer with precision & completeness ; almost confident and professional	Able to answer with precision but lack of confidence and professional behaviour	Missing conceptual information with lack of confidence and professional behaviour
	Regularity **(5M)**	Students having more than 85% in the project attendance	Students having 80%- 85% in the project attendance	Students having 75%-80% in the project attendance	Students having 65%- 75% in the project attendance
	Project Progress **(5M)** (only for Project Review I)	Completed 41%-50% of the project, In consultation with guide and team members	Completed 31%-40% of the project, In consultation with guide and team members	Completed 26%-30% of the project, In consultation with guide and team members	Completed 21%-25% of the project, In consultation with guide and team members
	Documentation **(5M)** (only for Project Review II)	The work is organised with clear diagrams and sketcbes using effieient strategy and/or procedures	The work is organised with clear diagrams and sketches using almost effective strategy and/or procedures	Sometime uses effeetive strategy but with inconsistent diagrams and sketches	The work appears unorganised, rarely uses effective strategies with inconsistent diagrams and sketches

The following rubric is used to evaluate Best Project

Factors	3	2	1
Objective and Problem Definition **(10 M)**	Objective and problem definitions are well defined. Extensive explanation on the limitations of the existing system. Advanced or Innovative idea.	Objective und problem definitions are well defined. Extensive explanation on the limitations of the existing system.	Objective and problem definitions are well defined. Good/ moderate explanation on the existing system.
Implementation and Results **(30 M)**	Implemented and obtained valid results.	Implemented and obtained valid results.	Implemented and obtained valid results.

	Comparison of the results with results of existing system.	Extensive Analysis and description of the obtained results.	Good/Moderate Analysis and description of the obtained results.
Usage of Modern Tools (25 M)	Effective usage of modern Software/Hardware for implementation	Moderate usage of modern Software/Hardware for implementation	usage of conventional Software/Hardware for implementation
Technical Report Writing (25 M)	Thesis is well written and organized as per the given template with clear diagrams and equations using toolboxes	Thesis is well written and organized as per the given template with clear diagrams and equations	Thesis is well written and appears unorganized as per the given template with inconsistent diagrams and equations
Useful for society (5 M)	Prototype is developed and can be upgraded for real time usage in society and recognized by external agencies	Prototype is developed and can be upgraded for real time usage in society	Prototype is developed and need further improvement to meet real time usage in society
Scope for publication (5 M)	The results and discussions presented are better than the existing system with use of new methods.	Tbe results and discussions presented can be extended to improve the results than the existing system.	Need more analysis on results and discussions to know the derived results are better than the existing systems.

[redacted]

The following rubric is used to evaluate seminar marks

Dimensions	Scales			
	4	3	2	1
Presentation (10M)	Excellent preparation, Well delivered and organised	Good delivery and preparation, presents idea in an effective manner	Preparation, organisation and delivery satisfactory	Lack of delivery and organisation, minimum preparation
Understanding of the topic (10M)	Excellent understanding of the topic and interpretation with required learning outcomes	Understanding of the topic and lack of interpretation with required learning outcomes	Minimum Understanding of the topic with required learning outcomes	Lack of understanding of the topic and learning outcomes
Viva (10M)	Able to answer with precision & completeness; confident and professional	Able to answer with precision & completeness ; aimost confident and professional	Able to answer with precision but lack of confidence and professional behaviour	Missing conceptual information with lack of confidence and professional behaviour
Time Management (5M)	Effective time management	Almost completed within the given time	Exceeded time limit	Lack of time management
Seminar Report (15M)	The work is organised with clear diagrams and sketches using efficient strategy and/or procedures	The work is organised with clear diagrams and sketches almost using effective strategy and/or procedures	Sometime uses effective strategy but with inconsistent diagrams and sketches	The work appears unorganised, rarely uses effective strategies with inconsistent diagrams and sketches

The following rubric is used to evaluate Engineering Drawing:

	Category	Points			
	Attendance(2)	Attended and eompleted on the same day.	Attended and partially completed on the same day.	Attended but eompleted in the extra lab.	Not attended but eompleted in the extra lab.

Day to day Performance (20m)	**Proper size and scale(2)**	Drawing has an excellent appearance. Space is used to display the final drawing in a professional manner. Drawn to scale. The title block is fully edited.	Spaee is used to display the final drawing in a professional manner. Drawn to scale. The title block is only 80% completed.	The title block is only 50% completed. Space is used properly to display drawing.	Information about the drawing is not provided. Not drawn to scale. Not drawn correctly.
	Understanding of Concept(2)	Complete understanding of thc concept with learning objectives	Partial understanding of the concept with learning objectives	Most of the concepts are misunderstood	Complete misunderstanding of the concept
	Activity sbeets(10)	All the activitics are done correctly	80% of activities are done correctly	50% of activities are done correctly	Less than 40% are done correctly
	Dimensions (2)	All-important dimensions are shown on the drawing. Dimensions are correct.	80% of dimensions are done correctly.	50% of dimensions are done correctly.	Improper and/or unnecessary dimensioning.
	Sbeets submission on time(2)	Submission of the sheet on time	Submission of the sheet almost on time	Submission of the sheet immediately after the extra lab	Submission of the sheet after the extra lab

The following rubric is used to evaluate English communication lab:

INTERNALS	DIMENSION	SCALE			
		4	3	2	1
DAY TO DAY PERFORMANCE (10)	ATTENDANCE (1)	Attended and completed the activity with active participation.	Attended and completed the activity with passive participation	Attended but completed the activity in the extra lab	Not attended but completed the activity in the extra lab
	GRAMMAR, DICTION, SENSE (3)	Excellent grammar, vocabulary, logic and coherence.	Good at grammar, vocabulary, logic and coherence	Partially correct in grammar& vocabulary, logic& coherence.	Lack of grammar, vocabulary, logic and coherence.
	CLARITY & PRONUNCIATION ACCENT & FLUENCY (2)	Clear expression and correct articulation of sounds. Smooth and fluid speech; few to no hesitations; no attempts to search for words.	Partially Clear in expression and correct articulation of sounds. Smooth and fluid speech; few hesitations; a slight search for words.	Shows a pattern of errors in punctuation and articulation. Speech is relatively smooth; some hesitation and searching for words.	No clarity in expression and articulation of sounds. No ease of expression and excess of mother tongue influence.
	BODY LANGUGAE (eye contaet, posture, gestures) (2)	Body language, gestures, and facial expressions adds greatly to the context	Body language, gestures, and facial expressions complement the context	Body language, facial expressions and gestures lack variety and spontaneity	Poor Body language, gestures, and facial expressions
	TONE & AUDIBILITY (2)	Voice is full of expression and appropriate volume.	Voice has some expression. and inaudible word or two.	Voice is monotonous with volume wavers.	Inaudible.
	COMPREHENSIVENESS & LEGIBILITY	Completion of exercises with correct answers and good hand writing.	Partial Completion of exercises with correct answers and	Completion of exercises with minimum errors and	Completion of exercises with errors and poor hand writing.

RECORD (5)	(3)		good hand writing.	poor hand writing.	
	TIMELY SUBMISSION (2)	Submission of the record on time.	Submission of the record almost on time	Submission of the record immediately after the extra lab.	Submission of the record after the extra lab.
INTERNAL (10)	**WRITE UP (5)**	Clear, accurate, detailed and comprehensive.	Clear and comprehensive but not accurate.	Limited ability to present information	Poor presentation.
	VIVA VOCE (5)	subject knowledge and application with good oral presentation	subject knowledge and application with poor oral presentation	Partial subject knowledge and application with poor oral presentation	Partial subject knowledge with poor application and oral presentation

Distribution and Weigbtage of Marks (R16)

Sl. No.	*Distribution*	*Frequency*	*Description*
1	*Internal Tests*	*Twice in a semester*	*Internal test 1*: Q. No. 1, 2, 3 — Marks 10, 10, 10; *Internal test 2*: Q. No. 1, 2, 3 — Marks 10, 10, 10 • *DAC will ensure the quality of question and scheme of evaluation* • *The internal descriptive marks are reduced to 15 marks*
2	*Assignment*	*Twice in a semester*	• *Assignment 1 questions covering Unit 1-3 is given to students before the internal test1 to evaluate for 5 marks as per the rubric* • *Assignment 2 questions covering Unit 4-6 is given to students before the internal test2 to evaluate for 5 marks as per the rubric*
3	*Online quiz*	*Twice in a semester*	• *Quiz 1 of 20 questions covering unit 1-3 is conducted for the students during the internal test1 by JNTUK to evaluate for 10 marks* • *Quiz 2 of 20 questions covering unit 4-6 is conducted for the students during the internal test2 by JNTUK to evaluate for 10 marks*

- *The total marks secured by the student in each mid-term examination are evaluated for 30 marks*
- *The marks secured by the students from the above internal tests 1 & 2 (Descriptive + Objective + Assignment) are finally considered as 80% of the best and 20% of the other*
- *Final internal Marks = (Best of (Mid-1/Mid-2) marks x 0.8 + Least of (Mid-1/Mid-2) marks x 0.2)*

<table>
<tr><td>4</td><td>Internal Laboratory Tests</td><td>Twice in a semester</td><td>• Experiment wise evaluation/ Weekly evaluation of Day-to-Day and Becord work for each experiment is evaluated for the marks 10 and 5 respectively
• Two internal tests are conducted for 10 marks each covering all the list of experiments as per JNTUK syllabus
• The rubrics developed for evaluation of Day-to-Day, Becord work and internal marks are used.</td></tr>
<tr><td>5</td><td>Semester-End Examinations (Theory / Practical)</td><td>Once in a semester</td><td>• The external theory exam is conducted by JNTUK for 70 marks covering all 6 units
• The external lab exam is scheduled by JNTUK for 50 marks covering all experiments.</td></tr>
<tr><td>6</td><td>Seminar</td><td>Once in a curriculum</td><td>• Each student has to be evaluated based on the presentation of any latest topic with report of 10-15 pages and a ppt of min 10 slides
• The Seminar report is evaluated for 50 marks by the internal department committee
• The rubrics developed for evaluation of ppt and report is used
• There is no external examiuation for Seminar</td></tr>
<tr><td>7</td><td>Project work</td><td>Once in a curriculum</td><td>• The Project work carries a total of 200 marks and of which 60 marks are internal and the rest 140 marks are awarded by the External
• Internal project work evaluation
<table>
<tr><td colspan="5">Internal Guide Evaluation</td></tr>
<tr><td>Rubrics</td><td>Day-to-Day Work</td><td>Involvement in Project</td><td>Team Work and Time Management</td><td>Regularity</td></tr>
<tr><td>Marks</td><td>10</td><td>10</td><td>05</td><td>05</td></tr>
<tr><td colspan="4">Internal Beview</td><td></td></tr>
<tr><td colspan="2">Review 1</td><td colspan="3">Review 2</td></tr>
<tr><td colspan="2">30</td><td colspan="3">30</td></tr>
<tr><td colspan="5">Total Internal Beview marks= 1/3rd * Beview 1+ 2/3rd * Review 2</td></tr>
</table>
• The rubrics developed for evaluation of Guide and Beview 1 &2 is used
• Fiual External Review is conducted by External Faculty nominated by JNTUK together with Internal Project Review Committee
• The rubrics are also developed for choosing the best projects by considering the total marks</td></tr>
</table>

Distribution and Weightage of Marks (R19)

<table>
<tr><th>Sl. No.</th><th>Distribution</th><th>Frequency</th><th>Description</th></tr>
<tr><td>1</td><td>Internal Tests</td><td>Twice in a semester</td><td>
<table>
<tr><td colspan="4">Internal test I</td></tr>
<tr><td>Q. No.</td><td>1</td><td>2</td><td>3</td></tr>
<tr><td>Marks</td><td>08</td><td>08</td><td>04</td></tr>
<tr><td colspan="4">Internal test 2</td></tr>
<tr><td>Q. No.</td><td>1</td><td>2</td><td>3</td></tr>
<tr><td>Marks</td><td>08</td><td>08</td><td>04</td></tr>
</table>
• DAC will ensure the quulity of question and scheme of evaluation
• The internal descriptive marks are reduced to 10 marks</td></tr>
<tr><td>2</td><td>Assignment</td><td>Twice in a semester</td><td>Assignment 1 questions covering Unit 1-3 is given to students before the internal test1 to evaluate for 5 marks us per the rubric.
Assignment 2 questions covering Unit 3-5 is given to students before the internal test2 to evuluate for 5 marks as per the rubric.</td></tr>
<tr><td>3</td><td>Online quiz</td><td>Twice in a semester</td><td>Quiz 1 of 20 questions covering unit 1-3 is conducted for the students during the internal test1 by JNTUK to evaluate for 10 marks.
Quiz 2 of 20 questions covering unit 3-5 is conducted for the students during the internal test2 by JNTUK to evaluate for 10 marks.</td></tr>
<tr><td colspan="4">• The total marks secured by the student in each mid-term examination are evuluated for 25 marks
• The marks secured by the students from the above internal tests 1 & 2 (Descriptive + Objective + Assignment) are finally considered as 80% of the best and 20% of the other
• Final internal Marks = (Best of (Mid-1/Mid-2) marks x 0.8 + Least of (Mid-1/Mid-2) marks x 0.2)</td></tr>
<tr><td>4</td><td>Internal Laboratory Tests</td><td>Twiee In a semester</td><td>• Experiment wise evaluution/ Weekly evuluution of Day-to-Day and Record work for each experiment is evaluated for the marks 5 and 5 respectively
• Two internal tests are conducted for 10 marks each covering all the list of experiments as per JNTUK syllabus
• The rubrics developed for evaluation of Day-to-Day, Record work and Internal marks are used.</td></tr>
<tr><td>5</td><td>Semester-End Exuminations (Theory / Pructicul)</td><td>Once in a semester</td><td>• The external theory exam is conducted by JNTUK for 75 marks covering all 5 units
• The external lab exam is scheduled by JNTUK for 30 marks covering all experiments.</td></tr>
<tr><td>6</td><td>Seminur</td><td>Once in a curriculum</td><td>• Each student has to be evaluated based on tbe presentution of any latest topic with report of 10-15 pages and a ppt of min 10 slides
• The Seminar report is evaluutcd for 50 marks by the internal department committee</td></tr>
</table>

<table>
<tr><td></td><td></td><td></td><td>

- *The rubrics developed for evaluation of ppt and report is used*
- *There is no external examination for Seminar*

</td></tr>
<tr><td>*7*</td><td>*Project work*</td><td>*Twice in a curriculum*</td><td>

- *The Project work I carries a total of 50 marks and of which 20 marks are internal and the rest 30 marks are awarded by the External*
- *The Project work II carries a total of 150 marks and of which 60 marks are internal and the rest 90 marks are awarded by the External*
- *project work I evaluation*

<table>
<tr><td colspan="3">*Internal Guide Evaluation*</td></tr>
<tr><td>*Rubrics*</td><td>Day-to-Day Work</td><td>Report</td></tr>
<tr><td>*Marks*</td><td>*05*</td><td>*05*</td></tr>
<tr><td colspan="3">*Internal Review*</td></tr>
<tr><td colspan="3">*10*</td></tr>
</table>

- *project work II evaluation*

<table>
<tr><td colspan="3">*Internal Guide Evaluation*</td></tr>
<tr><td>*Rubrics*</td><td>Day-to-Day Work</td><td>Report</td></tr>
<tr><td>*Marks*</td><td>*15*</td><td>*15*</td></tr>
<tr><td colspan="3">*Internal Review*</td></tr>
<tr><td colspan="3">*30*</td></tr>
</table>

- *The rubrics developed for evaluation of Guide and Review is used*
- *Final External Review is conducted by External Faculty nominated by JNTUK together with Internal Project Review Committee*
- *The rubrics are also developed for choosing the best projects by considering the total marks*

</td></tr>
<tr><td>*8*</td><td>*Engineering drawing*</td><td>*Once in a curriculum*</td><td>

- *Internal marks are evaluated for 25 marks*
- *15 marks for continuous Assessment (day–to–day work)*
- *Two internal exams are conducted for 10 marks*
- *Final internal Marks = (Best of (Mid-1/Mid-2) marks x 0.8 + Least of (Mid-1/Mid-2) marks x 0.2)*
- *External exam is conducted by JNTUK for 75 marks*

</td></tr>
<tr><td>*9*</td><td>*For Socially Relevant Project*</td><td>*Once in a curriculum*</td><td>

- *Two internal evaluations are conducted for 20 marks*
- *Final Marks = (Best of evaluation marks x 0.8 + Least of evaluation marks x 0.2)*
- *External Review is conducted by External Faculty nomlnated by JNTUK togelher with Internal Project Review Committee for 30 marks*

</td></tr>
<tr><td>*10*</td><td>*Engineering Exploration Course*</td><td>*Once in a curriculum*</td><td>

- *Two internal evaluations are conducted for 20 marks*
- *Final Internal Marks = (Best of evaluation marks x 0.8 + Leust of evaluation marks x 0.2)*
- *External Review is conducted by External Faculty nominated by JNTUK togetber with Internal Project Review Committee for 30 marks*

</td></tr>
</table>

11	Mini Project/Internship/ Industrial Training/Skill Development programmes/Research Project guidelines	Once in a curriculum	• External Review is conducted by External Faculty nominated by JNTUK together with Internal Project Review Committee for 50 marks

Distribution and Weightage of Marks (R20)

<table>
<tr><th>Sl. No.</th><th>Distribution</th><th>Frequency</th><th>Description</th></tr>
<tr><td>1</td><td>Internal Tests</td><td>Twice in a semester</td><td>
<table>
<tr><td colspan="4">Internal test 1</td></tr>
<tr><td>Q. No.</td><td>1</td><td>2</td><td>3</td></tr>
<tr><td>Marks</td><td>05</td><td>05</td><td>05</td></tr>
<tr><td colspan="4">Internal test 2</td></tr>
<tr><td>Q. No.</td><td>1</td><td>2</td><td>3</td></tr>
<tr><td>Marks</td><td>05</td><td>05</td><td>05</td></tr>
</table>
• DAC will ensure the quality of question and scheme of evaluation
• The internal descriptive marks are evaluated for 15 marks</td></tr>
<tr><td>2</td><td>Assignment</td><td>Twice in a semester</td><td>Assignment 1 questions covering Unit 1-3 is given to students before the internal test1 to evaluate for 5 marks as per the rubric.
Assignment 2 questions covering Unit 3-5 is given to students before the internal test2 to evaluate for 5 marks as per the rubric.</td></tr>
<tr><td>3</td><td>Online qulz</td><td>Twice in a semester</td><td>Quiz 1 of 20 questions covering unit 1-3 is conducted for the students during the internal test1 by JNTUK to evaluate for 10 marks.
Quiz 2 of 20 questions covering unit 3-5 is conducted for the students during the internal test2 by JNTUK to evaluate for 10 marks.</td></tr>
<tr><td colspan="4">• The total marks secured by the student in each mid-term examination are evaluated for 25 marks
• The marks secured by the students from the above internal tests 1 & 2 (Descriptive + Objective + Assignment) are finally considered as 80% of the best and 20% of the other
• Final internal Marks = (Best of (Mid-1/Mid-2) marks x 0.8 + Least of (Mid-1/Mid-2) marks x 0.2)</td></tr>
<tr><td>4</td><td>Engineering drawing</td><td>Once in a curriculum</td><td>• Internal marks are evaluated for 30 marks
• 15 marks for continuous Assessment (day–to–day work)
• Two internal exams are conducted for 15 marks
• Final internal Marks = (Best of (Mid-1/Mid-2) marks x 0.8 + Least of (Mid-1/Mid-2) marks x 0.2)
• External exam is conducted by JNTUK for 70 marks</td></tr>
</table>

<table>
<tr><td>5</td><td>Internal Laboratory Tests</td><td>Twice in a semester</td><td>• Experiment wise evaluation/ Weekly evaluation of Day-to-Day and Record work for each experiment is evaluated for the marks 5 and 5 respectively
• Two internal tests are conducted for 5 marks each covering all the list of experiments as per JNTUK syllabus
• The rubrics developed for evaluation of Day-to-Day, Record work and internal marks are used.</td></tr>
<tr><td>6</td><td>Semester-End Exuminations (Theory / Practical)</td><td>Once in a semester</td><td>• The external theory exam is conducted by JNTUK for 70 marks covering all 5 units
• The external lab exam is scheduled by JNTUK for 35 marks covering all experiments.</td></tr>
<tr><td>7</td><td>Project work</td><td>Once in a curriculum</td><td>• The Project work carries a total of 200 marks and of which 60 marks are internal and the rest 140 marks are awarded by the External
• Internal project work evaluation
<table>
<tr><td colspan="5">Internal Guide Evaluation</td></tr>
<tr><td>Rubrics</td><td>Day-to-Day Work</td><td>Involvement in Project</td><td>Team Work and Time Management</td><td>Regularity</td></tr>
<tr><td>Marks</td><td>10</td><td>10</td><td>05</td><td>05</td></tr>
<tr><td colspan="4">Internal Review</td><td></td></tr>
<tr><td colspan="3">Review 1</td><td colspan="2">Review 2</td></tr>
<tr><td colspan="3">30</td><td colspan="2">30</td></tr>
<tr><td colspan="5">Total Internal Review marks= 1/3rd * Review 1+ 2/3rd * Review 2</td></tr>
</table>
• The rubrics developed for evaluation of Guide and Review 1 &2 is used
• Final External Review is conducted by External Faculty nominated by JNTUK together with Internal Project Review Committee
The rubrics are also developed for choosing the best projects by considering the total marks</td></tr>
</table>

Vice Principal
GVP College of Engineering
for Women
Visakhapatnam

GVP COLLEGE OF ENGINEERING FOR WOMEN
MADHURAWADA::VISAKHAPATNAM

Department of Electrical and Electronics Engineering

II B. Tech. (II Semester) Mid Examinations-I , Jan 2020

Question Bank

Subject: **Electrical Machines-II** Branch: EEE

Name of the Instructor/Faculty: **D. Srinivas Reddy**

Q. No.	Questions	Marks	CO	BL
1 (a)	Explain why an induction motor cannot develop torque when running at synchronous speed? Define the slip speed of an induction motor and deduce how the frequency of the rotor currents and magnitude of the rotor emf are related to slip?	5	1	L2
1 (b)	A three phase, 400 V, 50 Hz, 6 pole induction motor takes a power input of 35 kW at its full load speed of 980 rpm. The total stator losses are 1 kW and the friction and windage losses are 1.5 kW. Calculate (i) slip (ii) rotor ohmic losses (iii) shaft power (iv) shaft torque and (v) efficiency.	5	1	L2
2 (a)	The frequency of the e.m.f in the stator of a 4 pole induction motor is 50Hz, and that in the rotor is 1.5 Hz. What is the slip, and at what speed is the motor running?	3	1	L2
2 (b)	Discuss in detail about the principle of operation of a 3-phase induction motor.	7	1	L2
3 (a)	Explain the concept of Rotating magnetic Field?	5	1	L2
3 (b)	Discuss the points of similarities between a transformer and an induction motor. Hence, explain why an induction machine is called a generalized transformer?	5	1	L1
4 (a)	Show that the maximum torque occurs at a slip $s = r_2 / x_2$ and further show that T_{max} is independent of s.	5	2	L1
4 (b)	Outline the principle of speed control of a 3-phase induction motor by V/f method and draw the corresponding torque-speed characteristics, list applications and limitations of this method.	5	2	L2
5 (a)	Derive the expression for torque of a poly-phase induction motor during running and starting conditions?	3	2	L1
5 (b)	Explain the principle of speed control of a 3-phase induction motor by V/f method and draw the corresponding torque-speed characteristics and discuss the applications and limitations of these methods.	7	2	L2
6 (a)	Prove that maximum internal torque developed by the poly-phase induction motor does not depend on the rotor circuit resistance?	3	2	L2
6 (b)	Explain briefly about the tests to be conducted on three phase induction motor to get its equivalent circuit?	7	2	L2
7 (a)	Explain about the double-revolving field theory for single phase induction motors.	6	3	L2
7 (b)	Why single phase induction is not self starting motor?	4	3	L2
8 (a)	Using double field revolving field theory explain the torque-slip characteristics of a single phase induction motor and prove that it cannot produce starting torque?	7	3	L2
8 (b)	Draw equivalent circuit of a single phase induction motor during starting and running conditions.	3	3	L1
9 (a)	Outline the construction features and operation of single phase induction motor?	5	3	L2
9 (b)	Explain the principle of phase splitting?	5	3	L2

D. Srinivas Reddy

1. RVSL
2. DSV
3.

Signature of DAC

GVP COLLEGE OF ENGG. FOR WOMEN VSP

Head
Dept. of Electrical & Electronics
College of Engineering for Women
Madhurawada
VISAKHAPATNAM

GVP COLLEGE OF ENGINEERING FOR WOMEN
MADHURAWADA::VISAKHAPATNAM

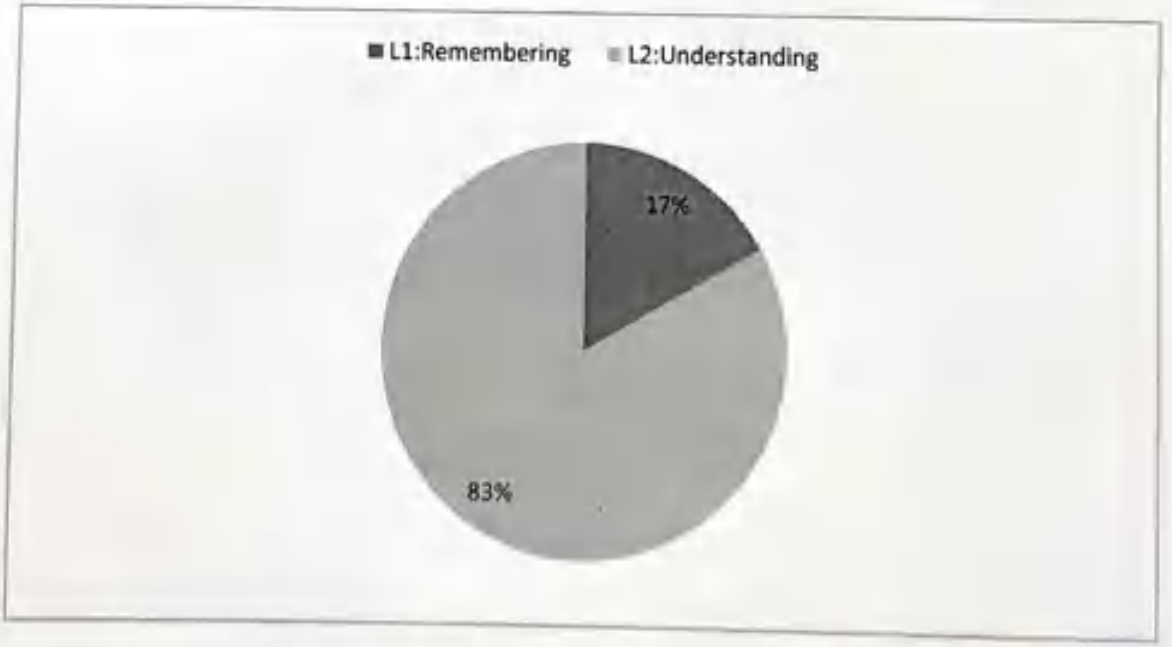
Department of Electrical and Electronics Engineering

II B. Tech. (II Semester) Mid Examinations-I, January 2020
Descriptive Type Examination (R-16 Regulation)

Subject: **Electrical Machines-II** Date: 25-01-2020
Sections: EEE Duration: 90 min.
Name of the Instructor/Faculty: **D. Srinivas Reddy**

Answer all the Questions Max marks: 30

Q. No.	Questions	Marks	CO	BL
1 (a)	Explain why an induction motor cannot develop torque when running at synchronous speed? Define the slip speed of an induction motor and deduce how the frequency of the rotor currents and magnitude of the rotor emf are related to slip?	5	1	L2
1 (b)	A three phase, 400 V, 50 Hz, 6 pole induction motor takes a power input of 35 kW at its full load speed of 980 rpm. The total stator losses are 1 kW and the friction and windage losses are 1.5 kW. Calculate (i) slip (ii) rotor ohmic losses (iii) shaft power (iv) shaft torque and (v) efficiency	5	1	L2
2 (a)	Derive the expression for torque of a poly-phase induction motor during running and starting conditions?	3	2	L1
2 (b)	Explain the principle of speed control of a 3-phase induction motor by V/f method and draw the corresponding torque-speed characteristics and discuss the applications and limitations of these methods.	7	2	L2
3 (a)	Explain about the double-revolving field theory for single phase induction motors.	6	3	L2
3 (b)	Why single phase induction is not self starting motor?	4	3	L2



Bloom's Level Wise Marks Distribution

GVP COLLEGE OF ENGINEERING FOR WOMEN
MADHURAWADA::VISAKHAPATNAM

Department of Electrical and Electronics Engineering

II B. Tech. (II Semester) Mid Examinations-I, January 2020
Descriptive Type Examination (R-16 Regulations)

Subject: Electrical Machines-2 | Date: 25-01-2020
Branch(s): EEE | Duration: 90 min.
Name of the Instructor/Faculty: **D Srinivas Reddy**

Scheme of Evaluation | Max marks: 30

Q. No.	Questions with Scheme of Evaluation
1 (a)	**Explain why an induction motor cannot develop torque when running at synchronous speed? Define the slip speed of an induction motor and deduce how the frequency of the rotor currents and magnitude of the rotor emf are related to slip? [CO1]** According to Faraday's law an emf induced in any circuit is due to the rate of change of magnetic flux linkage through the circuit. As the rotor winding in an induction motor are either closed through an external resistance or directly shorted by end ring, and cut the stator rotating magnetic field, an emf is induced in the rotor copper bar and due to this emf a current flows through the rotor conductor. Here the relative speed between the rotating flux and static rotor conductor is the cause of current generation; hence as per Lenz's law, the rotor will rotate in the same direction to reduce the cause, i.e., the relative velocity. Thus from the **working principle of three phase induction motor**, it may be observed that the rotor speed should not reach the synchronous speed produced by the stator. If the speeds become equal, there would be no such relative speed, so no emf induced in the rotor, and no current would be flowing, and therefore no torque would be generated. Consequently, the rotor cannot reach the synchronous speed. The difference between the stator (synchronous speed) and rotor speeds is called the slip. The rotation of the magnetic field in an induction motor has the advantage that no electrical connections need to be made to the rotor. .....(3M) **Slip speed:** The slip in an induction motor is the difference between the main flux speed and their rotor speed. The symbol S represents the slip. It is expressed by the percentage of synchronous speed. Mathematically, it is written as $N_s - N$ .....(1M) Expression for rotor current and emf .....(1M)
1 (b)	**A three phase, 400 V, 50 Hz, 6 pole induction motor takes a power input of 35 kW at its full load speed of 980 rpm. The total stator losses are 1 kW and the friction and windage losses are 1.5 kW. Calculate (i) slip (ii) rotor ohmic losses (iii) shaft power (iv) shaft torque and (v) efficiency [CO1]** Given, Supply line voltage (V_l) = 400V Supply frequency(f) = 50Hz Poles(P) = 6 Input Power(P_{in}) = 35kw

	full load speed(N) = 980 rpm Total Stator losses(Pst_losses)= 1kw friction and windage losses (Pmech_losses)= 1.5kw $N_s = 120f/P = 120 * 50/6 = 1000$ rpm (i) $s = (Ns - N)/Ns = (1000-980)/1000 = 0.02$ .....(1M) (ii) air gap power (P_g) = P_{in} – Pst_losses = 34kw Rotor ohmic losses (Prcu) = $P_g * s$ = 0.68kw ..(1M) (iii) shaft power (Psh) = P_g – Prcu – Pmech_losses = 31.82kw ...(1M) (iv) shaft torque(Tsh) = (Psh /2*pie*N) *60 = 310 N-m..(1M) (v)% efficiency = Psh/Pin * 100 = 90.9 % ...(1M)
2 (a)	Derive the expression for torque of a poly-phase induction motor during running and starting conditions? **Torque Equations** The gross torque Tg developed by an induction motor is given by; $T \propto \phi I_r \cos\phi_2$. where, E_r = rotor emf per phase under running condition = sE_2. (s=slip) I_r = rotor current per phase under running condition reactance per phase under running condition will be = sX_2 therefore, $$I_r = \frac{E_r}{Z_r} = \frac{sE_2}{\sqrt{(R_2^2 + (sX_2)^2)}} \quad \text{and} \quad \cos\phi_2 = \frac{R_2}{Z_r} = \frac{R_2}{\sqrt{(R_2^2 + (sX_2)^2)}}$$ $$T = \frac{k\phi sE_2 R_2}{\sqrt{(R_2^2 + (sX_2)^2)}}$$ as, $\phi \propto E_2$. $$T = \frac{k_1 sE_2^2 R_2}{\sqrt{(R_2^2 + (sX_2)^2)}} = \frac{3}{2\pi Ns} \frac{sE_2^2 R_2}{\sqrt{(R_2^2 + (sX_2)^2)}}$$ .....[2M] **Starting Torque** The torque developed at the instant of starting of a motor is called as starting torque. Starting torque may be greater than running torque in some cases, or it may be lesser. We know, $T = k_1 E_2 I_2 \cos\phi_2$. let, R2 = rotor resistance per phase X2 = standstill rotor reactance $$Z_2 = \sqrt{(R_2^2 + X_2^2)} = \text{rotor impedence per phase at standstill}$$ then,

$$I_2 = \frac{E_2}{Z_2} = \frac{E_2}{\sqrt{(R_2^2 + X_2^2)}} \quad \text{and} \quad \cos\phi_2 = \frac{R_2}{Z_2} = \frac{R_2}{\sqrt{(R_2^2 + X_2^2)}}$$

Therefore, starting torque can be given as,

$$Tst = k_1 E_2 \frac{E_2}{\sqrt{(R_2^2 + X_2^2)}} X \frac{R_2}{\sqrt{(R_2^2 + X_2^2)}} = \frac{k_1 E_2^2 R_2}{R_2^2 + X_2^2}$$

The constant k1 = 3 / 2πNs

$$Tst = \frac{3}{2\pi Ns} \frac{E_2^2 R_2}{R_2^2 + X_2^2}$$

.....[2M]

2 (b)

Explain the principle of speed coutrol of a 3-phase induetion motor hy V/f method and draw the eorresponding torque-speed characteristics aud discuss the applications aud limitations of these methods.

The basic formulas of speed and torque of three phase induction motor are

Synchronous Speed

$$N_s = \frac{120f}{P}$$

Where, f = frequency and P is the number of poles

The speed of induction motor is given by,

$$N = N_s(1 - s)$$

Where,

N is the speed of the rotor of an induction motor,

N_s is the synchronous speed,

S is the slip.

The torque produced by three phase induction motor is given by,

$$T = \frac{3}{2\pi N_s} X \frac{sE_2^2 R_2}{R_2^2 + (sX_2)^2}$$

When the rotor is at standstill slip, s is one. So the equation of torque is,

$$T = \frac{3}{2\pi N_s} X \frac{E_2^2 R_2}{R_2^2 + X_2^2}$$

Where,

E_2 is the rotor emf

N_s is the synchronous speed

R_2 is the rotor resistance

X_2 is the rotor inductive reactance .....(1M)

The Speed of Induction Motor is changed from Both Stator and Rotor Side. One of the speed control method is :

V / f control or frequency control:

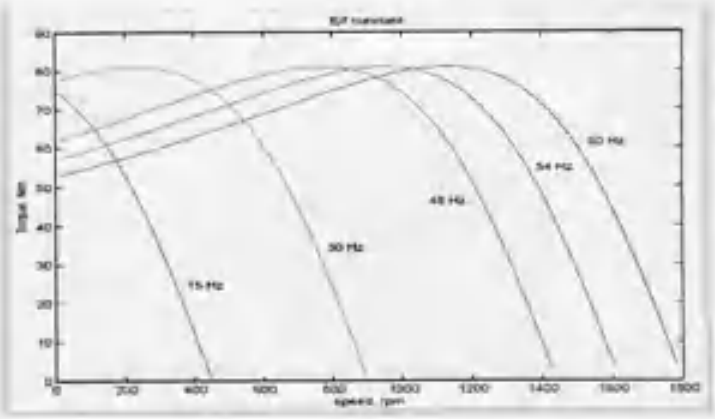
Whenever three phase supply is given to three phase induction motor rotating magnetic field is produced which rotates at synchronous speed given by

$$N_s = \frac{120f}{P}$$

In three phase induction motor emf is induced by induction similar to that of transformer which is given by

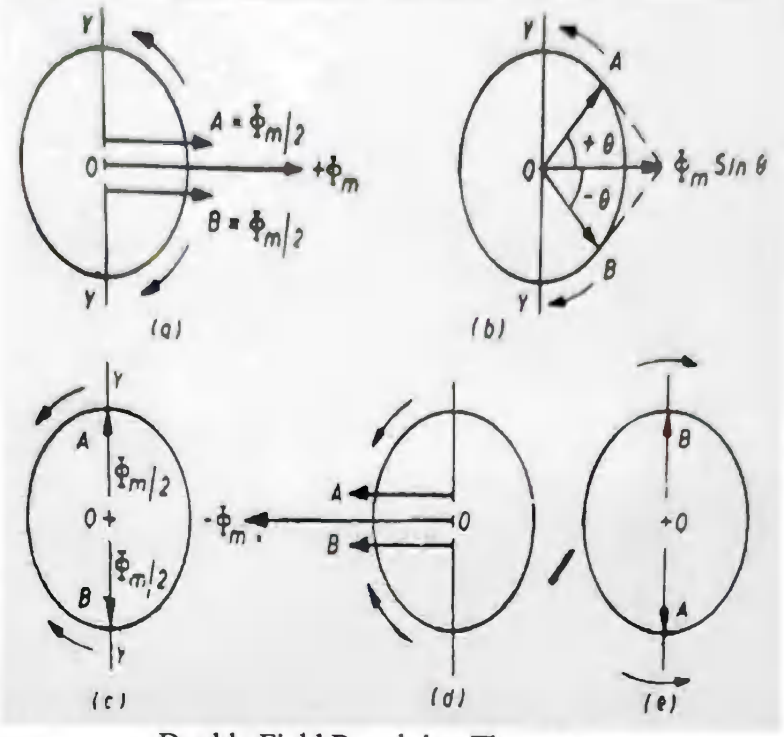
$$E \text{ or } V = 4.44\phi KTf \text{ or } \phi = \frac{V}{4.44KTf}$$

Where, K is the winding constant, T is the number of turns per phase and f is frequency. Now if we change frequency synchronous speed changes but with decrease in frequency flux will increase and this change in value of flux causes saturation of rotor and stator cores which will further cause increase in no load current of the motor . So, its important to maintain flux , φ constant and it is only possible if we change voltage. i.e if we decrease frequency flux increases but at the same time if we decrease voltage flux will also decease causing no change in flux and hence it remains constant. So, here we are keeping the ratio of V/f as constant. Hence its name is V/ f method. For controlling the speed of three phase induction motor by V/f method we have to supply variable voltage and frequency which is easily obtained by using converter and inverter set. .....(3M)



.....(1M)

3 (a)	**Explain about the double-revolving field theory for single phase induction motors. [CO3]** **Double Field Revolving Theory** This theory makes use of the idea that an alternating uniaxial quantity can be represented by two oppositely rotating vectors of half magnitude. So, an alternating sinusoidal flux can be represented by two revolving fluxes, each equal to half the value of alternating flux and each rotating synchronously in opposite directions.



Double Field Revolving Theory

As shown in Fig. (a), let the alternating flux have a maximum value of φ_m . Its component fluxes A and B will each be equal to $\varphi_m/2$ revolving in anticlockwise and clockwise directions respectively.

After some time when A and B would have rotated through the angles $+\theta$ and $-\theta$ as in Fig (b), the resultant flux would be

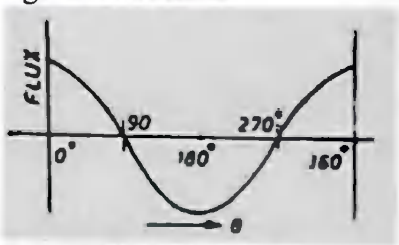
Resultaut Flux = $2\times(\varphi_m/2)$ sin (20/2) = φ_m sin 0 .....(2M)

After a quarter cycle of rotation, fluxes A and B will be oppositely directed as shown in Fig(c) so that the resultant flux would be zero.

After half a cycle, fluxes A and B will have a resultant of $-2\times(\varphi_m/2) = -\varphi_m$.

After three-quarters of a cycle, again the resultant is zero as shown in Fig(e) and so on.

If we plot the values of resultant flux against θ between limits $\theta=0°$ to $\theta=360°$, then a curve similar to the one shown in the figure is obtained.

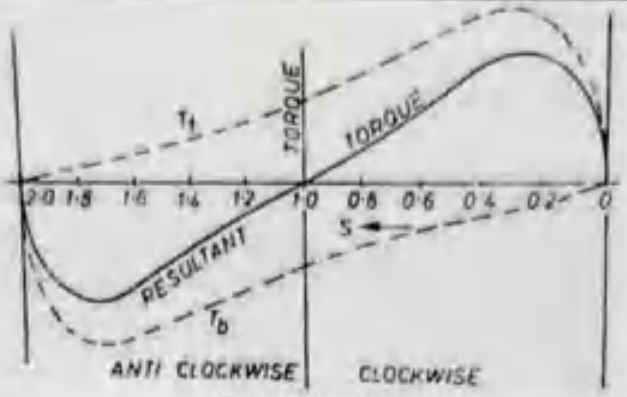


Alternating Flux

That is why an alternating flux can be looked upon as composed of two revolving fluxes each of half the value and revolving synchronously in opposite directions.

It may be noted that if the slip of the rotor is *s* with respect to the forward rotating flux (i.e. one which rotates in the same direction as the rotor) then its slip with respect to backward rotating flux is (2-s).

.....(2M)



Torque vs Slip

Each of the two component fluxes while revolving around the stator cuts the rotor, induces an emf, and thus produces its own torque.
Obviously, the two torques (called forward and backward torques) are oppositely-directed so that the net or resultant torque is equal to their difference.
Hence, Tf and Tb are numerically equal but being oppositely directed, produce no resultant torque. **That explains why there is no starting torque in a single-phase motor**.
However, if the rotor is started somehow, say, in the clockwise direction, the clockwise torque starts increasing and, at the same time, the anticlockwise torque starts decreasing. Hence, there is a certain amount of net torque in the clockwise direction which accelerates the motor to full speed.
.....(2M)

3 (b)

Why single phase induction is not self starting motor? [CO3]

When single-phase AC supply is given to stator winding. It produces alternating flux i.e. which alternates along one space axis only. It is not synchronously revolving (or rotating) flux, as in case of 3 phase stator winding, the fed cannot produce rotation. Hence single phase induction motor is not self-starting.
.....(2M)

To overcome this problem and to make the motor self-starting, it is temporarily converted into two-phase motor during starting. For this purpose, the stator of 1 phase motor is provided with extra winding known as starting winding in addition to the main winding. These two winding are placed across the single phase supply.
This winding is so arranged that the phase difference between the currents in the two stator winding is very large. Hence motor behaves like a two-phase motor. The two current produces a revolving flux and this makes the 1 Ø motor self-starting.
.....(2M)

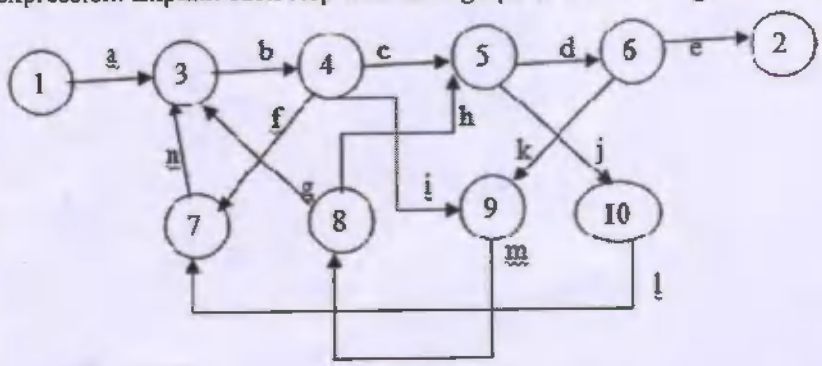
D. Srinivasa

GVP COLLEGE OF ENGINEERING FOR WOMEN
MADHURAWADA::VISAKHAPATNAM
Department of Computer Science and Engineering
III B.Teeh (II Semester) Mid Examinations-I, JAN 2020
Deseriptive Type Examination (R-16 Regulations)

Subject: **STM** Code: R1632054 Date: 23-1-2020
Seetion: CSE-1,CSE-2 Time: 3.15-4.45PM Duration: 90 min.
Name of the Faculty: **Br. M.Bhanu Sridhar, Mrs. K. N. S. Chitra**

NBA Subject Code	Course Outcomes	
CS604	CO1	**Extend** Software Testing to software engineeringwith the eoncepts of Flow graphs and Path Testing.
	CO2	**Interpret** the concepts of transaction flow testing and **experiment with** the conccpts of data flow testing in real-time situations.
	CO3	**Identify** the strategies in domain testing and **extend** them to path products and expressions.

Question Bank

Q. No.	Questions	Marks	CO	BL
1	**Illustrate** the myths and facts that exist in a tester's mind.	10	1	L2
2 (a)	**Explain** the taxooomy of bugs. Disouss about data bugs in detail.	4	1	L2
2 (b)	**List** the eoncepts of path testing and **outline** the idea of path predicates, path sensitization and path instrumentation.	6	1	L2
3 (a)	**Outline** coneept of achievable paths using an example.	3	1	L2
3 (b)	**Compare and eontrast** different dichotomies in software testing.	7	1	L2
4 (a)	**Explain** about transaction flow testing through an example.	5	2	L2
4 (b)	**Identify** different types of data flow testing and discuss about different anomalies that are possible.	5	2	L3
5(a)	**Interpret** the complications of transaction flow testing using appropriate diagrams.	4	2	L2
5(b)	**Construet** the diagram that depicts rclative strengths of structural test strategies in an order.	6	2	L3
6	**Organize** different transaction flow testing techniques in the respective order.	10	2	L3
7	**Make use of** the following coneepts to explain them in detail. (a) Nice and ugly domains (b) Domains and ioterfaces testing	10	3	L3
8 (a)	**Utilizingreduction** prooedure convert flow graph whose links are labeled into a path expression. Explain eaeh step with flow graph as shown in figure below. 	7	3	L3
8 (b)	**Outline** the probability metric details and disouss about the formulae.	3	3	L2
9	**Apply** the concept of Huang's theorem on any flow graph and identify its limitations.	10	3	L3

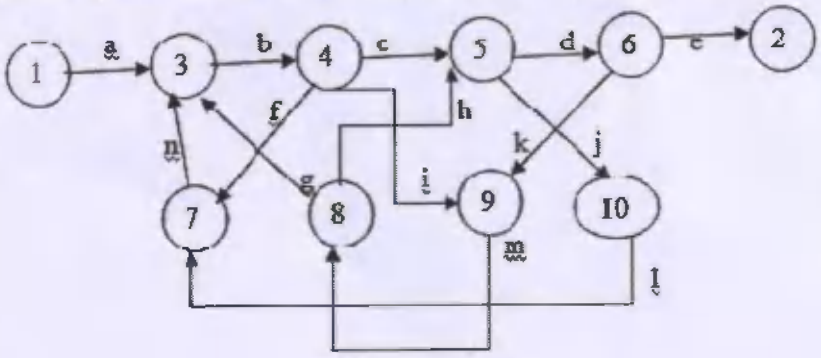
Name of the Faculty Signature: **Dr.M.Bhanu Sridhar, Mrs. K. N. S. Chitra**

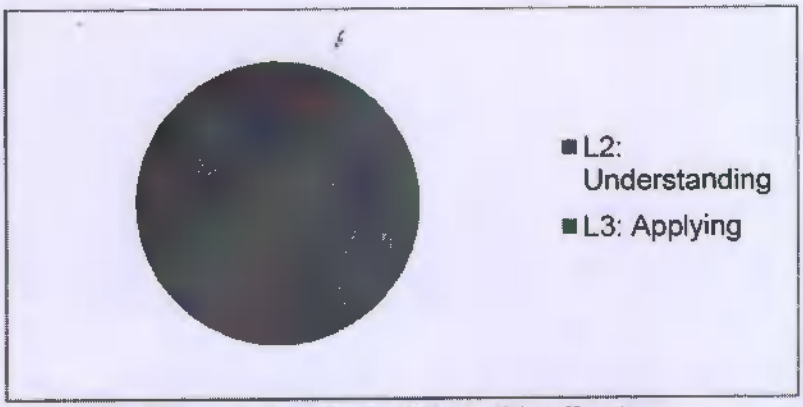
GVP COLLEGE OF ENGG FOR WOMEN VISAKHAPATNAM

GVP COLLEGE OF ENGINEERING FOR WOMEN
MADHURAWADA::VISAKHAPATNAM
Department of Computer Scienee and Engineering
III B.Tech. (II Semester) Mid Examinations-I, JAN 2020
Deseriptive Type Examination (R-16 Regulations)

Subject: **STM** Code:R1632054 Date: 23-1-2020
Sections: CSE-1, CSE-2 Time: 3.15-4.45PM Duration: 90 min.
Name of the Instructor/Faculty: **Dr. M. Bhanu Sridhar, Mrs. K. N. S. Chitra**

Answer all the Questions Max marks: 30

Q. No.	Questions	Marks	CO	BL
1 (a)	**Explain** the taxonomy of bugs. Diseuss about data bugs in detail.	4	1	2
1 (b)	**List** the concepts of path testing and **outline** the idea of path predieates, path sensitization and path instrumentation.	6	1	2
2 (a)	**Explain** about transaction flow testing through an example.	5	2	2
2 (b)	**Identify** different types of data flow testing and discuss about different anomalies that are possible.	5	2	3
3 (a)	**Utilizing** reduction procedure convert flow graph whose links are labeled into a path expression. Explain each step with flow graph as shown in figure below. 	7	3	3
3 (b)	**Outline** the probability metric details and discuss about the formulae.	3	3	2



Bloom's Level wise MarksDistribution

Name of the Instructor/Faculty Signature: **Dr. M. B. Sridhar, Ms. K. N. S. Chitra**

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GVP COLLEGE OF ENGINEERING FOR WOMEN
MADHURAWADA::VISAKHAPATNAM

SCHEME OF VALUATION

NAME OF EXAMINATION : III B. Tech II Semester R16 Mid – 1
DATE OF EXAMINATION : 23-01-2020
NAME OF SUBJECT :SOFTWARE TESTING METHODOLOGIES
SUBJECT CODE : R1632054
BRANCHES : CSE
MAX. MARKS : 30
NAME OF THE COURSE INSTRUCTOR: Dr. M. Bhanu Sridhar, Ms. K. N. S. Chitra

Q.No.	Sub Q. No.	Scheme of Evaluation	Marks allotted
1.	a)	• Classification of the bugs • Requirements, features and functionality bugs • Data bugs, their types and comparison • Information, parameter, control, content bugs etc.	1 1 1 1
	b)	• Concepts of path testing • Path predicates • Path sensitization • Path instrumentation	2 2 1 1
2.	a)	• Examples of transaction flows • Transaction flow testing techniques • Inspections, walkthroughs and overviews • Path selection, sensitisation and instrumentation	1 2 1 1
	b)	• Types of data flow testing like du, dk, acu+p etc. • Possible Anomalies • Examples	2 2 1
3.	a)	• Application of the reduction processes to obtain a reduced expression for the provided problem	7
	b)	• Table of Probability metrics • Explanation of the formula	1 2

(Dr. M. B. Sridhar)

K. N. S. Chitra

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GVP COLLEGE OF ENGINEERING FOR WOMEN (JG)
MADHURAWADA::VISAKHAPATNAM

Department of Computer Science and Engineering
III B. Tech. (II Semester) Mid Examinations-II, October 2020
Question Bank (R-16 Regulations)

Subject: **STM** Sections: CSE

Name of the Instructor/Faculty: **Dr. M. Bhanu Sridhar, Mrs. K. N. S. Chitra**

1 (a) Explain about grammar for formats using BNF notation using examples.
1 (b) Illustrate the concept of decision table with necessary definitions and tables.
2 (a) Demonstrate the idea of a state graph/Finite State Machine with example of Tape-Driver Routine.
2 (b) Explain about graph matrices in detail with an example.
3 (a) Organize a batch test on any application using the testing tool WinRunner.
3 (b) Discuss about the concepts of test automation with an emphasis on WinRunner.
4. Organize a data-based test on any application using the testing tool WinRunner.
5. Develop a test placing bitmap checkpoints for the application of Flight 1A.
6. Outline the basics of automated testing and usage of WinRunner in recording, running and debugging tests.
7. (a) What is meant by functional testing and structural testing? Discuss its differences.
(b) What is meant by test design bugs? Discuss the remedies for test bugs
8(a) Present the V-V diagram with brief explanations for all stages.
(b) Describe the FSM with an example utilizing diagram, state table and some test cases.

Dr. M. B. Sridhar, Mrs. KNS. Chitra

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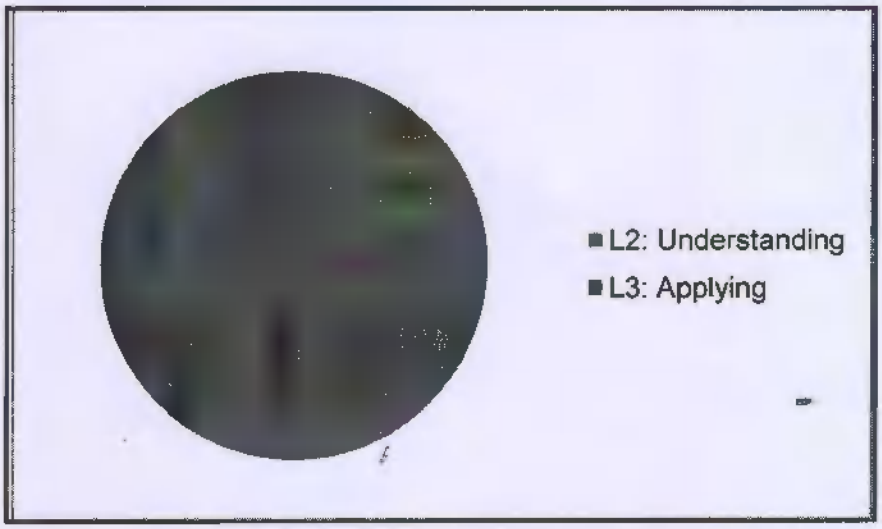
GVP COLLEGE OF ENGINEERING FOR WOMEN(JG)
MADHURAWADA::VISAKHAPATNAM

Department of Compnter Seienee and Engineering
III B. Tech. (II Semester) Mid Examinations-II, Oetober 2020
Deseriptive Type Examination (R-16 Regulations)

Subject: **STM** Date: 15-10-2020
Sections: CSE Duration: 90 min.
Name of the Instructor/Faculty: **Dr.M.Bbanu Sridhar, Mrs. K. N. S. Cbitra**

Answer all the Questions MaximumMarks: 30

Q. No.	Questions	Marks	CO	BL
1 (a)	Explain about grammar for formats using BNF notation using examples.	5	4	L2
1 (b)	Illustrate the concept of decision table with necessary definitions and tables.	5	4	L2
2 (a)	Demonstrate the idea of a state graph/Finite State Machine with example of Tape-Driver Routine.	5	5	L2
2 (b)	Explain about graph matrices in detail with an example.	5	5	L2
3 (a)	Organize a batch test on any application using the testing tool WinRunner.	5	6	L3
3 (b)	Develop a test placing bitmap checkpoints for the application of Flight 1A.	5	6	L3



Bloom's Level wise MarksDistribution

GVP COLLEGE OF ENGG. FOR WOMEN * VISAKHAPATNAM *

Name of the Instructor/Faculty Signature
1. **Dr.M.Bhanu Sridhar –**
2. **Mrs.Knppili N Satya Chitra–**

GVP COLLEGE OF ENGINEERING FOR WOMEN
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DETAILED SCHEME OF VALUATION

NAME OF EXAMINATION : III/II B. Tech. Mid-Exams-II, OCTOBER 2020 (R16)

DATE OF EXAMINATION : 15/10/2020

NAME OF SUBJECT : SOFTWARE TESTING METHODOLOGIES

SUBJECT CODE :R1632054

DEPARTMENT(S) : CSE

MAX. MARKS : 30

NAME OF THE COURSE INSTRUCTORS: Dr.M. Bhanu Sridhar, Mrs. KNS. Chitra

Q. No.	Sub Q. No.	Detailed Scheme of Evaluation	Marks allotted
1.	a)	• Basics of Backus-Naur Form • Elements, operators, repetitions • Example for the BNF usage	2 2 1
	b)	• Structure of a Decision Table and its explanation • Usage of Decision Table • Problem to Decision Table conversion example	2 1 2
2.	a)	• State graphs and FSM details • Tape-driver routine (erase, error write etc.) with diagram	2 3
	b)	• Definition of graph matrix (row, eolumn, cell) • Application of graph matrices in testing • Example with a diagram and matrix	2 1 2
3.	a)	• The definition and need of batch test • Programming in batch mode (in TSL form) • Diagram depicting batch test usage (WinRunner)	1 2 2
	b)	• The need of placing bitmap check points in Flight 1A • Procedure for placing the checkpoint(s) and output	1 4

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GVP COLLEGE OF ENGINEERING FOR WOMEN(JG)
MADHURAWADA::VISAKHAPATNAM

Department of Computer Science and Engineering
III B. Tech. (II Semester) Mid Examinations-II, October 2020
Objeetive Type Examination (R-16 Regulatious)

Subject: **STM** Date: 15-10-2020
Sections: CSE Duration: 20 min.
Name of the Instructor/Faculty: **Dr.M. Bbanu Sridhar, Mrs. K. N. S. Cbitra**

Answer all the Questions Maximum Marks: 20

1. Boolean algebra is also known as ___. [ ]
   a) Caleulus b) Sentential Calculus c) General Calculus d) Multiple Calculus
2. In artificial intelligence, processing is done by a program called ____. [ ]
   a)Math Engine b) Fault Logic c)Interference Engine d) Fuzzy Logic
3. A limited-entry decision table consists of ___ areas. [ ]
   a)Two b) Three c) Four d) Five
4. The condition stub is a list of ___. [ ]
   a)Conditions b) Condition names c)Links d) Link Names
5. The ___ names the actions that will be initiated as per conditions. [ ]
   a)Action Stub b) Action Entry c)Condition Stub d) Condition Entry
6. In a condition entry, if a rule is specified as I, it means ___. [ ]
   a)Improbable b) Important c)Immaterial d) Irretrievable
7. The default action to be taken is speeified by ___ rule. [ ]
   a)General b) Specific e)Matrix d) Default
8. Logic form can be expressed as IF predicate THEN ___. [ ]
   a)Node b) Exit c)Aetion d) Reaction
9. ___ can be implemented as table-driven software. [ ]
   a)Finite State Machine b) Graphs c)Graph Matrices d) Matrices
10. ___ are represented by nodes. [ ]
    a)Positions b) Levels c)States d) Rows
11. The set of different encoded input values is called ___. [ ]
    a)Input Values b) Inpul Set c)Input Alphabet d) Inputs
12. The combination of state and input code is known as ___. [ ]
    a)Stale Producls b) State Symbol Products c)State Codes d) Literals
13. If we can return to the starting state by a sequence of inputs, that state graph is ___. [ ]
    a)Immaterial b) Good c)Bad d) Self-looped

14. An unimportant part of a state graph is termed as ___. [ ]

a)Wrong b) Block c)Blob d) Clob

15. If two transitions are unspecified, they are termed as ___. [ ]

a)Transition Bugs b) Improbable Transition c)Blob d) Nonc

16. A state that cannot be left once entered is known as __. [ ]

a)Closed State b) Discarded State c)Dead State d) Overlapped State

17. A state that cannot be reached is known as ___ state. [ ]

a)Impossible State b) Dead State c)Long State d) Discarded State

18. Noting all possible paths is known as path ___. [ ]

a)Instrumentation b) Implementation c)Tracing d) Covering

19. The intersection of two matrices is denoted by___. [ ]

a)A#B b) A * B c)A/B d) A^B

20. A___ is a property that exists between two objects of interest. [ ]

a)Connection b) Relation e)Link d) None

GVP COLLEGE OF ENGINEERING FOR WOMEN(JG)
MADHURAWADA::VISAKHAPATNAM

Department of Computer Science and Engineering
III B. Tech. (II Semester) Mid Examinations-II, October 2020
Objective Type Examination Key (R-16 Regulations)

Subject: **STM** Date: 15-10-2020
Sections: CSE Duration: 20 min.
Name of the Instructor/Faculty: **Dr.M. Bhanu Sridhar, Mrs. K. N. S. Chitra**

Answer all the Questions MaximumMarks: 20

----------****** The Answers for the objective questions given below are made bold ******----------

1. Boolean algebra is also known as ___. [ ]
   a) Calculus **b) Sentential Calculus** c) General Calculus d) Multiple Calculus
2. In artificial intelligence, processing is done by a program called ____. [ ]
   a)Math Engine b) Fault Logic **c) Interference Engine** d) Fuzzy Logic
3. A limited-entry decision table consists of ___ areas. [ ]
   a)Two b) Three **c) Four** d) Five
4. The condition stub is a list of ___. [ ]
   a)Conditions **b) Condition names** c)Links d) Link Names
5. The ___ names the actions that will be initiated as per conditions. [ ]
   a) Action Stub b) Action Entry c)Condition Stub d) Condition Entry
6. In a condition entry, if a rule is specified as I, it means ___. [ ]
   a)Improbable b) Important **c) Immaterial** d) Irretrievable
7. The default action to be taken is specified by ___ rule. [ ]
   a)General b) Specific c)Matrix **d) Default**
8. Logic form can be expressed as IF predicate THEN ___. [ ]
   a)Node b) Exit **c) Action** d) Reaction
9. ___ can be implemented as table-driven software. [ ]
   a) Finite State Machine b) Graphs c)Graph Matrices d) Matrices
10. ___ are represented by nodes. [ ]
    a)Positions b) Levels **c) States** d) Rows
11. The set of different encoded input values is called ___. [ ]
    a)Input Values b) Input Set **c) Input Alphabet** d) Inputs
12. The combination of state and input code is known as ___. [ ]
    a)State Products b) State Symbol Products **c) State Codes** d) Literals
13. If we can return to the starting state by a sequence of inputs, that state graph is ___. [ ]

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a)Immaterial **b) Good** c)Bad d) SeIf-looped

I4. An unimportant part of a state graph is termed as ___. [ ]

a)Wrong b) Block **c) BIob** d) Clob

I5. If two transitions are unspeeified, they are termed as ___. [ ]

a) Transition Bogs b) Improbable Transition c)Blob d) None

16. A state that cannot be left once entered is known as __. [ ]

a)Closed State b) Discarded State **e) Dead State** d) Overlapped State

I7. A state that cannot be reached is known as ___ state. [ ]

a) ImpossibIe State b) Dead State c)Long State d) Discarded Statc

18. Noting all possible paths is known as path ___. [ ]

a)Instrumentation b) Implemcntation **e) Traeing** d) Covering

I9. The intersection of two matrices is denoted by___. [ ]

a) A#B b) A * **B** c)A/B d) A^B

20. A___ is a property that exists betwcen two objects of interest. [ ]

a)Connection b) Relation **e) Link** d) None

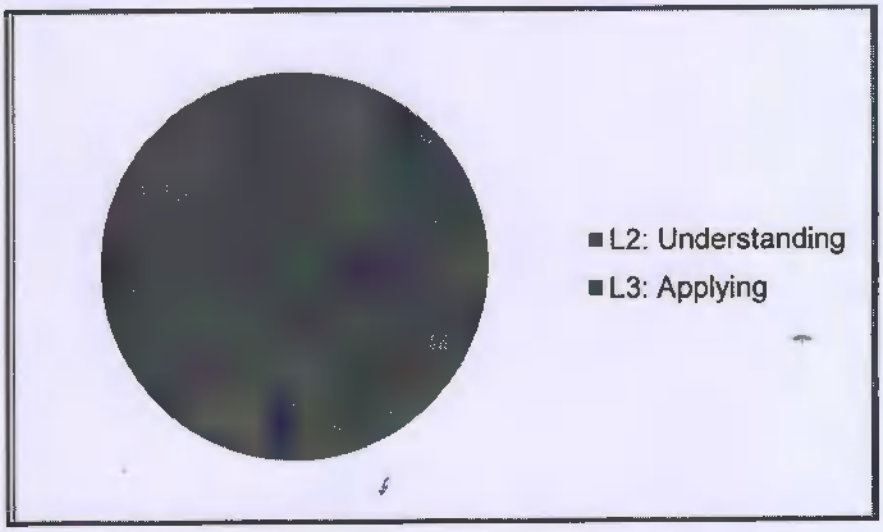
GVP COLLEGE OF ENGINEERING FOR WOMEN (JG)
MADHURAWADA::VISAKHAPATNAM

Department of Computer Science and Engineering
III B. Tech. (II Semester) Mid Examinations-II, October 2020
Descriptive Type Examination (R-16 Regulations)

Subject: **STM** Date: 15-10-2020
Sections: CSE Duration: 90 min.
Name of the Instructor/Faculty: **Dr. M. Bhann Sridhar, Mrs. K. N. S. Chitra**

Answer all the Questions Maximum Marks: 30

Q. No.	Questions	Marks	CO	BL
1 (a)	Explain about grammar for formats using BNF notation using examples.	5	4	L2
1 (b)	Illustrate the concept of decision table with necessary definitions and tables.	5	4	L2
2 (a)	Demonstrate the idea of a state graph/Finite State Machine with example of Tape-Driver Routine.	5	5	L2
2 (b)	Explain about graph matrices in detail with an example.	5	5	L2
3 (a)	Organize a batch test on any application using the testing tool WinRunner.	5	6	L3
3 (b)	Discuss about the concepts of test automation with an emphasis on WinRunner.	5	6	L3



Bloom's Level wise MarksDistribution

GVP COLLEGE OF ENGG. FOR WOMEN * VISAKHAPATNAM *

Name of the Instructor/Faculty Signature
1. **Dr.M.Bhanu Sridhar –**
2. **Mrs. Kuppili N Satya Chitra–**

GVP COLLEGE OF ENGINEERING FOR WOMEN
MADHURAWADA::VISAKHAPATNAM

DETAILED SCHEME OF VALUATION

NAME OF EXAMINATION : III/II B. Tech. Mid-Exams-II, OCTOBER 2020 (R16)

DATE OF EXAMINATION : 15/10/2020

NAME OF SUBJECT : SOFTWARE TESTING METHODOLOGIES

SUBJECT CODE :R1632054

DEPARTMENT(S) : CSE

MAX. MARKS : 30

NAME OF THE COURSE INSTRUCTORS: Dr.M. Bhanu Sridhar, Mrs. KNS. Chitra

Q. No.	Sub Q. No.	Detailed Scheme of Evaluation	Marks allotted
1.	a)	• Basics of Baekus-Naur Form • Elements, operators, repetitions • Example for the BNF usage	2 2 1
	b)	• Structure of a Decision Table and its explanation • Usage of Decision Table • Problem to Decision Tabie conversion example	2 1 2
2.	a)	* State graphs and FSM details • Tape-driver routine (erase, error write ete.) with diagram	2 3
	b)	• Definition of graph matrix (row, column, celi) • Application of graph matrices in testing • Example with a diagram and matrix	2 1 2
3.	a)	• The definition and need of batch test • Programming in batch mode (in TSL form) * Diagram depicting batch test usage (WinRunner)	1 2 2
	b)	• The need of test automation • Advantages of automated testing over static testing • Basics of WinRunner tool	1 1 3

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MADHURAWADA :: VISAKHAPATNAM

Department of Computer Science and Engineering
III B. Tech. (II Semester) Mid Examinations-II, October 2020
Objective Type Examination (R-16 Regulations)

Subject: **STM** Date: 15-10-2020
Sections: CSE Duration: 20 min.
Name of the Instructor/Faculty: **Dr.M. Bhanu Sridhar, Mrs. K. N. S. Chitra**
Name of the Subject: Rollno:

Answer all the Questions Maximum Marks: 20

1. ___ cause most contradictions in decision-tables. [ ]
   a) Immaterial Cases b) Wrong Actions c) Irregular cases d) Wrong Conditions
2. It is not possible to change the order of evaluation of ___. [ ]
   a)Nodes b) Links c)Loops d) Predicates
3. Predicates are based on ___ operators. [ ]
   a)Conditional b) Relational c) Logical d) Arithmetic
4. A + AB = ___. [ ]
   a)A b) B c)AB d) None
5. Path dependence is obtained by ___ the predicate. [ ]
   a)Defusing b) Interpreting c)Reducing d) Adding
6. Beyond ___ variables, KV charts get cumbersome. [ ]
   a)4 b) 5 c)6 d) 7
7. Two boxes are ___ if they change in only one bit. [ ]
   a)Adjacent b) Distanced c)Closer d) Overlapped
8. As a result of inputs, a state graph is said to have made ___. [ ]
   a)Movements b) Transitions c)Transactions d) Transpositions
9. An outcome consists of an input, transition and ___. [ ]
   a)Output b) Final State c)New State d) New Link
10. State graph represented as a table is known as ___. [ ]
    a)State Transition Table b) Transition table c)Output Table d) Device Table
11. Problems/errors in a state graph are known as ___. [ ]
    a)State Errors b) State Problems c)State Impossibilities d) State Bugs
12. The difference between a programmers and a testers state count is known as___. [ ]
    a)Impossible States b) Wrong State c)Improbable States d) None
13. Two states are ___ if every sequence of inputs produces the same sequence of outputs. [ ]
    a)Equal b) Equivalent c)Exact d) Cleared

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14. Graph matrix is ___ with one row and one column for every node. [ ]

a)Square Matrix b) Node Matrix c)Node Graph d) Square Graph

15. The size of graph matrix equals the no. of ___. [ ]

a)Links b) Nodes c)Inputs d) Outputs

16. If there are several links between two nodes, the links are termed as ___. [ ]

a)Multiple b) Several c)Parallel d) Multiple & Several

17. If links are represented in a matrix as a binomial form, the matrix is known as___. [ ]

a)Application Matrix b) Connection Matrix c)General Matrix d) Binary Matrix

18. By interchanging the rows and columns, we obtain a matrix ___. [ ]

a)Intersection b) Union c)Connection d) Transpose

19. In a graph, relations are denoted by ___. [ ]

a)Links b) Nodes e)Parallel Links d) Multiple Links

20. If aRb and bRc implies aRc, the relation is known as ___. [ ]

a)Continuing b) Multiple c)Transitive d) Reflexive

GVP COLLEGE OF ENGINEERING FOR WOMEN(JG)
MADHURAWADA :: VISAKHAPATNAM

Department of Computer Science and Engineering
III B. Tech. (II Semester) Mid Examinations-II, October 2020
Objective Type Examination Key (R-16 Regulations)

Subject: **STM** Date: 15-10-2020
Sections: CSE Duration: 20 min.
Name of the Instructor/Faculty: **Dr.M.Bhanu Sridhar, Mrs. K. N. S. Chitra**

Answer all the Questions MaximumMarks: 20

----------****** The Answers for the objective questions given below are made bold ******----------

1. ___ cause most contradictions in decision-tables. [ ]
   a) Immaterial Cases b) Wrong Actions c) Irregular cases d) Wrong Conditions
2. It is not possible to change the order of evaluation of ___. [ ]
   a)Nodes b) Links c)Loops **d) Predicates**
3. Predicates are based on ___ operators. [ ]
   a)Conditional **b) Relational** c) Logical d) Arithmetic
4. A + AB = ___. [ ]
   a) A b) B c)AB d) None
5. Path dependence is obtained by ___ the predicate. [ ]
   a)Defusing **b) Interpreting** c)Reducing d) Adding
6. Beyond ___ variables, KV charts get cumbersome. [ ]
   a)4 b) 5 **c) 6** d) 7
7. Two boxes are ___ if they change in only one bit. [ ]
   a) Adjacent b) Distanced c)Closer d) Overlapped
8. As a result of inputs, a state graph is said to have made ___. [ ]
   a)Movements **b) Transitions** c)Transactions d) Transpositions
9. An outcome consists of an input, transition and ___. [ ]
   a)Output b) Final State **c) New State** d) New Link
10. State graph represented as a table is known as ___. [ ]
    a) State Transition Table b) Transition table c)Output Table d) Device Table
11. Problems/errors in a state graph are known as ___. [ ]
    a)State Errors b) State Problems c)State Impossibilities **d) State Bugs**
12. The difference between a programmers and a testers state count is known as___. [ ]
    a) Impossible States b) Wrong State c)Improbable States d) None
13. Two states are ___ if every sequence of inputs produces the same sequence of outputs. [ ]
    a)Equal **b) Equivalent** c)Exact d) Cleared

GVP COLLEGE OF ENGG FOR WOMEN * VISAKHAPATNAM *

14. Graph matrix is ___ with one row and one eolumn for every node. [ ]

a) Square Matrix b) Node Matrix c)Nodc Graph d) Square Graph

15. The size of graph matrix equals the no. of ___. [ ]

a)Links **b) Nodes** c)Inputs d) Outputs

16. If there are several links between two nodes, the links are termed as ___. [ ]

a)Multiple b) Several **c) Parallei** d) Multiple & Scveral

17. If links are represented in a matrix as a binomial form, the matrix is known as___. [ ]

a)Application Matrix **b) Counectiou Matrix** c)General Matrix d) Binary Matrix

18. By interchanging the rows and columns, we obtain a matrix ___. [ ]

a) Iutersection b) Union c)Connection d) Transpose

19. In a graph, relations are denoted by ___. [ ]

a) Links b) Nodes c)Parallel Links d) Multiple Links

20. If aRb and bRc implies aRc, the relation is known as ___. [ ]

a)Continuing b) Multiple **c) Transitive** d) Reflexive

GVP COLLEGE OF ENGINEERING FOR WOMEN
MADHURAWADA::VISAKHAPATNAM

Department of BS&H: MATHEMATICS

I B. Tech. (I Semester) Mid Examinations-II, March 2022
Descriptive Type Examination (R-20 Regulations)

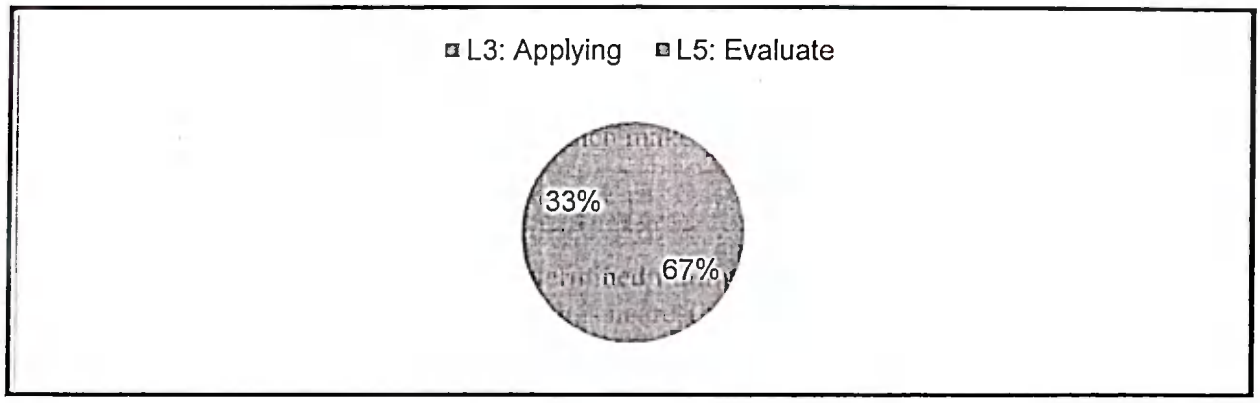
Subject:**Mathematics-I** Date: 24-03-2022

Sections: All Sections Duration: 90 min.

Name of the Instructor/Faculty: **Dr.K. L. SAIPRASAD, Dr. A.SUSEELATHA, Ms.B.BHARATHI, Mr. V.S.S.V.D.PRAKASH & Dr. T. POORNA KANTHA**

Answer all the Questions Max marks:30

Q. No.	Qnestions	Marks	CO	BL
I (a)	Solve $\frac{d^2y}{dx^2} + a^2y = cosecax$ by the method of variation of parameters.	5	3	L3
I (b)	Solve $x^2\frac{d^2y}{dx^2} + 4x\frac{dy}{dx} + 2y = \log x$.	5	3	L3
2 (a)	If $u = x + y + z$, $u^2v = y + z$, $u^3w = z$, then make use of the Jacobian to find $J\left(\frac{u,v,w}{x,y,z}\right)$.	5	4	L3
2 (b)	Apply Lagrange's method of undetermined multipliers to find a point on the plane $3x - 4y + 5z = 26$ which is nearest to the origin.	5	4	L3
3 (a)	By changing the order of integration evaluate $\int_0^1 \int_x^{\sqrt{x}} xydydx$.	5	5	L5
3 (b)	Evaluate: $\int_0^1 \int_{y^2}^1 \int_0^{1-x} xdzdxdy$.	5	5	L5



Bloom's Level Wise MarksDistribution

GVP COLLEGE OF ENGINEERING FOR WOMEN
MADHURAWADA::VISAKHAPATNAM

SCHEME OF VALUATION

NAME OF EXAMINATION : I B.Tech. I Semester Mid-II Examination

DATE OF EXAMINATION :24-03-22

NAME OF SUBJECT : Mathematics-I

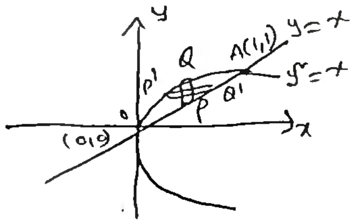
SUBJECT CODE :R201101

BRANCHES :EEE, ECE, CSE, CSM & IT

MAX. MARKS :30

NAME OF THE COURSE INSTRUCTOR : **Dr. K. L. SAIPRASAD, Dr. A. SUSEELATHA, Ms. B. BHARATHI, Mr. V.S.S.V.D. PRAKASH, &Dr. T. POORNA KANTHA**

Q.No.	Sub Q. No.	Scheme of Evaluation	Marks allotted
1.	a)	Solve $\frac{d^2y}{dx^2} + a^2y = cosecax$ by the method of variation of parameters. **Solution:** Auxillary equation is $m^2 + a^2 = 0$, $m = \pm ai$; 1M CF: $y_c = c_1\cos ax + c_2\sin ax$ 1M PI: $y_p = Au + Bv$ Where $u = \cos ax$, $v = \sin ax$ $A = -\int \frac{vR}{uv'-u'v}dx = -\frac{x}{a}$ 1M $B = \int \frac{uR}{uv'-u'v}dx = \frac{1}{a^2}log\lvert sinax\rvert$ 1M $y = c_1\cos ax + c_2\sin ax - \frac{x}{a}cosax + \frac{sinax}{a^2}log\lvert sinax\rvert$ 1M	5M
	b)	Solve $x^2\frac{d^2y}{dx^2} + 4x\frac{dy}{dx} + 2y = \log x$ **Solution:** Let $x = e^t$, $t = \log x$, $\frac{d}{dt} = D$ 1M The equation reduces to $(D^2 + 3D + 2)y = t$ CF: $c_1e^{-t} + c_2e^{-2t}$ 1M PI: $\frac{t}{2} - \frac{3}{4}$ 1M $y = c_1e^{-t} + c_2e^{-2t} + \frac{t}{2} - \frac{3}{4}$ 1M $y = \frac{c_1}{x} + \frac{c_2}{x^2} + \frac{logx}{2} - \frac{3}{4}$ 1M	5M
2.	a)	If $u = x + y + z$, $u^2v = y + z$, $u^3w = z$, then find $J\left(\frac{u,v,w}{x,y,z}\right)$ **Solution:** $J\left(\frac{u,v,w}{x,y,z}\right) = \frac{1}{J\left(\frac{x,y,z}{u,v,w}\right)}$ 1M $J\left(\frac{x,y,z}{u,v,w}\right) = \begin{vmatrix} \frac{\partial x}{\partial u} & \frac{\partial x}{\partial v} & \frac{\partial x}{\partial w} \\ \frac{\partial y}{\partial u} & \frac{\partial y}{\partial v} & \frac{\partial y}{\partial w} \\ \frac{\partial z}{\partial u} & \frac{\partial z}{\partial v} & \frac{\partial z}{\partial w} \end{vmatrix} = \begin{vmatrix} 1-2uv & -u^2 & 0 \\ 2uv-3u^2w & u^2 & -u^3 \\ 3u^2w & 0 & u^3 \end{vmatrix}$ $= u^5$ 3M $J\left(\frac{u,v,w}{x,y,z}\right) = \frac{1}{u^5}$ 1M	5M

	b)	Find the point on the plane $3x - 4y + 5z = 26$ which is nearest to the origin **Solution:** $F = u + \lambda\phi$ $u = x^2 + y^2 + z^2$ 1M $\phi = 3x - 4y + 5z - 26$ $y = -\frac{4}{3}x,\ z = \frac{5}{3}x$ 2M $x = \frac{39}{25},\ y = -\frac{52}{25},\ z = \frac{65}{25}$ 2M	5M
3.	a)	By changing the order of integration evaluate $\int_0^1 \int_x^{\sqrt{x}} xy\,dy\,dx$ **Solution:** On changing the order of integration , the limits are $x = y$ to y^2 $y = 0$ to 1  $\int_{y=0}^{1} \int_{x=y^2}^{y} xy\,dy\,dx$ 1M $= \int_{y=0}^{1} y\left(\frac{y^2 - y^4}{2}\right) dy$ 2M $= \frac{1}{24}$ 2M	5M
	b)	Evaluate $\int_0^1 \int_{y^2}^1 \int_0^{1-x} x\,dz\,dx\,dy$ **Solution:** $\int_0^1 \int_{y^2}^1 \int_0^{1-x} x\,dz\,dx\,dy$ $= \int_{y=0}^{1} \int_{x=y^2}^{1} \int_{z=0}^{1-x} x\,dz\,dx\,dy$ 1M $= \int_{y=0}^{1} \int_{x=y^2}^{1} (x - x^2)\,dx\,dy$ 2M $= \int_{y=0}^{1} \left(\frac{1}{6} - \frac{y^4}{2} + \frac{y^6}{3}\right) dy = \frac{4}{35}$ 2M	5M

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GVP COLLEGE OF ENGINEERING FOR WOMEN
MADHURAWADA::VISAKHAPATNAM

Department of Basic Science and Humanities

I B. Tech. (I Semester) Mid Examinations-I, March 2022
Descriptive Type Examination (R-20 Regulations)

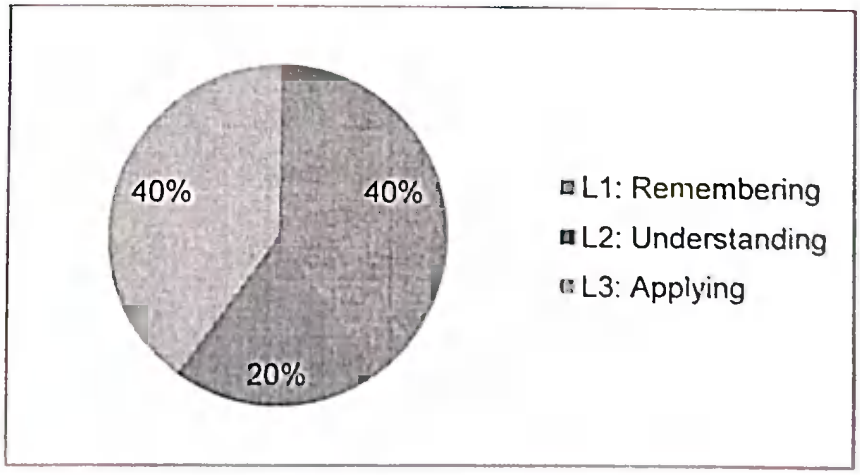
Subject: **Engineering Drawing & Design**
Sections: EEE
Name of the Instructor/Faculty:**P.Hemalatha**

Date: 17-03-2022
Duration: 90 min

Answer all the Questions

Max marks: 30

Q. No.	Questions	Marks	CO	BL
1 (a)	Construct a regular pentagon with the given length of its side 30mm.	5	1	L1
1 (b)	Inscribe an ellipse in a parallelogram having sides 150mm and 100mm long and an included angle of 120°.	5	1	L1
2 (a)	A point Q is situated in first quadrant. It is 40mm above H.P. and 30mm in front of V.P. Draw its projections and find its shortest distance from the intersection of H.P., V.P. and auxiliary plane.	5	2	L2
2 (b)	A line AB, 75mm long, is inclined at 45° to the H.P. and 30° to the V.P. Its end B is in the H.P. and 40mm in front of the V.P. Draw its projections and determine its traces.	5	2	L3
3	Draw the projections of a circle of 50 mm diameter, having its plane vertical and inclined at 30° to the V.P. Its center is 30 mm above the H.P. and 20 mm in front of the V.P.	10	3	L3



Bloom's Level wise MarksDistribution

Name of the Instructor/Faculty Signature: **P.Hemalatha**

GVP COLLEGE OF ENGINEERING FOR WOMEN
MADHURAWADA::VISAKHAPATNAM

Department of Basic Science and Humanities

I B. Tech. (I Semester) Mid Examinations-I, March 2022
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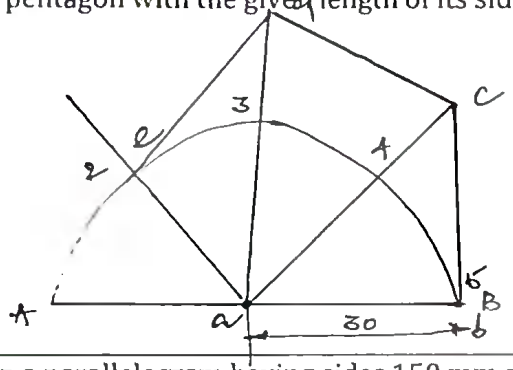
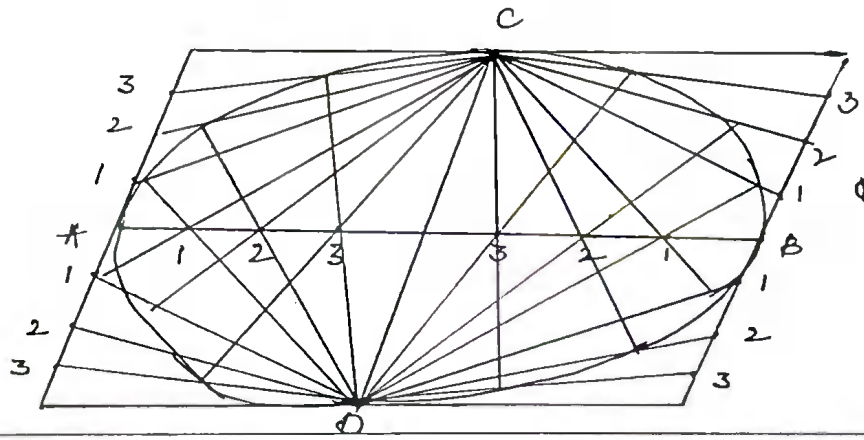
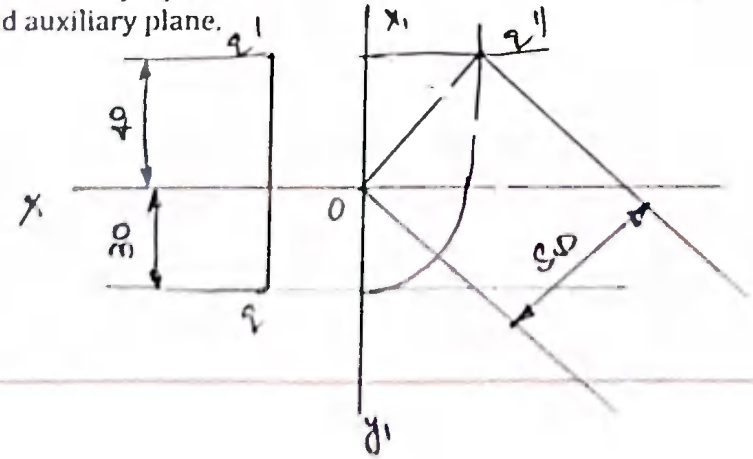
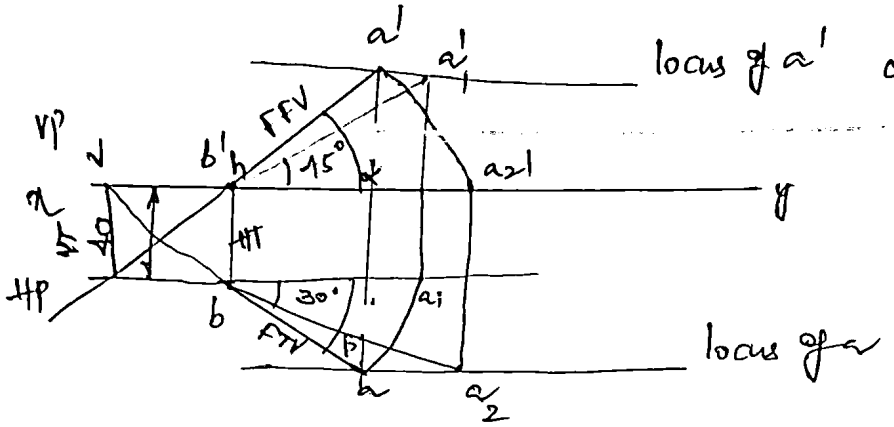
Subject: **Engineering Drawing & Design** | Date: 17-03-2022
Sections: EEE | Duration: 90 min.
Name of the Instructor/Faculty: **P.Hemalatha**

Scheme of Evaluation | Max marks: 30

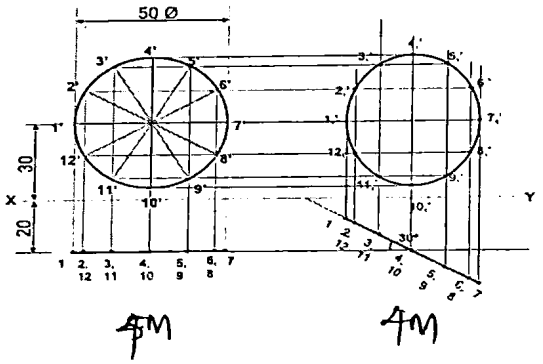
Q. No.	Questions with Scheme of Evaluation	5M
1 (a)	Construct a regular pentagon with the given length of its side 30 mm.  Construction - 3M Dimensioning - 2M	
1 (b)	Inscribe an ellipse in a parallelogram having sides 150 mm and 100 mm long and an included angle of 120°.  construction - 3M Dimensioning - 2M	
2 (a)	A point Q is situated in first quadrant. It is 40 mm above H.P. and 30 mm in front of V.P. Draw its projections and find its shortest distance from the intersection of H.P., V.P. and auxiliary plane.  Method - 4M Dimensioning - 1M notation	

2 (b)	A line AB, 75 mm long, is inclined at 45° to the H.P. and 30° to the V.P. Its end B is in the H.P. and 40 mm in front of the V.P. Draw its projections and determine its traces.
3	Draw the projections of a circle of 50 mm diameter, having its plane vertical and inclined at 30° to the VP. Its center is 30 mm above the HP and 20 mm in front of VP.



construction - 3M
notation - 2M
Dimensioning

Q.12.5:Draw the projections of a circle of 5 cm diameter having its plane vertical and inclined at 30° to the V.P. Its centre is 3cm above the H.P. and 2cm in front of the V.P. Show also its traces



4M 4M

notation - 2M

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Dcpartment of Elcctronics and Commnnieation Engineering

I M. Tech. (II Semester) Mid Examinations-I, April 2019
Descriptive Type Examination (R-16 Regulations)

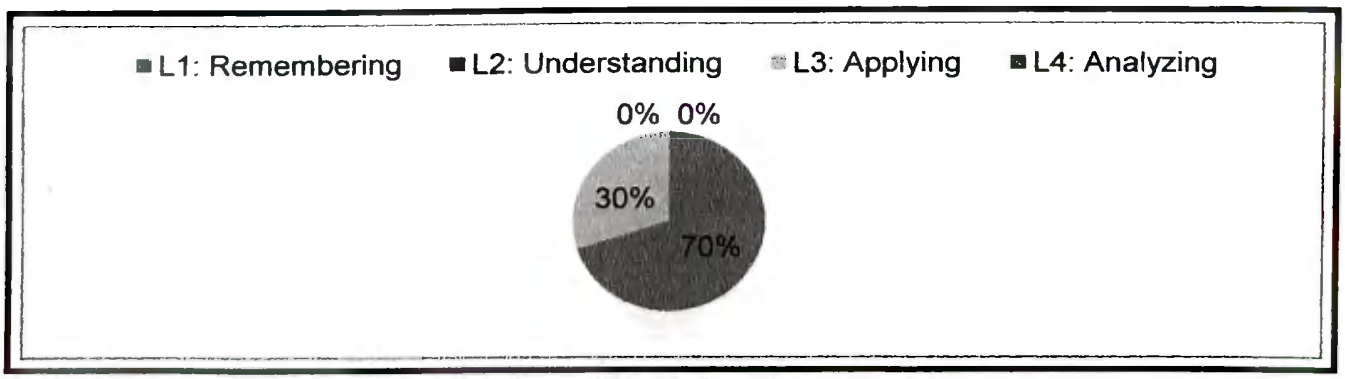
Subject: Embedded System Design
Branch : VLSI &ES
Name of the Instructor/Faculty: **G. P. S. PRASHANTHI**

Date: 1-4-19
Duration: 2 hrs

Answer all the Questions

Max marks: 40

Q. No.	Qnestions	Marks	CO	BL
1 (a)	Define an Embedded System? Explain the Characteristics of it.	5	1	L2
1 (b)	Classify the Processors in an Embedded System.	5	1	L2
2 (a)	Explain the different types of ROMs that are used in Embedded Boards.	5	2	L2
2 (b)	Construct the interfacing of an I/O device with an embedded board by comparing serial and parallel I/O.	5	2	L3
3 (a)	Identify the role of different processor architectures in designing an Embedded System?	7	2	L3
3 (b)	Outline the concept of Bus arbitration.	3	2	L2
4 (a)	What is an Interrupt Service Routine (ISR)? Explain the device drivers for Interrupt handling.	6	3	L2
4 (b)	Describe the concept of Memory device drivers.	4	3	L2



Bloom's Level Wise MarksDistrihntion

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Prasanthi

GVP COLLEGE OF ENGINEERING FOR WOMEN
MADHURAWADA::VISAKHAPATNAM

Department of Electronics and Commnnication Engineering
I M. Tech. (II Semcster) Mid Examinations-I, April 2019
Descriptive Type Examination (R-16 Regulations)

Subject: Embedded System Design — Date: 1-4-19
Branch : VLSI & ES — Duration: 2 hrs
Name of the Instructor/Faculty: **G. P. S. PRASHANTHI**

Schemc of Evaluation — Max marks: 40

Q. No.	Questions with Seheme of Evaluation
1 (a)	Define an Embedded System? Explain the Characteristics of it. Embedded System is an integrated system including both hardware and software. An embedded system is a dedicated computer system, designed to work for single or few specific functions often within a larger system. Some of the key characteristics of Embedded Systems are as mentioned below. All Embedded Systems are task specific. They do the same task repeatedly /continuously over their lifetime. An mp3 player will function only as an mp3 player. Embedded systems are created to perform the task within a certain time frame. It must therefore perform fast enough. A car's brake system, if exceeds the time limit, may cause accidents. They have minimal or no user interface (UI). A fully automatic washing machine works on its own after the programme is set and stops once the task is over. Some embedded systems are designed to react to external stimuli and react accordingly. A thermometer, a GPS tracking device. Embedded systems are built to achieve certain efficiency levels. They are small sized, can work with less power and are not too expensive.
1(b)	Classify the Processors in an Embedded System **General Purpose Processors** ❖ Such as x86, ARM ❖ High-end processors consume thousands of designer-years. ❖ Aim to MAX flexibility for all applications ❖ Compiler and OS must be designed for all applications, entry level is too high ❖ x86 price is high **ASIP** ❖ Is designed for a domain of applications ❖ Its assembly instruction set is designed to accelerate most appearing function and critical functions. ❖ The hardware cost and power consumption are relatively much lower. The price can be very low under volome sales. ❖ It is usually for predictable computing **ASIC** ❖ Non-programmable, usually can reach the lowest power and silicon cost for only one application. ❖ ASIC was a dominant solution when the level of integration was limited. ❖ Because of the high NRE cost, it will be gradually less popular

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VARIOUS PROCESSOR

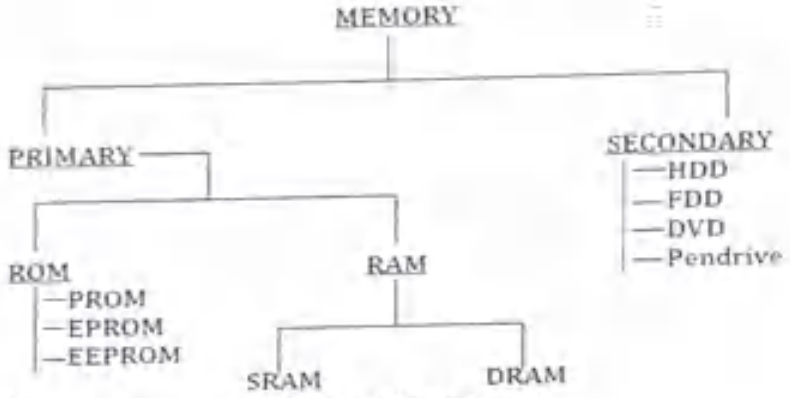
1. General Purpose processor (GPP)

Microprocessor
Microcontroller
Embedded Processor
Digital signal Processor

2. Application Specific System Processor (ASSP)

3. Multi Processor System using GPPs

2 (a) Explain the different types of ROMs that are used in Embedded Boards.



Electrically erasable programmable ROM (EEPROM)

- EEPROM is a type of non-volatile memory used in computers and other electronic devices to store small amounts of data that must be saved when power is removed, for example, calibration tables or device configuration.
- Modern EEPROMs allow multi-byte page read/write operations as well as data read/write lock.Limitation of EEPROM is less number of data program cycles

FLASH

- Flash memory is non-volatile advanced type of EEPROM mostly used in Microcontrollers and other electronics device to store the firmware. There are two main types of flash memory

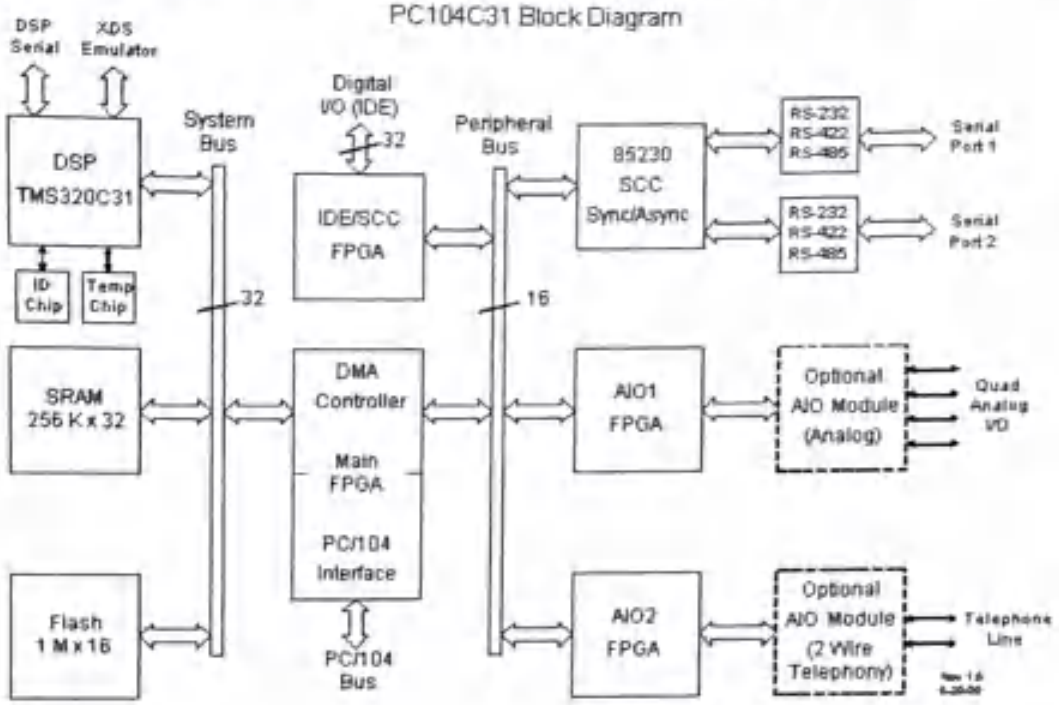
1. NAND Flash – NAND type flash memory may be written and read in blocks (or pages). The NAND type is primarily used in main memory, memory cards, USB flash drives, solid-state drives, and similar products, for general storage and transfer of data.
2. NOR Flash – NOR type flash allows a single machine word (byte) to be written to an erased location or read independently. NOR flash memory is also often used to store configuration data in numerous digital products, a task previously made possible by EEPROM

2(b) Construct the interfacing of an I/O device with an embedded board by comparing serial and parallel I/O.

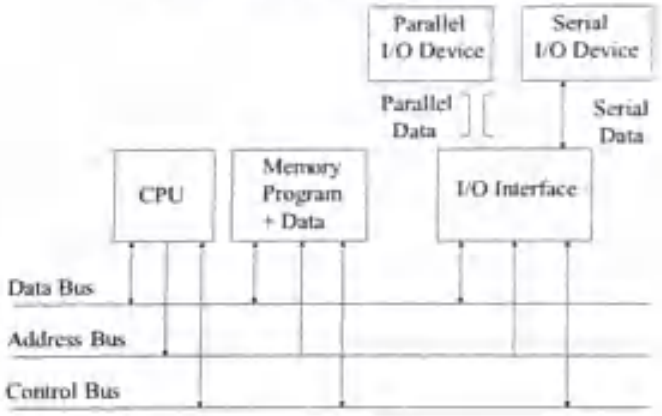
All embedded systems include some form of input and output (I/O) operations. Examples of embedded systems built explicitly to deal with I/O devices: the Cell phone, pager, and a

handheld MP3 player. I/O operations are interpreted differently depending on the viewpoint taken and place different requirements on the level of understanding of the hardware details. The combination of I/O devices, device drivers, and the I/O subsystem comprises the overall I/O system in an embedded environment † The purpose of the I/O subsystem .. To hide the device-specific information from the kernel as well as from the application developer .. To provide a uniform access method to the peripheral I/O devices of the system.

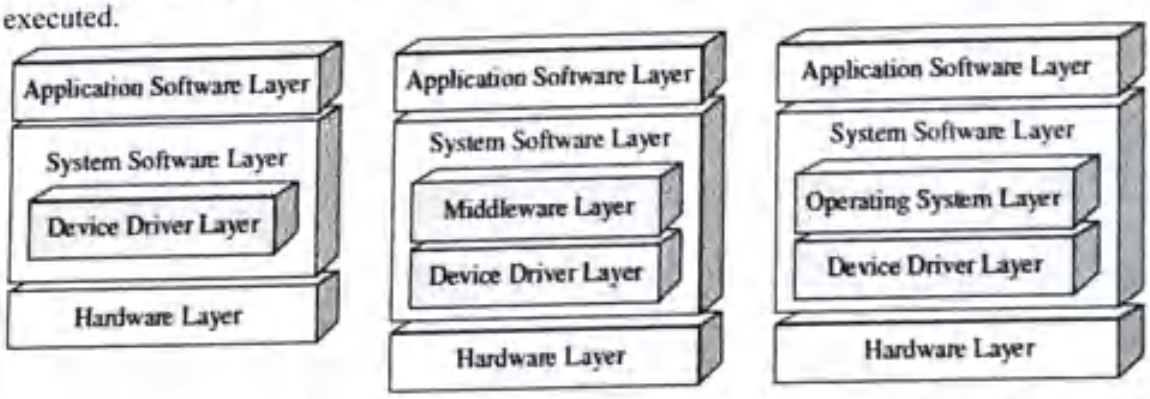
PC104C31 Block Diagram



Basic Computer System



3 (a)	Identify the role of different processor architectures in designing an Embedded System? (1)Von-Neumann (or stored program computer) architecture (2) Harvard architecture (4) CISC (Complex Instruction Set Computer) (5) RISC (Reduced Instruction Set) (6) DSPs (7) VLIW architecture (8) VLIW vs super scalar (9) SIMD

	(10) Multi-Core architectures (11) Stream Processor
3 (b)	Outline the concept of Bus arbitration. • The device that is allowed to initiate data transfers on the bus at any given time is called the bus master. In a computer system there may be more than one bus master such as processor, DMA controller etc. • They share the system bus. When current master relinquishes control of the bus, another bus master can acquire the control of the bus. • Bus arbitration is the process by which the next device to become the bus master is selected and bus mastership is transferred to it. The selection of bus master is usually done on the priority basis. • There are two approaches to bus arbitration: Centralized and distributed. **Centralized Arbitration** • In centralized bus arbitration, a single bus arbiter performs the required arbitration. The bus arbiter may be the processor or a separate controller connected to the bus. • There are three different arbitration schemes that use the centralized bus arbitration approach. There schemes are: a. Daisy chaining b. Polling method c. Independent request **2. Distributed Arbitration** • In distributed arbitration, all devices participate in the selection of the next bus master. • In this scheme each device on the bus is assigned a4-bit identification number. • The number of devices connected on the bus when one or more devices request for the control of bus, they assert the start-arbitration signal and place their 4-bit ID numbers on arbitration lines, ARB0 through ARB3.
4 (a)	What is an Interrupt Service Routine (ISR)? Explain the device drivers for Interrupt handling. Interrupt service routine (ISR) is actually a call back function (program) in case of software or device driver (I/O device) in case of hardware. When an interrupt is acknowledged by the processor, the routine or program which is running currently gets pause or interrupted, and ISR program gets executed.  An interrupt handler or interrupt service routine (ISR) is the function that the kernel runs in response to a specific interrupt: • Each device that generates interrupts has an associated interrupt handler. • The interrupt handler for a device is part of the device's driver (the kernel code that manages the device). In Linux, interrupt handlers are normal C functions, which match a specific prototype and thus enables the kernel to pass the handler information in a standard way.

4 (b)	Describe the concept of Memory device drivers. In computing, a device driver is a computer program that operates or controls a particular type of device that is attached to a computer. A driver provides a software interface to hardware devices, enabling operating systems and other computer programs to access hardware functions without needing to know precise details about the hardware being used. Memory Subsystem Startup: initialization of the hardware upon PowerON or reset (initialize translation lookaside buffers (TLBs) for MMU, initialize/configure MMU). Memory Subsystem Shutdown: configuring hardware into its PowerOFF state. (Note: Under the MPC860, there is no necessary shutdown sequence for the memory subsystem, so pseudocode examples are not shown.) Memory Subsystem Disable: allowing other software to disable hardware on-the-fly (disabling cache). Memory Subsystem Enable: allowing other software to enable hardware on-the-fly (enable cache). Memory Subsystem Write: storing in memory a byte or set of bytes (i.e., in cache, ROM, and main memory). Memory Subsystem Read: retrieving from memory a "copy" of the data in the form of a byte or set of bytes (i.e., in cache, ROM, and main memory).

Prasanthi

GVP COLLEGE OF ENGINEERING FOR WOMEN
MADHURAWADA::VISAKHAPATNAM

Department of Electronics and Communication Engineering

I M. Tech. (II Semester) Mid Examinations-II, July 2019
Descriptive Type Examination (R-16 Regulations)

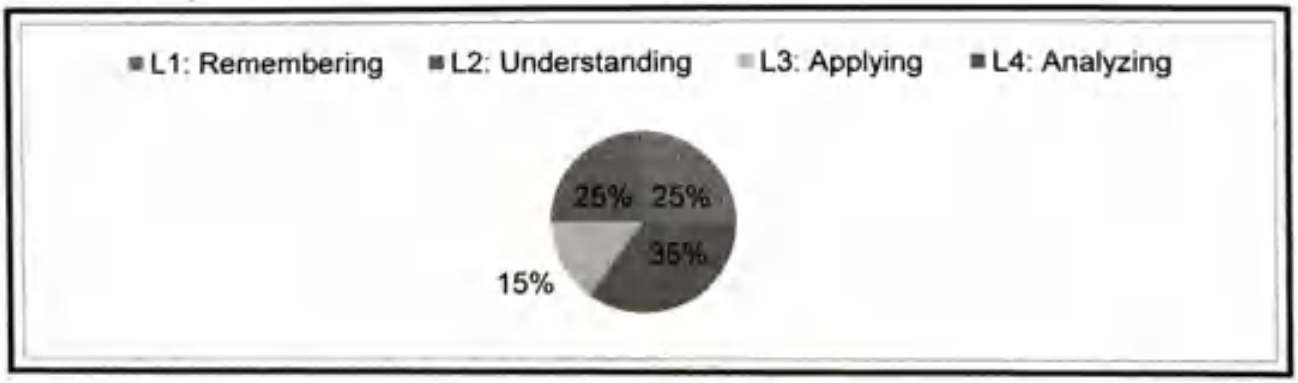
Subject: Embedded System Design
Branch : VLSI &ES
Name of the Instructor/Faculty: **G. P. S. PRASHANTHI**

Date: 8-7-19
Duration: 2 hrs

Answer all the Questions

Max marks: 40

Q. No.	Questions	Marks	CO	BL
1 (a)	Define POSIX. Explain the Characteristics of it.	6	3	L1
1 (b)	Illustrate the concept of Application software with examples.	4	3	L1
2 (a)	With a neat diagram, explain the Embedded system lifecycle model.	6	4	L2
2 (b)	Outline the concept of Embedded software development process.	4	4	L2
3 (a)	Describe the issues in hardware software Co-design.	6	4	L3
3 (b)	Summarize "System Boot up".	4	4	L2
4 (a)	Analyze the power PC processor based Embedded system design on Xilinx platform.	5	5	L4
4 (b)	Analyze the Micro blaze processor based Embedded system design on Xilinx platform.	5	5	L4



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Prasanthi

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Department of Electronics and Communication Engineering
I M. Tech. (II Semester) Mid Examinations-II, JULY 2019
Descriptive Type Examination (R-16 Regulations)

Subject: Embedded System Design
Branch : VLSI & ES
Name of the Instructor/Faculty: **G. P. S. PRASHANTHI**

Date: 8-7-19
Duration: 2 hrs

Scheme of Evaluation

Max marks: 40

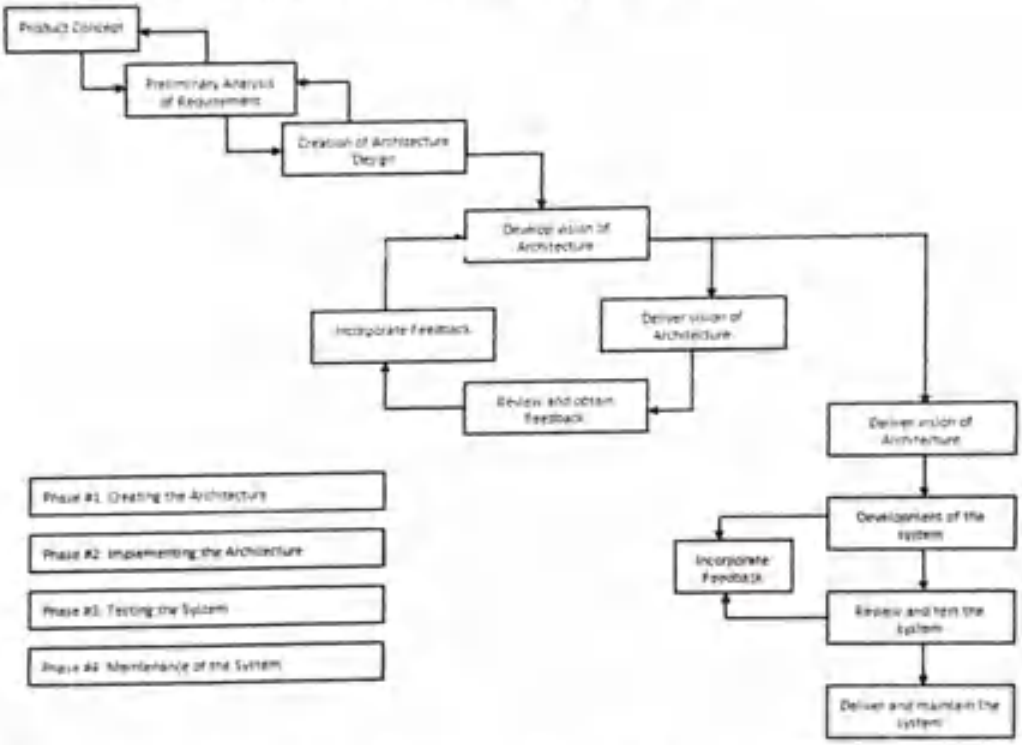
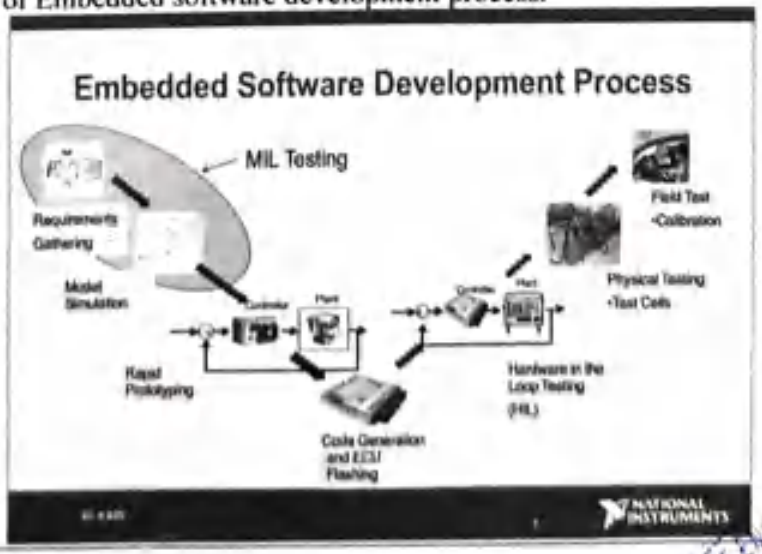
Q. No. 1 (a)

Define POSIX. Explain the Characteristics of it.

POSIX (Portable Operating System Interface) is a set of standard operating system interfaces based on the Unix operating system. One of the key standards implemented in off-the-shelf embedded OS today is portable operating system interface (POSIX). POSIX is based upon the IEEE (1003.1-2001) and The Open Group (The Open Group Base Specifications Issue 6) set of standards that define a standard operating system interface and environment. POSIX provides OS-related standard APIs and definitions for process management, memory management, and I/O management functionality.

OS Subsystem	Function	Definition
Process Management		
	Threads	Functionality to support multiple flows of control within a process. These flows of control are called threads and they share their address space and most of the resources and attributes defined in the operating system for the owner process. The specific functional areas included in threads support are: • Thread management: the creation, control, and termination of multiple flows of control that share a common address space. • Synchronization primitives optimized for tightly coupled operation of multiple control flows in a common, shared address space.
	Semaphores	A minimum synchronization primitive to serve as a basis for more complex synchronization mechanisms to be defined by the application program
	Priority scheduling	A performance and determinism improvement facility to allow applications to determine the order in which threads that are ready to run are granted access to processor resources

1(b) *Illustrate the concept of* Application software with examples.
The final type of software in an embedded system is the application software. An application software

	sits on top of the system software layer, and is dependent on, managed, and run by the system software. It is the software within the application layer that inherently defines what type of device an embedded system is, because the functionality of an application represents at the highest level the purpose of that embedded system and does *most* of the interaction with users or administrators of that device, if any exists. (Note: I say *most* because features such as powering on or off the device when a user hits a button may trigger a device driver function directly for the power-on/power-off sequence, rather than bringing up an application—it depends on the programmer how that is handled.) Like embedded standards, embedded applications can be divided according to whether they are market specific (implemented in only a specific type of device, such as video-on-demand applications in a interactive digital TV) or general-purpose (can be implemented across various types of devices, such as a browser).
2 (a)	With a neat diagram, explain the Embedded system lifecycle model. 
2(b)	Outline the concept of Embedded software development process. 

3 (a)

Describe the issues in hardware software Co-design.

The systems with less area and simple elements can be designed by using software and the systems with timing/speed can be designed by using hardware. Because of portability, battery based designs; System on Chip concepts, low power high speed issues there is a need to integrate hardware and software components together. The integration of hardware and software components together is called as co-design. Designing of a co-design system needs a generic co-design methodology.

The generic co-design flow for hardware software co-design system consists of the following steps.

A. Specifications

The specifications can be refined from the user requirements such that the target system can fit into any one of the available hardware component or software element. This caring of specifications is not available in the HW or SW design methodologies. Because of the HW/SW elements in the target system specifications must be properly selected.

B. Design Entry

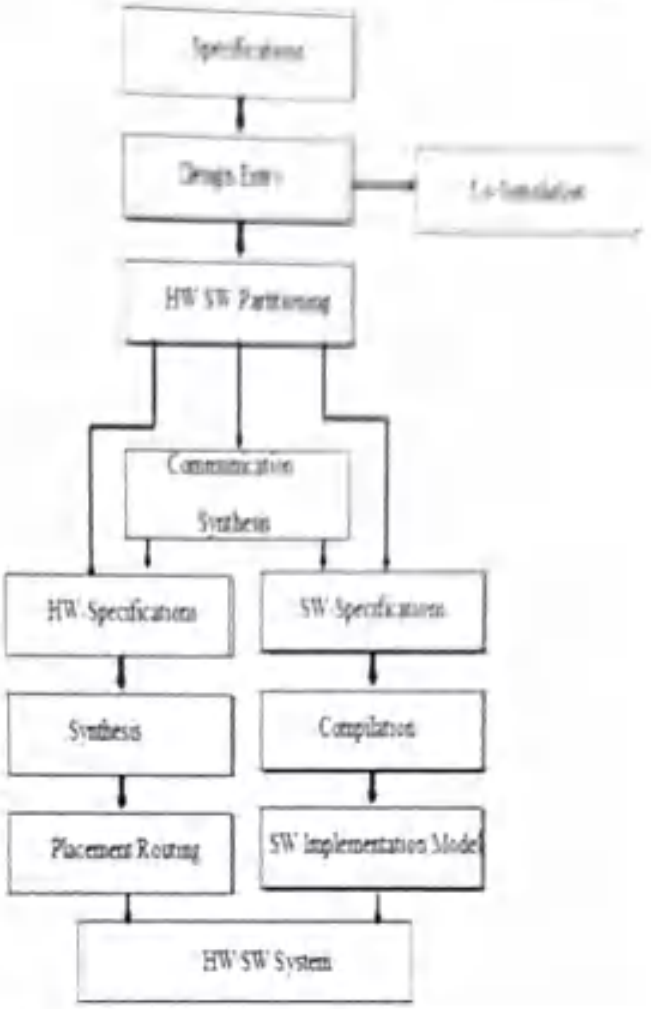
The specifications can be expressed by using different types of models, languages etc. One best method of design entry is combining some programming language features with hardware description language features or vice versa. This combination of different languages leads to a new co-design language. The design entry can be selected based on the following criteria. If the specifications can not be partitioned /separated into hardware blocks or software blocks then use this co-design language. The languages can be combined based on the performance criteria i.e. primal approach or dual approach. The number of registers of the target processor is also a constraint in the language selection. The language selection can be based on target compiler availability also. Some of the co-design specification languages are like LOTOS, SDL, and HardwareC etc

C. Co-Simulation

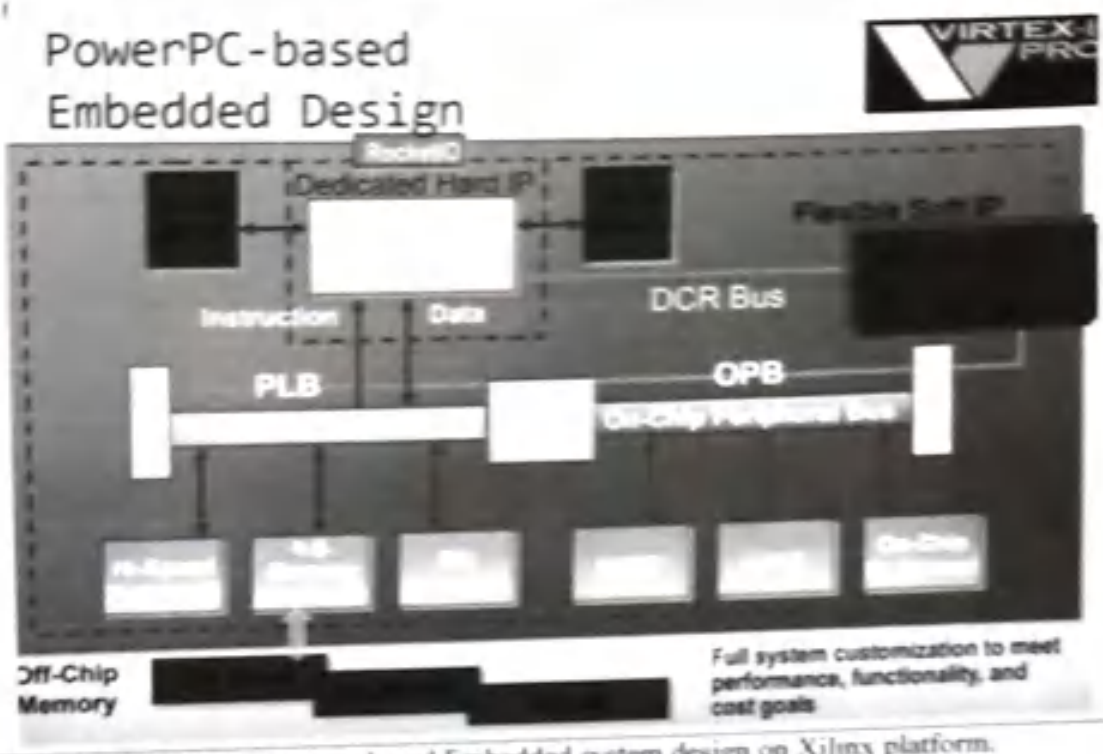
The system functionality of a hardware system can be verified by simulation; software system can be verified by using compilation. But functionality of a system with both hardware and software parts can not be verified by using simulation or compilation. This is due to the following reasons. The system feature with HDL language elements can not be verified by using compilation. The system feature with programming language can not be verified by using simulation. The present available compilers can not be used for the verification because of register widths, processor capacity and new features because of multiple systems integration. Hence, to verify the HW/SW co-design system functionality a new tool called as co-simulation is required. The design of a co-simulator is also a complex process. After the co simulation, the design can be applied to HW/SW portioning block.

D. HW/SW Partitioning

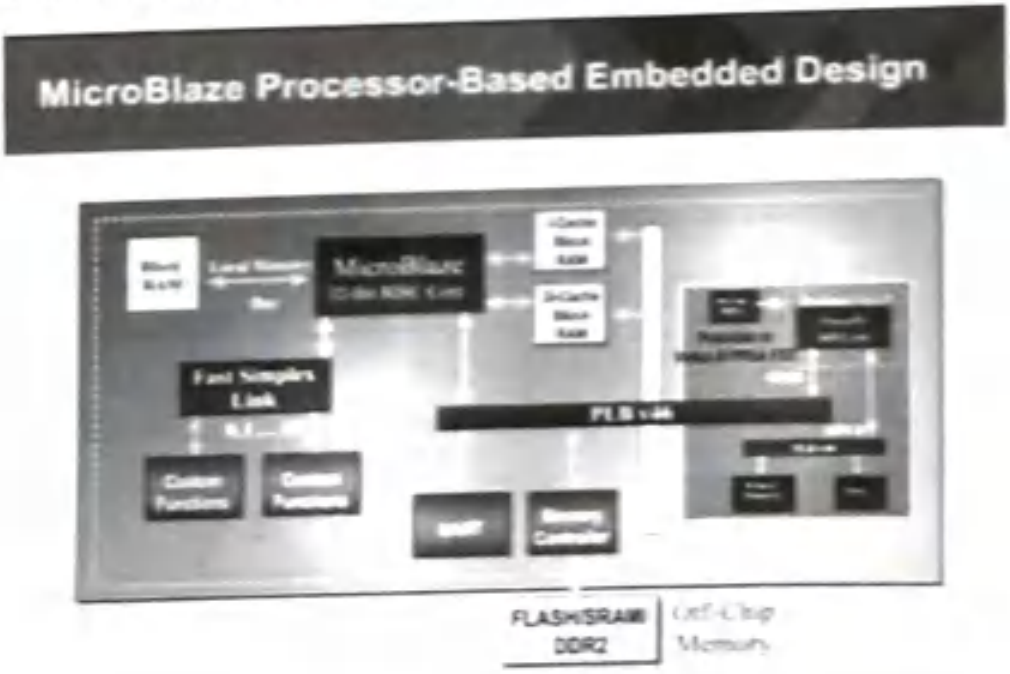
After the co simulation of the system some of the elements can be portioned into hardware/software elements. This partitioning consists of following three portions to avoid functionality mismatch. Hardware Part Software Part Communication Synthesis The system is partitioned into hardware part which consists of hardware elements and software part which consists of software elements. The integration of these hardware and software parts to maintain the functionality can be done by using communication synthesis.



3 (b)	Summarize "System Boot up". System boot-up means that some type of power-on or reset source, such as an internal/external hard reset (i.e., generated by a check-stop error, the software watchdog, a loss of lock by the PLL, debugger, etc.) or an internal/external soft reset (i.e., generated by a debugger, application code, etc.), has occurred. When power is applied to an embedded board (because of a reset), start-up code, also referred to as ***boot code***, ***bootloader***, ***bootstrap*** code, or ***BIOS*** (basic input output system) depending on the architecture, in the system's ROM is loaded and executed by the master processor. Some embedded (master) architectures have an internal program counter that is automatically configured with an address in ROM in which the start of the boot-up code (or table) is located, while others are hardware wired to start executing at a specific location in memory. Boot code differs in length and functionality depending on where in the development cycle the board is, as well as the components of the actual platform that need initialization.
4 (a)	Analyze the power PC processor based Embedded system design on Xilinx platform.



4 (b) Analyze the Micro blaze processor based Embedded system design on Xilinx platform.



Prasanthi.

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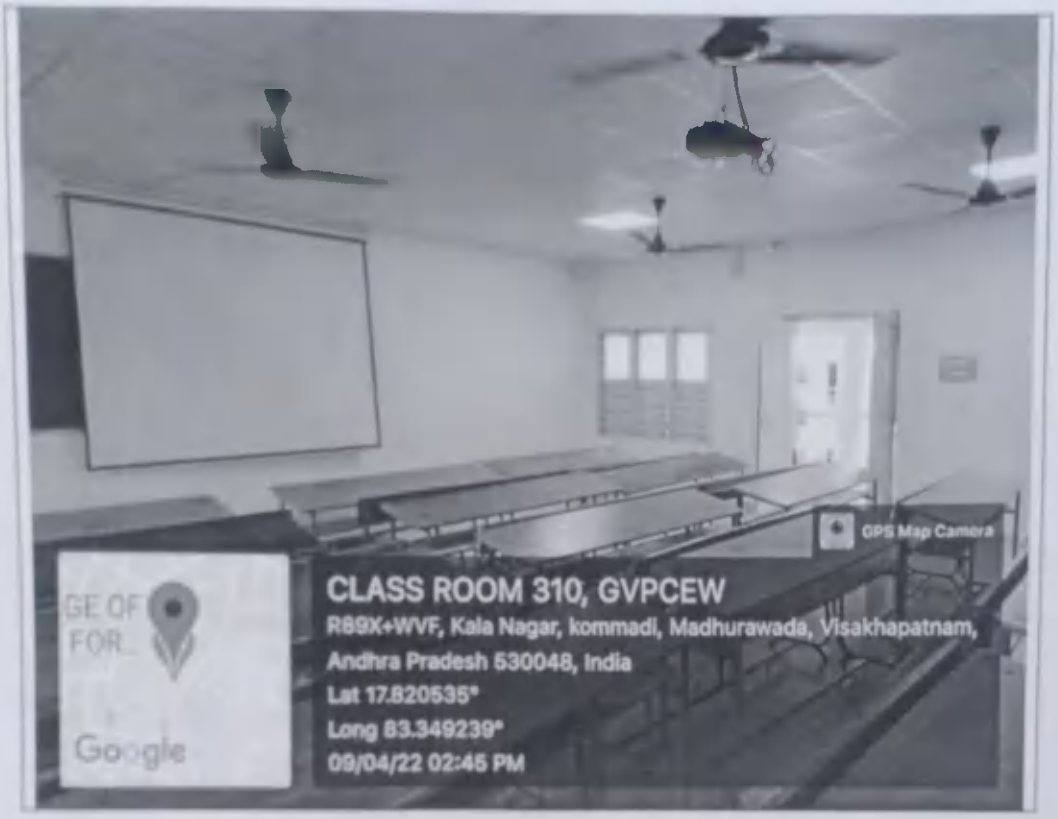
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Admission Details | Communication Details | Identity Details | Qualification Details | Certificate Details | AICTE Details | Important Dates | Student History

Allotment Details

Batch:*	2016 - 2017
Year of Join:*	2016
Admin Date:*	01/07 2016
Lateral:	No
AutonomousBatch:	No
Spot Admission:	No
Admission Type:	COMMON
Admission Category:	
Other Admission Type:	JKC

Scholarship details

Caste Category:	
Caste Name:	
Fee Reimbursement:	No
Reimbursement Amt:	
Scholarship:	No
Education Loan:	

Personal details

DOB:* 27/08/1999 Gender:* Female

Father Name:* KUPPILI RAVISHANKAR

Occupation: Annual Income:

Mother Name: KUPPILI MANJULA

Occupation: Annual Income:

Nationality: Indian Blood Group: -SELECT-

Religion: HINDU Mother Tounge: -SELECT-

Qualified CET Details

Test: Study Duration

Test H.T No: 551460403 2016 - 2020

Rank: 9373

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Father Ph : 9059922032
Father Email:
Date of Admission : 01-07-2016
Admn No : 16JG1A0460
Admission Type:
Fee Reimbursement-:
CAste Category:-:
RouteName:-:
StageName:-:
HostelName:-:
StageName:-:

KUPPILI BHAVYA HARSHITHA
16JG1A0460 ECE IV/IV II SEM
2016 - 2017 (Active)

Ph:
Email:

Academic Details

Final CGPA : 8.56

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MADHURAWADA, VISAKHAPATNAM - 530 048, A.P. INDIA.

Report Date : 20/02/2020

CONSOLIDATED GRADE MEMO/CREDIT SHEET

Name: KUPPILI BHAVYA HARSHITHA
Branch: ECE
Hall Ticket No: 16JG1A0460

Year of Admission : **2016**
Month & Year of Exam : **November2019**
Regulation : **R-16**

I YEAR

I SEMESTER

Sno	COURSE	Gr	GPA	Points	Cr
1	[illegible]	S	9.25	9	3
2	MATHEMATICS-I	O	9.25	10	3
3	MATHEMATICS-II(NUMERICAL METHODS AND COMPLEX VARIABLES)	O	9.25	10	3
4	APPLIED PHYSICS	A	9.25	8	3
5	COMPUTER PROGRAMMING	B	9.25	7	3
6	ENGINEERING DRAWING	O	9.25	10	3
7	ENGLISH COMMUNICATION SKILLS LAB-1	O	9.25	10	2
8	ENGINEERING PHYSICS LAB	O	9.25	10	2
9	[illegible] WORKSHOP	O	9.25	10	2

II SEMESTER

Sno	COURSE	Gr	GPA	Points	Cr
1	English - II	S	9.25	9	3
2	Mathematics - III	O	9.25	10	3
3	Applied Chemistry	A	9.25	8	3
4	Electrical and Mechanical Technology	A	9.25	8	3
5	Environmental Studies	B	9.25	7	3
6	Data Structures	S	9.25	9	3
7	Applied / Engineering Chemistry Laboratory	O	9.25	10	2
8	English - Communication Skills Lab -1	O	9.25	10	2
9	Computer Programming Lab	O	9.25	10	2

II YEAR

I SEMESTER

Sno	COURSE	Gr	GPA	Points	Cr
1	Electronic Devices and Circuits	B	8.77	7	3
2	Switching Theory and Logic Design	A	8.77	8	3
3	Signals and Systems	S	8.77	9	3
4	Network Analysis	S	8.77	9	3
5	Random Variables and Stochastic	S	8.77	9	3
6	Managerial Economics & Financial	S	8.77	9	3
7	Electronic Devices and Circuits Lab	O	8.77	10	2
8	Networks & Electrical Technology Lab	O	8.77	10	2

II SEMESTER

Sno	COURSE	Gr	GPA	Points	Cr
1	Electronic Circuit Analysis	S	9.23	9	3
2	Control Systems	A	9.23	8	3
3	Electromagnetic Waves and Transmission Lines	C	9.23	6	3
4	Analog Communications	B	9.23	7	3
5	Pulse and Digital Circuits	S	9.23	9	3
6	Management Science	A	9.23	8	3
7	Electronic Circuit Analysis Lab	O	9.23	10	2
8	Analog Communications Lab	O	9.23	10	2

III YEAR

I SEMESTER

Sno	COURSE	Gr	GPA	Points	Cr
1	Computer Architecture and	A	8.43	8	3
2	Linear IC Applications	B	8.43	7	3
3	Digital IC Applications	A	8.43	8	3
4	Digital Communications	O	8.43	10	3
5	Antenna and Wave Propagation	C	8.43	6	3
6	Pulse and Digital Circuits Lab	O	8.43	10	2
7	Linear IC Applications Lab	O	8.43	10	2
8	Digital IC Applications Lab	O	8.43	10	2

II SEMESTER

Sno	COURSE	Gr	GPA	Points	Cr
1	Micro Processors & Micro Controllers	A	9.00	8	3
2	Micro Wave Engineering	A	9.00	8	3
3	VLSI Design	C	9.00	6	3
4	Digital Signal Processing	C	9.00	6	3
5	Artificial Neural Networks	A	9.00	8	3
6	Micro Processors & Micro Controllers LAB	O	9.00	10	2
7	VLSI Lab	O	9.00	10	2
8	Digital Communications Lab	O	9.00	10	2

IV YEAR

I SEMESTER

Sno	COURSE	Gr	GPA	Points	Cr
1	Radar Systems	B	8.23	7	3
2	Digital Image Processing	B	8.23	7	3
3	Computer Networks	A	8.23	8	3
4	Optical Communications	A	8.23	8	3
5	System Design through Verilog	A	8.23	8	3
6	Embedded Systems	S	8.23	9	3
7	Micro Wave Engineering & Optical Lab	O	8.23	10	2
8	Digital Signal Processing Lab	O	8.23	10	2

II SEMESTER

Sno	COURSE	Gr	GPA	Points	Cr

Number of Credits Registered : 156

Number of Credits Obtained : 156

Final CGPA : 8.56

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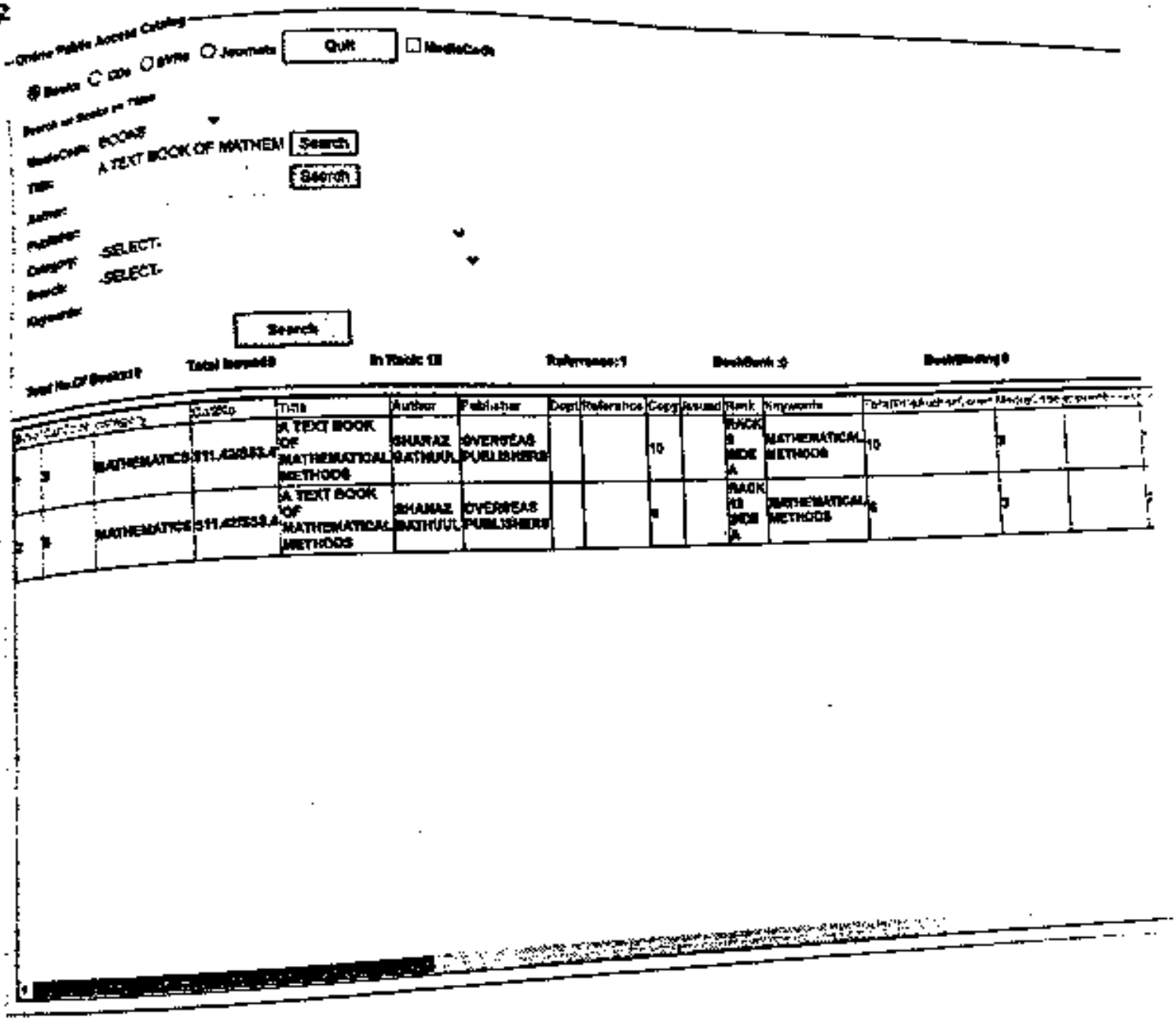

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