

## FACULTY DETAILS

<b>Name</b>	L SARIKA
<b>Designation</b>	Assistant Professor
<b>Department</b>	Electronics and Communication Engineering

<b>Educational Qualifications</b>			
Degree	Specialization	University/College	Year of Award
B.E	ECE	ANDHRA UNIVERSITY (AU)/ANITS Engg. College	2007 (First Class)
M.Tech	Radar & Microwave Engineering	ANDHRA UNIVERSITY(AU)	2011(First Class with Distinction)

<b>Area of Expertise</b>	Antennas, Radar & Microwave Engineering
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<b>Subjects Taught</b>	Optical Communication Satellite Communication Radar systems Microwave Engineering Cellular Mobile Communication Signals and systems Electronic devices and circuits Pulse digital circuits Electronic Circuit Analysis Electronic measurements and instruments Analog Communication Digital Communication
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### **Experience Details:**

Institution	Position held	From date	To date	Years of Experience
GVPCEW	Assistant Professor	2011	Till date	8
VITAM Engg. College	Assistant Professor	2009	2011	2
Al-Ameer College of Engg. for women	Assistant Professor	2007	2009	2

**Conferences/Workshops/FDPs attended/ MOOCS:**

Name of Program	National/ International	Venue	Year
National Conference on Advances in On Digital signal processing (NCASP-09) Organized by Blekige institute of technology, Sweden & IETE Visakhapatnam centre	National Conference	IETE Visakhapatnam Centre	2009
National level workshop on cognitive radio technologies.(CRT-2015)	National Workshop	GVPCEW	2015
Women Empowerment in Research	National	GVPCE	2015
EMRF	National	GVPCEW	2016
“SIGNAL PROCESSING AND COMMUNICATION SYSTEMS	FDP	GVPCEW	2017
“CUSTOM ANALOG & DIGITAL IC DESIGN USING MENTOR EDA TOOLS”	workshop	GVPCEW	2018
“CRYPTOLOGY AND CYBER SECURITY (WCCS 2018)”,	National workshop	GVPCEW	2018
“ROLE OF IT IN THE NEW AGE BROADCASTING”	One day seminar	GVPCEW	2019
MICROELECTRONICS: DEVICES TO CIRCUITS	12-week MOOCS course	NPTEL (MHRD, govt of India) IIT, ROORKEE& SWAYAM	2019

**Papers / Books Published**

Title	Name of Journal	National/ International	Year of publication with page Nos.
Design of CMOS Full Adder Cells for Arithmetic Applications	International Journal of Engineering Research & Technology(IJERT)	International	Vol. 2, Issue 9, ISSN:2278-0181. 2013
Side lobe Rejection In A Uniform Linear Array Antenna Using Windowing Techniques	International Journal of Engineering Research & Technology(IJERT)	International	Vol. 3, Special Issue 04, pISSN: 2321-7308, 2014

**Professional Body Memberships**

(AMIETE) Associate Member of IETE

