

## JALAJA RAJANA

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### **Career Objective :**

To work in a creative, challenging environment and achieve growth oriented career in an organization where I can contribute best to the growth of the organization and my technical skills.

### **Teaching Experience :**

- ◆ Currently working as Assistant Professor in Department of ECE in Gayatri Vidya Parishad College of Engineering for Women from 28<sup>th</sup> Dec 2012.
- ◆ Worked as Mathematics Lecturer in Vikas Educational Institutions, Visakhapatnam from July 2009 to Dec 2010.

### **Academic Record :**

Qualification	School/College	University/Board	Period	Percentage of Marks
<b>Ph.D</b> (Electronics & Communication Engg)	Andhra University College of Engineering,(A)	Andhra University	2021 (Pursuing)	---
<b>M.Tech</b> (Digital Electronics & Communication Systems)	G.M.R.Institute of Technology, Rajam.	J.N.T.U,Kakinada	2010-2012	80.07
<b>B.Tech</b> (Electronics & Communication Engg)	Vignan's Institute Of Information Technology, Visakhapatnam.	J.N.T.U,Kakinada	2005-2009	75.13
<b>Intermediate</b>	Sri Chaitanya Jr college, Visakhapatnam.	Board of Intermediate Education,A.P.	2003-2005	92.10
<b>S.S.C</b>	Visvodaya High School, Visakhapatnam.	Board of Secondary Education,A.P.	2002-2003	83.33

## Technical Skills :

- ◆ Languages : C, VHDL, Verilog
- ◆ Software Packages : MATLAB, Mentor Graphics, CC Studio, MASM Assembler

## Personality Traits:

- ◆ Strong interpersonal skills.
- ◆ Ability to work in teams.
- ◆ Adaptability to new environments.
- ◆ Ability to work in time bound crisis situations.
- ◆ Hard working and result oriented.

## Subjects Taught :

- ◆ Embedded & Real Time Operating Systems
- ◆ Switching theory and logic design
- ◆ Microprocessor & Microcontrollers
- ◆ Digital logic design
- ◆ Digital System Design & Digital IC Applications
- ◆ Microprocessor & Multicore Systems
- ◆ Signals & Systems
- ◆ Digital Signal Processing
- ◆ CMOS Digital IC Design

## Online Courses (NPTEL):

S.No.	Title of Lecture	Organization Details	Duration & Dates	Score
1	PRINCIPLES OF COMMUNICATIONS SYSTEMS-1	NPTEL	12 WEEKS 31/01/2018 - 21/04/2018	70%
2	SWITCHING THEORY AND LOGIC DESIGN	NPTEL	12 WEEKS (JULY-OCT,2018)	70%
3	DIGITAL CIRCUITS	NPTEL	12 WEEKS (JULY-OCT,2018)	83% (TOP 5%)

## Conferences & Publications :

- ◆ S.C. Satapathy, R. Jalaja and B. Biswal “Power Quality Event Classification Using Hilbert Huang Transform”, Proc. of Int. Conf. on Front. of Intell. Comput., AISC 199, pp. 153–161, Springer-Verlag Berlin Heidelberg 2013.

- ◆ B. Biswal, S. Misra and R. Jalaja, “Automatic Classification of Power Quality Events using Balanced Neural Tree”, IEEE Trans. Ind. Electron., vol. 61, no. 1, pp 521-530, Jan. 2014.
- ◆ Mavuri Charita, Surada Manisha, Rapaka RamyaSai , Satyavarapu Ramya Ramani , R.Jalaja “Continuous Motorcycle Handle Control Monitoring System”, International Journal of Engineering Science and Innovative Technology (IJESIT) Volume 6, Issue 3, May 2017.

### **FDP's & Workshops:**

- A three day national workshop on “Advanced Optimization Techniques for Engineering and Management”, GMRIT, 2013.
- A Two weeks “DSP learning course on Blackfin Processor”, IIT MADRAS, 2013.
- A Two day Workshop on “Cognitive Radio Technologies”, GVPCEW, 2015.
- A Three day DST National workshop on “Advanced Signal, Speech & Image Processing ” GVPCE(A), 2016 .
- A Three day FDP on "Electromagnetics and RF Fundamentals", GVPCEW, 2016.
- A One Week FDP on “Advanced Digital Signal Processing And Applications”, Organized by the E &ICT academy, NIT Warangal, MVGR (A), 2016.
- A three day National workshop on “Biological Inspired Computing & Applications", GVPCEW, 2018.
- A three day National workshop on “Cryptology and Cyber Security", GVPCEW, 2018.
- A One and Half week NPTEL - AICTE FDP on "Switching Circuits and Logic Design ",2018
- A three day workshop on "Custom Analog and Digital IC Design using Mentor EDA Tools", GVPCEW, 2018.
- A One week National workshop on “Machine Intelligence In Smart Grid & Communication”, GVPCE(A), 2019.
- A Two Week FDP on “Matlab tools and their Applications in Engineering”, Organized by DataTeach Labs, 2020.
- A Webinar on “VLSI Design and Verification”, GVPCEW, 2020.
- A One Week Online Certificate course on “Digital Teaching Techniques”, Organized by the ICT Academy, 2020.
- A five day National online FDP on “Advances in Signal Processing and Artificial Intelligence Technologies”, Organized by the G. Pulla Reddy Engineering College(A), Kurnool, 2020.
- An eight day online FDP on “Optimization Techniques for Engineering Applications”, Organized by the E &ICT academy, NIT Warangal, 2021.
- A 30-hour online Internship on “Machine Learning”, Organized by the Pantech Prolabs in association with APSSDC, 2021.

- A five day International online hands - on workshop on “Signal Processing using Machine Learning Applications”, organized by the School of Electronics Engineering (SENSE), VIT-AP University, 2021.
- A three day National workshop on “Recent Trends in Radar Signal Processing ”, GVPCEW 2022.
- A 40-hour online FDP on “Machine Learning and its Applications with Python”, Organized by the E &ICT academy, NIT Warangal, Vignan – Guntur, 2022.

### **Personal Profile :**

Date of birth : 23-12-1987  
Father’s name : R. Narayana Rao  
Gender : Female  
Marital status : Married  
Languages known : English, Hindi and Telugu  
Nationality : Indian.

### **Declaration :**

I here by declare that all the information mentioned above is true to the best of my knowledge.

Date:

Place: Visakhapatnam

[R.JALAJA]