



# JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY KAKINADA

Results for II B.Tech I semester (R16) Regular/Supplementary Examinations Nov 2020

College name: GAYATHRI VIDYA PARISHAD COLL OF ENGG FOR WOMEN, VISAKHAPATNA:JG

Htno	Subcode	Subname	Grade	Credits
16JG1A0203	R1621021	ELECTRICAL CIRCUIT ANALYSIS - II	F	0
16JG1A0203	R1621022	ELECTRICAL MACHINES-I	F	0
16JG1A0203	R1621023	BASIC ELECTRONICS AND DEVICES	F	0
16JG1A0203	R1621024	ELECTRO MAGNETIC FIELDS	F	0
16JG1A0205	R1621023	BASIC ELECTRONICS AND DEVICES	D	3
16JG1A0205	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	F	0
16JG1A0206	R1621022	ELECTRICAL MACHINES-I	F	0
16JG1A0207	R1621021	ELECTRICAL CIRCUIT ANALYSIS - II	F	0
16JG1A0207	R1621022	ELECTRICAL MACHINES-I	ABSENT	0
16JG1A0207	R1621023	BASIC ELECTRONICS AND DEVICES	F	0
16JG1A0207	R1621024	ELECTRO MAGNETIC FIELDS	ABSENT	0
16JG1A0207	R1621025	THERMAL AND HYDRO PRIME MOVERS	ABSENT	0
16JG1A0207	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	ABSENT	0
16JG1A0208	R1621021	ELECTRICAL CIRCUIT ANALYSIS - II	F	0
16JG1A0208	R1621024	ELECTRO MAGNETIC FIELDS	ABSENT	0
16JG1A0209	R1621022	ELECTRICAL MACHINES-I	F	0
16JG1A0209	R1621023	BASIC ELECTRONICS AND DEVICES	D	3
16JG1A0209	R1621024	ELECTRO MAGNETIC FIELDS	F	0
16JG1A0209	R1621025	THERMAL AND HYDRO PRIME MOVERS	F	0
16JG1A0220	R1621023	BASIC ELECTRONICS AND DEVICES	F	0
16JG1A0220	R1621024	ELECTRO MAGNETIC FIELDS	ABSENT	0
16JG1A0222	R1621022	ELECTRICAL MACHINES-I	F	0
16JG1A0232	R1621021	ELECTRICAL CIRCUIT ANALYSIS - II	F	0
16JG1A0232	R1621022	ELECTRICAL MACHINES-I	F	0
16JG1A0232	R1621023	BASIC ELECTRONICS AND DEVICES	F	0
16JG1A0232	R1621024	ELECTRO MAGNETIC FIELDS	ABSENT	0
16JG1A0412	R1621041	ELECTRONIC DEVICES AND CIRCUITS	F	0
16JG1A0412	R1621042	SWITCHING THEORY AND LOGIC DESIGN	ABSENT	0
16JG1A0412	R1621043	SIGNALS AND SYSTEMS	F	0
16JG1A0416	R1621042	SWITCHING THEORY AND LOGIC DESIGN	F	0
16JG1A0416	R1621043	SIGNALS AND SYSTEMS	F	0
16JG1A0429	R1621042	SWITCHING THEORY AND LOGIC DESIGN	F	0
16JG1A0429	R1621043	SIGNALS AND SYSTEMS	F	0
16JG1A0429	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	F	0
16JG1A0435	R1621043	SIGNALS AND SYSTEMS	F	0
16JG1A0435	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	F	0
16JG1A0452	R1621042	SWITCHING THEORY AND LOGIC DESIGN	F	0
16JG1A0452	R1621043	SIGNALS AND SYSTEMS	F	0
16JG1A0477	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	F	0
16JG1A0477	R1621041	ELECTRONIC DEVICES AND CIRCUITS	F	0
16JG1A0477	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	ABSENT	0
16JG1A0483	R1621042	SWITCHING THEORY AND LOGIC DESIGN	F	0
16JG1A0483	R1621043	SIGNALS AND SYSTEMS	ABSENT	0
16JG1A0483	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	F	0
16JG1A0493	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	ABSENT	0

Htno	Subcode	Subname	Grade	Credits
16JG1A0493	R1621041	ELECTRONIC DEVICES AND CIRCUITS	ABSENT	0
16JG1A0493	R1621042	SWITCHING THEORY AND LOGIC DESIGN	ABSENT	0
16JG1A0493	R1621043	SIGNALS AND SYSTEMS	ABSENT	0
16JG1A0493	R1621044	NETWORK ANALYSIS	ABSENT	0
16JG1A0497	R1621041	ELECTRONIC DEVICES AND CIRCUITS	F	0
16JG1A0497	R1621043	SIGNALS AND SYSTEMS	F	0
16JG1A0516	R1621051	STATISTICS WITH R PROGRAMMING	ABSENT	0
16JG1A0516	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	ABSENT	0
16JG1A0516	R1621053	DIGITAL LOGIC DESIGN	ABSENT	0
16JG1A0516	R1621054	PYTHON PROGRAMMING	D	3
16JG1A0516	R1621056	COMPUTER GRAPHICS	ABSENT	0
16JG1A0519	R1621053	DIGITAL LOGIC DESIGN	C	3
16JG1A0530	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	D	3
16JG1A0534	R1621056	COMPUTER GRAPHICS	F	0
16JG1A0542	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	ABSENT	0
16JG1A0542	R1621053	DIGITAL LOGIC DESIGN	C	3
16JG1A0542	R1621054	PYTHON PROGRAMMING	ABSENT	0
16JG1A0542	R1621055	DATA STRUCTURES THROUGH C++	F	0
16JG1A0542	R1621056	COMPUTER GRAPHICS	F	0
16JG1A0549	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	D	3
16JG1A0549	R1621053	DIGITAL LOGIC DESIGN	C	3
16JG1A0553	R1621053	DIGITAL LOGIC DESIGN	F	0
16JG1A0558	R1621055	DATA STRUCTURES THROUGH C++	C	3
16JG1A0569	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	D	3
16JG1A0569	R1621053	DIGITAL LOGIC DESIGN	ABSENT	0
16JG1A0595	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	D	3
16JG1A0595	R1621053	DIGITAL LOGIC DESIGN	D	3
16JG1A0595	R1621054	PYTHON PROGRAMMING	F	0
16JG1A0595	R1621055	DATA STRUCTURES THROUGH C++	D	3
16JG1A0595	R1621056	COMPUTER GRAPHICS	ABSENT	0
16JG1A0599	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	D	3
16JG1A0599	R1621053	DIGITAL LOGIC DESIGN	D	3
16JG1A1202	R1621053	DIGITAL LOGIC DESIGN	D	3
16JG1A1218	R1621051	STATISTICS WITH R PROGRAMMING	D	3
16JG1A1218	R1621053	DIGITAL LOGIC DESIGN	F	0
16JG1A1218	R1621054	PYTHON PROGRAMMING	F	0
16JG1A1218	R1621121	SOFTWARE ENGINEERING	F	0
16JG1A1254	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	F	0
16JG1A1254	R1621053	DIGITAL LOGIC DESIGN	D	3
16JG1A1256	R1621051	STATISTICS WITH R PROGRAMMING	F	0
17JG1A0202	R1621021	ELECTRICAL CIRCUIT ANALYSIS - II	F	0
17JG1A0202	R1621022	ELECTRICAL MACHINES-I	F	0
17JG1A0202	R1621023	BASIC ELECTRONICS AND DEVICES	F	0
17JG1A0202	R1621024	ELECTRO MAGNETIC FIELDS	F	0
17JG1A0202	R1621025	THERMAL AND HYDRO PRIME MOVERS	F	0
17JG1A0208	R1621022	ELECTRICAL MACHINES-I	F	0
17JG1A0209	R1621022	ELECTRICAL MACHINES-I	B	3
17JG1A0209	R1621024	ELECTRO MAGNETIC FIELDS	D	3
17JG1A0215	R1621022	ELECTRICAL MACHINES-I	F	0
17JG1A0216	R1621022	ELECTRICAL MACHINES-I	F	0
17JG1A0216	R1621023	BASIC ELECTRONICS AND DEVICES	ABSENT	0

Htno	Subcode	Subname	Grade	Credits
17JG1A0221	R1621021	ELECTRICAL CIRCUIT ANALYSIS - II	F	0
17JG1A0221	R1621022	ELECTRICAL MACHINES-I	F	0
17JG1A0232	R1621022	ELECTRICAL MACHINES-I	F	0
17JG1A0232	R1621023	BASIC ELECTRONICS AND DEVICES	D	3
17JG1A0411	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	ABSENT	0
17JG1A0411	R1621041	ELECTRONIC DEVICES AND CIRCUITS	F	0
17JG1A0411	R1621044	NETWORK ANALYSIS	D	3
17JG1A0411	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	F	0
17JG1A0428	R1621041	ELECTRONIC DEVICES AND CIRCUITS	ABSENT	0
17JG1A0428	R1621042	SWITCHING THEORY AND LOGIC DESIGN	ABSENT	0
17JG1A0432	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	D	3
17JG1A0443	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	D	3
17JG1A0454	R1621043	SIGNALS AND SYSTEMS	F	0
17JG1A0454	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	D	3
17JG1A0458	R1621043	SIGNALS AND SYSTEMS	D	3
17JG1A0461	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	ABSENT	0
17JG1A0464	R1621041	ELECTRONIC DEVICES AND CIRCUITS	F	0
17JG1A0464	R1621043	SIGNALS AND SYSTEMS	F	0
17JG1A0464	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	F	0
17JG1A0465	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	F	0
17JG1A0469	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	F	0
17JG1A0480	R1621041	ELECTRONIC DEVICES AND CIRCUITS	F	0
17JG1A0485	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	F	0
17JG1A0489	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	ABSENT	0
17JG1A0492	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	F	0
17JG1A0499	R1621041	ELECTRONIC DEVICES AND CIRCUITS	ABSENT	0
17JG1A0499	R1621043	SIGNALS AND SYSTEMS	ABSENT	0
17JG1A0499	R1621044	NETWORK ANALYSIS	ABSENT	0
17JG1A0499	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	ABSENT	0
17JG1A04A0	R1621043	SIGNALS AND SYSTEMS	ABSENT	0
17JG1A04A0	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	ABSENT	0
17JG1A04A4	R1621042	SWITCHING THEORY AND LOGIC DESIGN	ABSENT	0
17JG1A04A5	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	ABSENT	0
17JG1A04A6	R1621042	SWITCHING THEORY AND LOGIC DESIGN	F	0
17JG1A04A6	R1621043	SIGNALS AND SYSTEMS	F	0
17JG1A04A6	R1621044	NETWORK ANALYSIS	D	3
17JG1A04A9	R1621043	SIGNALS AND SYSTEMS	F	0
17JG1A04A9	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	F	0
17JG1A04B4	R1621042	SWITCHING THEORY AND LOGIC DESIGN	F	0
17JG1A04B4	R1621043	SIGNALS AND SYSTEMS	F	0
17JG1A04B4	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	D	3
17JG1A0527	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	F	0
17JG1A0527	R1621053	DIGITAL LOGIC DESIGN	C	3
17JG1A0527	R1621054	PYTHON PROGRAMMING	ABSENT	0
17JG1A0530	R1621051	STATISTICS WITH R PROGRAMMING	F	0
17JG1A0530	R1621053	DIGITAL LOGIC DESIGN	D	3
17JG1A0532	R1621053	DIGITAL LOGIC DESIGN	D	3
17JG1A0566	R1621054	PYTHON PROGRAMMING	F	0
17JG1A0566	R1621055	DATA STRUCTURES THROUGH C++	D	3
17JG1A0573	R1621053	DIGITAL LOGIC DESIGN	D	3
17JG1A0573	R1621054	PYTHON PROGRAMMING	F	0

Htno	Subcode	Subname	Grade	Credits
17JG1A0573	R1621055	DATA STRUCTURES THROUGH C++	C	3
17JG1A0573	R1621057	DATA STRUCTURES THROUGH C++ LAB	B	2
17JG1A0584	R1621051	STATISTICS WITH R PROGRAMMING	ABSENT	0
17JG1A0584	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	ABSENT	0
17JG1A0584	R1621053	DIGITAL LOGIC DESIGN	F	0
17JG1A0584	R1621054	PYTHON PROGRAMMING	F	0
17JG1A0584	R1621056	COMPUTER GRAPHICS	F	0
17JG1A0584	R1621057	DATA STRUCTURES THROUGH C++ LAB	A	2
17JG1A0595	R1621056	COMPUTER GRAPHICS	F	0
17JG1A05A3	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	F	0
17JG1A05A3	R1621053	DIGITAL LOGIC DESIGN	F	0
17JG1A05A3	R1621055	DATA STRUCTURES THROUGH C++	F	0
17JG1A05A6	R1621051	STATISTICS WITH R PROGRAMMING	ABSENT	0
17JG1A05A6	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	ABSENT	0
17JG1A05A6	R1621053	DIGITAL LOGIC DESIGN	ABSENT	0
17JG1A05A6	R1621054	PYTHON PROGRAMMING	ABSENT	0
17JG1A05A6	R1621055	DATA STRUCTURES THROUGH C++	ABSENT	0
17JG1A05A6	R1621056	COMPUTER GRAPHICS	ABSENT	0
17JG1A1211	R1621053	DIGITAL LOGIC DESIGN	D	3
17JG1A1211	R1621054	PYTHON PROGRAMMING	F	0
17JG1A1219	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	F	0
17JG1A1219	R1621053	DIGITAL LOGIC DESIGN	F	0
17JG1A1219	R1621054	PYTHON PROGRAMMING	F	0
17JG1A1230	R1621051	STATISTICS WITH R PROGRAMMING	ABSENT	0
17JG1A1238	R1621053	DIGITAL LOGIC DESIGN	F	0
17JG1A1246	R1621051	STATISTICS WITH R PROGRAMMING	ABSENT	0
17JG1A1246	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	ABSENT	0
17JG1A1246	R1621053	DIGITAL LOGIC DESIGN	ABSENT	0
17JG5A0201	R1621024	ELECTRO MAGNETIC FIELDS	F	0
17JG5A0201	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	F	0
18JG1A0201	R1621022	ELECTRICAL MACHINES-I	D	3
18JG1A0202	R1621021	ELECTRICAL CIRCUIT ANALYSIS - II	ABSENT	0
18JG1A0202	R1621022	ELECTRICAL MACHINES-I	ABSENT	0
18JG1A0202	R1621023	BASIC ELECTRONICS AND DEVICES	ABSENT	0
18JG1A0206	R1621024	ELECTRO MAGNETIC FIELDS	F	0
18JG1A0207	R1621021	ELECTRICAL CIRCUIT ANALYSIS - II	F	0
18JG1A0207	R1621022	ELECTRICAL MACHINES-I	F	0
18JG1A0207	R1621023	BASIC ELECTRONICS AND DEVICES	D	3
18JG1A0208	R1621022	ELECTRICAL MACHINES-I	F	0
18JG1A0210	R1621022	ELECTRICAL MACHINES-I	F	0
18JG1A0210	R1621023	BASIC ELECTRONICS AND DEVICES	D	3
18JG1A0210	R1621024	ELECTRO MAGNETIC FIELDS	F	0
18JG1A0212	R1621022	ELECTRICAL MACHINES-I	D	3
18JG1A0213	R1621022	ELECTRICAL MACHINES-I	F	0
18JG1A0213	R1621024	ELECTRO MAGNETIC FIELDS	F	0
18JG1A0213	R1621028	ELECTRICAL CIRCUITS LABORATORY	A	2
18JG1A0219	R1621022	ELECTRICAL MACHINES-I	D	3
18JG1A0220	R1621021	ELECTRICAL CIRCUIT ANALYSIS - II	C	3
18JG1A0220	R1621022	ELECTRICAL MACHINES-I	D	3
18JG1A0223	R1621021	ELECTRICAL CIRCUIT ANALYSIS - II	D	3
18JG1A0223	R1621022	ELECTRICAL MACHINES-I	F	0

Htno	Subcode	Subname	Grade	Credits
18JG1A0223	R1621023	BASIC ELECTRONICS AND DEVICES	D	3
18JG1A0223	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	D	3
18JG1A0225	R1621022	ELECTRICAL MACHINES-I	F	0
18JG1A0228	R1621021	ELECTRICAL CIRCUIT ANALYSIS - II	D	3
18JG1A0228	R1621022	ELECTRICAL MACHINES-I	C	3
18JG1A0401	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	C	3
18JG1A0402	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	C	3
18JG1A0404	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	C	3
18JG1A0406	R1621041	ELECTRONIC DEVICES AND CIRCUITS	F	0
18JG1A0406	R1621042	SWITCHING THEORY AND LOGIC DESIGN	D	3
18JG1A0406	R1621044	NETWORK ANALYSIS	C	3
18JG1A0406	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	D	3
18JG1A0412	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	B	3
18JG1A0417	R1621041	ELECTRONIC DEVICES AND CIRCUITS	F	0
18JG1A0417	R1621042	SWITCHING THEORY AND LOGIC DESIGN	F	0
18JG1A0417	R1621043	SIGNALS AND SYSTEMS	F	0
18JG1A0417	R1621044	NETWORK ANALYSIS	F	0
18JG1A0417	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	F	0
18JG1A0418	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	F	0
18JG1A0418	R1621042	SWITCHING THEORY AND LOGIC DESIGN	F	0
18JG1A0418	R1621043	SIGNALS AND SYSTEMS	F	0
18JG1A0418	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	F	0
18JG1A0419	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	F	0
18JG1A0419	R1621041	ELECTRONIC DEVICES AND CIRCUITS	F	0
18JG1A0419	R1621043	SIGNALS AND SYSTEMS	F	0
18JG1A0420	R1621042	SWITCHING THEORY AND LOGIC DESIGN	C	3
18JG1A0420	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	C	3
18JG1A0421	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	D	3
18JG1A0422	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	C	3
18JG1A0425	R1621042	SWITCHING THEORY AND LOGIC DESIGN	F	0
18JG1A0425	R1621043	SIGNALS AND SYSTEMS	F	0
18JG1A0425	R1621044	NETWORK ANALYSIS	D	3
18JG1A0425	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	D	3
18JG1A0426	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	D	3
18JG1A0429	R1621042	SWITCHING THEORY AND LOGIC DESIGN	B	3
18JG1A0433	R1621042	SWITCHING THEORY AND LOGIC DESIGN	F	0
18JG1A0433	R1621043	SIGNALS AND SYSTEMS	D	3
18JG1A0433	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	F	0
18JG1A0434	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	B	3
18JG1A0436	R1621042	SWITCHING THEORY AND LOGIC DESIGN	ABSENT	0
18JG1A0444	R1621042	SWITCHING THEORY AND LOGIC DESIGN	F	0
18JG1A0444	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	F	0
18JG1A0445	R1621042	SWITCHING THEORY AND LOGIC DESIGN	F	0
18JG1A0446	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	C	3
18JG1A0450	R1621041	ELECTRONIC DEVICES AND CIRCUITS	F	0
18JG1A0450	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	F	0
18JG1A0452	R1621041	ELECTRONIC DEVICES AND CIRCUITS	F	0
18JG1A0452	R1621042	SWITCHING THEORY AND LOGIC DESIGN	F	0
18JG1A0453	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	C	3
18JG1A0455	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	F	0
18JG1A0457	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	F	0

Htno	Subcode	Subname	Grade	Credits
18JG1A0462	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	ABSENT	0
18JG1A0462	R1621041	ELECTRONIC DEVICES AND CIRCUITS	F	0
18JG1A0462	R1621042	SWITCHING THEORY AND LOGIC DESIGN	ABSENT	0
18JG1A0462	R1621043	SIGNALS AND SYSTEMS	ABSENT	0
18JG1A0462	R1621044	NETWORK ANALYSIS	D	3
18JG1A0462	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	F	0
18JG1A0463	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	C	3
18JG1A0468	R1621043	SIGNALS AND SYSTEMS	D	3
18JG1A0468	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	D	3
18JG1A0472	R1621042	SWITCHING THEORY AND LOGIC DESIGN	F	0
18JG1A0475	R1621041	ELECTRONIC DEVICES AND CIRCUITS	F	0
18JG1A0475	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	F	0
18JG1A0478	R1621041	ELECTRONIC DEVICES AND CIRCUITS	F	0
18JG1A0478	R1621042	SWITCHING THEORY AND LOGIC DESIGN	F	0
18JG1A0478	R1621043	SIGNALS AND SYSTEMS	F	0
18JG1A0478	R1621044	NETWORK ANALYSIS	F	0
18JG1A0478	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	F	0
18JG1A0479	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	D	3
18JG1A0479	R1621041	ELECTRONIC DEVICES AND CIRCUITS	F	0
18JG1A0479	R1621042	SWITCHING THEORY AND LOGIC DESIGN	D	3
18JG1A0479	R1621043	SIGNALS AND SYSTEMS	F	0
18JG1A0479	R1621044	NETWORK ANALYSIS	D	3
18JG1A0479	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	D	3
18JG1A0485	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	ABSENT	0
18JG1A0488	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	D	3
18JG1A0489	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	F	0
18JG1A0497	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	C	3
18JG1A04A4	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	D	3
18JG1A04A5	R1621042	SWITCHING THEORY AND LOGIC DESIGN	D	3
18JG1A04A5	R1621043	SIGNALS AND SYSTEMS	F	0
18JG1A04A5	R1621044	NETWORK ANALYSIS	F	0
18JG1A04A5	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	F	0
18JG1A04A7	R1621043	SIGNALS AND SYSTEMS	F	0
18JG1A04A7	R1621044	NETWORK ANALYSIS	F	0
18JG1A04A7	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	F	0
18JG1A04A8	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	C	3
18JG1A0504	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	C	3
18JG1A0504	R1621055	DATA STRUCTURES THROUGH C++	C	3
18JG1A0505	R1621056	COMPUTER GRAPHICS	F	0
18JG1A0508	R1621056	COMPUTER GRAPHICS	F	0
18JG1A0509	R1621056	COMPUTER GRAPHICS	F	0
18JG1A0526	R1621056	COMPUTER GRAPHICS	F	0
18JG1A0532	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	C	3
18JG1A0535	R1621051	STATISTICS WITH R PROGRAMMING	F	0
18JG1A0535	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	F	0
18JG1A0535	R1621053	DIGITAL LOGIC DESIGN	D	3
18JG1A0535	R1621054	PYTHON PROGRAMMING	F	0
18JG1A0535	R1621055	DATA STRUCTURES THROUGH C++	F	0
18JG1A0535	R1621056	COMPUTER GRAPHICS	F	0
18JG1A0536	R1621051	STATISTICS WITH R PROGRAMMING	F	0
18JG1A0536	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	D	3

Htno	Subcode	Subname	Grade	Credits
18JG1A0540	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	ABSENT	0
18JG1A0540	R1621056	COMPUTER GRAPHICS	F	0
18JG1A0542	R1621051	STATISTICS WITH R PROGRAMMING	F	0
18JG1A0542	R1621055	DATA STRUCTURES THROUGH C++	D	3
18JG1A0544	R1621051	STATISTICS WITH R PROGRAMMING	F	0
18JG1A0546	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	C	3
18JG1A0548	R1621051	STATISTICS WITH R PROGRAMMING	F	0
18JG1A0548	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	F	0
18JG1A0550	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	F	0
18JG1A0556	R1621051	STATISTICS WITH R PROGRAMMING	C	3
18JG1A0575	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	C	3
18JG1A0575	R1621056	COMPUTER GRAPHICS	F	0
18JG1A1204	R1621051	STATISTICS WITH R PROGRAMMING	F	0
18JG1A1204	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	F	0
18JG1A1205	R1621051	STATISTICS WITH R PROGRAMMING	C	3
18JG1A1209	R1621051	STATISTICS WITH R PROGRAMMING	D	3
18JG1A1212	R1621051	STATISTICS WITH R PROGRAMMING	F	0
18JG1A1212	R1621053	DIGITAL LOGIC DESIGN	ABSENT	0
18JG1A1212	R1621054	PYTHON PROGRAMMING	F	0
18JG1A1228	R1621051	STATISTICS WITH R PROGRAMMING	C	3
18JG1A1234	R1621121	SOFTWARE ENGINEERING	D	3
18JG1A1237	R1621053	DIGITAL LOGIC DESIGN	A	3
18JG1A1246	R1621051	STATISTICS WITH R PROGRAMMING	C	3
18JG5A0204	R1621024	ELECTRO MAGNETIC FIELDS	D	3
18JG5A0404	R1621043	SIGNALS AND SYSTEMS	F	0
18JG5A0404	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	F	0
18JG5A0407	R1621043	SIGNALS AND SYSTEMS	F	0
18JG5A0407	R1621044	NETWORK ANALYSIS	D	3
18JG5A0407	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	D	3
18JG5A0502	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	F	0
18JG5A0502	R1621056	COMPUTER GRAPHICS	ABSENT	0
19JG5A0204	R1621022	ELECTRICAL MACHINES-I	D	3
19JG5A0205	R1621021	ELECTRICAL CIRCUIT ANALYSIS - II	C	3
19JG5A0206	R1621021	ELECTRICAL CIRCUIT ANALYSIS - II	F	0
19JG5A0208	R1621021	ELECTRICAL CIRCUIT ANALYSIS - II	C	3
19JG5A0208	R1621022	ELECTRICAL MACHINES-I	D	3
19JG5A0209	R1621021	ELECTRICAL CIRCUIT ANALYSIS - II	F	0
19JG5A0210	R1621021	ELECTRICAL CIRCUIT ANALYSIS - II	D	3
19JG5A0211	R1621022	ELECTRICAL MACHINES-I	F	0
19JG5A0214	R1621022	ELECTRICAL MACHINES-I	D	3
19JG5A0214	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	F	0
19JG5A0215	R1621022	ELECTRICAL MACHINES-I	A	3
19JG5A0216	R1621021	ELECTRICAL CIRCUIT ANALYSIS - II	D	3
19JG5A0401	R1621042	SWITCHING THEORY AND LOGIC DESIGN	D	3
19JG5A0402	R1621042	SWITCHING THEORY AND LOGIC DESIGN	D	3
19JG5A0402	R1621043	SIGNALS AND SYSTEMS	F	0
19JG5A0402	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	D	3
19JG5A0405	R1621042	SWITCHING THEORY AND LOGIC DESIGN	D	3
19JG5A0405	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	F	0
19JG5A0406	R1621041	ELECTRONIC DEVICES AND CIRCUITS	F	0
19JG5A0406	R1621043	SIGNALS AND SYSTEMS	F	0

Htno	Subcode	Subname	Grade	Credits
19JG5A0406	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	F	0
19JG5A0408	R1621042	SWITCHING THEORY AND LOGIC DESIGN	C	3
19JG5A0408	R1621043	SIGNALS AND SYSTEMS	F	0
19JG5A0410	R1621042	SWITCHING THEORY AND LOGIC DESIGN	F	0
19JG5A0410	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	D	3
19JG5A0412	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI	D	3
19JG5A0412	R1621043	SIGNALS AND SYSTEMS	F	0
19JG5A0412	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS	D	3

**\*\*Note:1)[Last Date to apply for Recounting/Revaluation/Challenge Revaluation is : 06-01-2021 ]**

**\*\* Note:\*\***

\* -1 in the filed of externals indicates student is absent for the respective subject.

\* -2 in the filed of externals indicates student result Withheld for the respective subject.

\* -3 in the filed of externals indicates student involved in Malpractice for the respective subject.



Date:31.12.2020

Controller of Examinations