

A Five Day online FDP on
APPLICATIONS OF OPTIMIZATION
TECHNIQUES TO ELECTRICAL
ENGINEERING

22nd – 26th June 2020

Time: 11:00AM to 12:30 PM

Organized by
Department of Electrical and Electronics Engineering



Gayatri Vidya Parishad
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ABOUT THE INSTITUTION AND DEPARTMENT

Gayatri Vidya Parishad College of Engineering for Women is established under the aegis of Gayatri Vidya Parishad, an educational society founded by eminent academicians, industrialists, and Philanthropists of Visakhapatnam. To empower women in technological and engineering fields, the society has established this engineering college exclusively for women in the year 2008. It is a self-financed institution offering excellence in engineering education and is affiliated to Jawaharlal Nehru Technological University, Kakinada (JNTUK), approved by AICTE, New Delhi. The College is offering UG courses in ECE, EEE, CSE, IT and a PG Course in ECE. The UG programs of ECE, CSE and IT are accredited by NBA.

The Department of Electrical and Electronics Engineering was established in 2011 and offers B.Tech Program in EEE. The Department is well established with experienced faculty having multiple years of experience and involved in research activities in the areas of power systems, renewable energy systems, control systems and advanced power electronic drives. The department has state-of-the-art laboratories with advanced software and hardware to facilitate research in some areas of EEE. The Department flourishes in its enthusiastic and energetic environment comprising of the major fields of Electrical, Electronics and Elements of Computer Science.

ABOUT THE TOPIC

Optimization techniques find their applications in all streams of engineering and sciences. These seek to find variable values that optimize a multivariate objective function under a set of constraints. This situation of finding the value of a variable is very often encountered in every field of engineering and science. With emergence of metaheuristics research, many different approaches have been developed and are in wide use for problems hitherto unsolvable.

OBJECTIVE OF THE WORKSHOP:

There exists numerous optimization techniques and their applications to electrical engineering are also increasing in the recent years. However, most of the researchers working on optimization problem understand the techniques, sometime lack in applying them to the fields of engineering. These days the optimization techniques are being applied in all major areas of electrical engineering.

The FDP aims to introduce the optimization technique and application to various research problems in the field of electrical engineering. The topic is of utmost important for the research scholars working in this area and also lay the foundation for those are yet to start with the research.

To understand and apply the optimization techniques to electrical engineering problems a five day online FDP on "Application of Optimization Techniques to Electrical Engineering" is being organized by the Department of Electrical and Electronics Engineering, Gayatri Vidya Parishad College of Engineering for Women, Visakhapatnam.

RESOURCE PERSONS

Date	Resource Person
22/06/2020	Dr Shankar Pedapati, Asst. Prof., Department of EE, NIT AP, AP
23/06/2020	Dr Siddarth Panda, Professor, Department of EE, VSSUT Burla, Odisha
24/06/2020	Dr I Satish Kumar Asst. Prof., Department of EE, NIT Warangal, Telangana
25/06/2020	Dr Subhojit Ghosh, Assoc Prof., Department of EE, NIT Raipur, CG
26/06/2020	Dr T V Dixit, Principal and Head, Department of EE, Government Polytechnic College, Berla

WHO CAN PARTICIPATE?

All PG/Research Scholars and faculty from AICTE approved engineering colleges can participate.

E-certificates will be sent to the mail address of the participants who attend.

REGISTRATION:

Each Participant can register to the FDP by paying an amount of Rs. 100/- through Google Pay/Phone Pay to 9490696564

Click on the below link for registration form.

<https://forms.gle/wnrmCgo3YC5rud1F7>

All the registered participants will be sent the meeting link one day prior.

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Sri D.Dakshina Murthy, *Vice-President, Gayatri Vidya Parishad*

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