



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY KAKINADA

Results for IV B.Tech I semester (R16) Adv. Supplementary Examinations FEB 2020

College name: GAYATHRI VIDYA PARISHAD COLL OF ENGG FOR WOMEN, VISAKHAPATNA:JG

Htno	Subcode	Subname	Grade	Credits
16JG1A0203	R1641022	LINEAR IC APPLICATION	F	0
16JG1A0203	R1641023	POWER SYSTEM OPERATION & CONTROL	F	0
16JG1A0203	R1641024	SWITCHGEAR AND PROTECTION	F	0
16JG1A0205	R1641022	LINEAR IC APPLICATION	F	0
16JG1A0205	R164102D	INSTRUMENTATION	D	3
16JG1A0208	R1641022	LINEAR IC APPLICATION	F	0
16JG1A0208	R1641024	SWITCHGEAR AND PROTECTION	D	3
16JG1A0209	R1641024	SWITCHGEAR AND PROTECTION	C	3
16JG1A0209	R164102G	SPECIAL ELECTRICAL MACHINES	C	3
16JG1A0220	R1641022	LINEAR IC APPLICATION	F	0
16JG1A0220	R164102G	SPECIAL ELECTRICAL MACHINES	D	3
16JG1A0221	R1641021	UTILIZATION OF ELECTRICAL ENERGY	C	3
16JG1A0221	R1641022	LINEAR IC APPLICATION	F	0
16JG1A0221	R1641024	SWITCHGEAR AND PROTECTION	D	3
16JG1A0223	R164102G	SPECIAL ELECTRICAL MACHINES	C	3
16JG1A0224	R1641022	LINEAR IC APPLICATION	F	0
16JG1A0224	R1641024	SWITCHGEAR AND PROTECTION	F	0
16JG1A0224	R1641028	POWER SYSTEMS & SIMULATION LABORATORY	C	2
16JG1A0224	R164102D	INSTRUMENTATION	F	0
16JG1A0224	R164102G	SPECIAL ELECTRICAL MACHINES	D	3
16JG1A0232	R1641022	LINEAR IC APPLICATION	F	0
16JG1A0232	R1641023	POWER SYSTEM OPERATION & CONTROL	ABSENT	0
16JG1A0232	R1641028	POWER SYSTEMS & SIMULATION LABORATORY	F	0
16JG1A0232	R164102D	INSTRUMENTATION	F	0
16JG1A0402	R164104C	SYSTEM DESIGN THROUGH VERILOG	F	0
16JG1A0412	R164104C	SYSTEM DESIGN THROUGH VERILOG	F	0
16JG1A0415	R1641042	DIGITAL IMAGE PROCESSING(COMMON TO ECE ,	C	3
16JG1A0415	R164104C	SYSTEM DESIGN THROUGH VERILOG	F	0
16JG1A0416	R164104C	SYSTEM DESIGN THROUGH VERILOG	F	0
16JG1A0416	R164104D	EMBEDDED SYSTEMS	F	0
16JG1A0427	R164104D	EMBEDDED SYSTEMS	F	0
16JG1A0429	R164104C	SYSTEM DESIGN THROUGH VERILOG	F	0
16JG1A0441	R164104C	SYSTEM DESIGN THROUGH VERILOG	F	0
16JG1A0442	R1641042	DIGITAL IMAGE PROCESSING(COMMON TO ECE ,	C	3
16JG1A0442	R164104C	SYSTEM DESIGN THROUGH VERILOG	D	3
16JG1A0444	R164104C	SYSTEM DESIGN THROUGH VERILOG	F	0
16JG1A0450	R164104C	SYSTEM DESIGN THROUGH VERILOG	C	3
16JG1A0452	R164104C	SYSTEM DESIGN THROUGH VERILOG	F	0
16JG1A0452	R164104D	EMBEDDED SYSTEMS	F	0
16JG1A0459	R164104C	SYSTEM DESIGN THROUGH VERILOG	D	3
16JG1A0469	R1641042	DIGITAL IMAGE PROCESSING(COMMON TO ECE ,	F	0
16JG1A0477	R1641042	DIGITAL IMAGE PROCESSING(COMMON TO ECE ,	F	0
16JG1A0477	R1641043	COMPUTER NETWORKS(COMMON TO ECE & EIE)	F	0
16JG1A0477	R164104C	SYSTEM DESIGN THROUGH VERILOG	F	0
16JG1A0477	R164104D	EMBEDDED SYSTEMS	ABSENT	0

Htno	Subcode	Subname	Grade	Credits
16JG1A0479	R164104C	SYSTEM DESIGN THROUGH VERILOG	D	3
16JG1A0484	R1641041	RADAR SYSTEMS	ABSENT	0
16JG1A0484	R164104C	SYSTEM DESIGN THROUGH VERILOG	F	0
16JG1A0485	R1641041	RADAR SYSTEMS	D	3
16JG1A0487	R164104C	SYSTEM DESIGN THROUGH VERILOG	F	0
16JG1A0492	R1641044	OPTICAL COMMUNICATIONS	F	0
16JG1A0492	R164104C	SYSTEM DESIGN THROUGH VERILOG	F	0
16JG1A0492	R164104D	EMBEDDED SYSTEMS	F	0
16JG1A0497	R1641042	DIGITAL IMAGE PROCESSING(COMMON TO ECE ,	C	3
16JG1A04A8	R164104C	SYSTEM DESIGN THROUGH VERILOG	F	0
16JG1A0513	R1641054	MANAGERIAL ECONOMICS AND FINANCIAL ANALY	C	3
16JG1A0513	R164105A	BIG DATA ANALYSIS(COMMON TO CSE,IT)	B	3
16JG1A0513	R164105D	CLOUD COMPUTING(COMMON TO CSE,IT)	C	3
16JG1A0517	R1641053	WEB TECHNOLOGIES	F	0
16JG1A0518	R1641054	MANAGERIAL ECONOMICS AND FINANCIAL ANALY	D	3
16JG1A0530	R1641054	MANAGERIAL ECONOMICS AND FINANCIAL ANALY	F	0
16JG1A0530	R164105D	CLOUD COMPUTING(COMMON TO CSE,IT)	D	3
16JG1A0533	R164105D	CLOUD COMPUTING(COMMON TO CSE,IT)	C	3
16JG1A0539	R1641052	SOFTWARE ARCHITECTURE & DEISGN PATTERNS	D	3
16JG1A0539	R1641054	MANAGERIAL ECONOMICS AND FINANCIAL ANALY	D	3
16JG1A0539	R164105D	CLOUD COMPUTING(COMMON TO CSE,IT)	D	3
16JG1A0542	R164105D	CLOUD COMPUTING(COMMON TO CSE,IT)	F	0
16JG1A0549	R1641053	WEB TECHNOLOGIES	F	0
16JG1A0552	R164105D	CLOUD COMPUTING(COMMON TO CSE,IT)	F	0
16JG1A0553	R1641051	CRYPTOGRAPHY AND NETWORK SECURITY (COMM	C	3
16JG1A0553	R1641053	WEB TECHNOLOGIES	D	3
16JG1A0556	R164105A	BIG DATA ANALYSIS(COMMON TO CSE,IT)	C	3
16JG1A0561	R1641051	CRYPTOGRAPHY AND NETWORK SECURITY (COMM	D	3
16JG1A0564	R1641052	SOFTWARE ARCHITECTURE & DEISGN PATTERNS	C	3
16JG1A0569	R1641051	CRYPTOGRAPHY AND NETWORK SECURITY (COMM	D	3
16JG1A0569	R1641053	WEB TECHNOLOGIES	F	0
16JG1A0569	R1641054	MANAGERIAL ECONOMICS AND FINANCIAL ANALY	F	0
16JG1A0569	R164105A	BIG DATA ANALYSIS(COMMON TO CSE,IT)	D	3
16JG1A05A8	R164105D	CLOUD COMPUTING(COMMON TO CSE,IT)	C	3
16JG1A1203	R1641054	MANAGERIAL ECONOMICS AND FINANCIAL ANALY	F	0
16JG1A1203	R1641127	MOBILE COMPUTING LAB	C	2
16JG1A1218	R164105C	MOBILE COMPUTING (COMMON TO CSE & IT)	F	0
16JG1A1226	R164105C	MOBILE COMPUTING (COMMON TO CSE & IT)	F	0
16JG1A1226	R164105D	CLOUD COMPUTING(COMMON TO CSE,IT)	C	3
16JG1A1228	R1641054	MANAGERIAL ECONOMICS AND FINANCIAL ANALY	D	3
16JG1A1231	R164105C	MOBILE COMPUTING (COMMON TO CSE & IT)	F	0
16JG1A1231	R164105D	CLOUD COMPUTING(COMMON TO CSE,IT)	F	0
16JG1A1250	R164105A	BIG DATA ANALYSIS(COMMON TO CSE,IT)	C	3
16JG1A1256	R164105C	MOBILE COMPUTING (COMMON TO CSE & IT)	F	0
17JG5A0201	R1641023	POWER SYSTEM OPERATION & CONTROL	F	0
17JG5A0204	R1641028	POWER SYSTEMS & SIMULATION LABORATORY	F	0
17JG5A0214	R1641023	POWER SYSTEM OPERATION & CONTROL	F	0
17JG5A0501	R1641052	SOFTWARE ARCHITECTURE & DEISGN PATTERNS	C	3

**Note:1)[Last Date to apply for Recounting/Revaluation/Challenge Revaluation is : 30-06-2020]

**** Note:****

* -1 in the filed of externals indicates student is absent for the respective subject.

* -2 in the filed of externals indicates student result Withheld for the respective subject.

* -3 in the filed of externals indicates student involved in Malpractice for the respective subject.

A handwritten signature in black ink, appearing to read "Robert A. Kelly". The signature is written in a cursive style with a prominent initial 'R'.

Date:23.06.2020

Controller of Examinations